

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select 2. CONTROLLER
- From CONTROLLER Submenu select 2. VEHICLE OVERLAPS

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OVERLAP A
Select TMG VEH OVLP [A] and 'PPLT FYA'
TMG VEH OVLP...[A] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 1
OPPOSING THROUGH..... PHASE 2

FLASHING ARROW OUTPUT....CH9 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 1
    
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← NOTICE SF BIT DISABLE 1

Toggle Twice

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OVERLAP C
Select TMG VEH OVLP [C] and 'PPLT FYA'
TMG VEH OVLP...[C] TYPE: ....PPLT FYA
PROTECTED LEFT TURN.... PHASE 5
OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 5
    
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← NOTICE SF BIT DISABLE 5

END PROGRAMMING

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	1, 5

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1,5: Modifies overlap parent phases for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2: Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.

Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

- From Main Menu select 5. TIME BASE
- From TIME BASE Submenu select 2. ACTION PLAN

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ACTION PLAN...[ 1]
PATTERN.....AUTO  SYS OVERRIDE.... NO
TIMING PLAN..... 0  SEQUENCE..... 0
VEH DETECTOR PLAN.. 2  DET LOG.....NONE
FLASH..... --  RED REST..... NO
VEH DET DIAG PLN... 0  PED DET DIAG PLN..0
DIMMING ENABLE.. NO  PRIORITY RETURN. NO
PED PR RETURN.. NO  QUEUE DELAY..... NO
PMT COND DELAY  NO

PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
PED RCL . . . . .
WALK 2 . . . . .
VEX 2 . . . . .
VEH RCL . . . . .
MAX RCL . . . . .
MAX 2 . . . . .
PHASE 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
MAX 3 . . . . .
CS 1NH . . . . .
OMIT . . . . .
SPC FCT X . . . X . . . (1-8)
AUX FCT . . . (1-3)

LP 1-15 . . . . .
LP 16-30 . . . . .
LP 31-45 . . . . .
LP 46-60 . . . . .
LP 61-75 . . . . .
LP 76-90 . . . . .
LP 91-100 . . . . .
    
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ECONOLITE ASC/3-2070 PED 3 PROGRAMMING ASSIGNMENT DETAIL

(program controller as shown)

- From Main Menu select 6. DETECTORS
- From DETECTOR Submenu select 3. PED DETECTOR INPUT ASSIGNMENT

PED DET PHASE ASSIGNMENT MODE: NTCIP																	
PHASE	1	2	3	4	5	6	7	8	PHASE	9	10	11	12	13	14	15	16
DETECTOR	0	2	8	0	0	6	0	0	DETECTOR	0	0	0	0	0	0	0	0

← NOTICE PED DETECTOR 8 ASSIGNED TO PHASE 3

- From Main Menu select 1. CONFIGURATION
- From CONFIGURATION Submenu select 3. LOAD SW ASSIGN

LD SWITCH ASSIGN											
PHASE	DIMMING	---FLASH---	/OVLP	TYPE	R	Y	G	D	PWR	AUT	TGR
1	1	V	A	R	X	
2	2	V	A	Y	.	
3	3	V	A	R	X	
4	4	V	A	R	.	
5	5	V	A	R	.	
6	6	V	A	Y	X	
7	7	V	A	R	.	
8	8	V	A	R	X	
9	1	O	A	R	X	
10	2	O	A	R	X	
11	3	O	A	R	.	
12	4	O	A	R	.	
13	2	P	A	.	.	
14	4	P	A	.	.	
15	6	P	A	.	.	
16	3	P	A	.	.	

← NOTICE PHASE 3 PED ASSIGNED TO LD SWITCH 16

Temporary Design 1 - TMP Phase I
Electrical Detail - Sheet 2 of 3

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0224T1
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A

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	ELECTRICAL AND PROGRAMMING DETAILS FOR: PLAN DATE: March 2018 PREPARED BY: G B Spell REVISIONS:	REVIEWED BY: L Overn DATE: 3/29/2018	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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User: rfmuncy