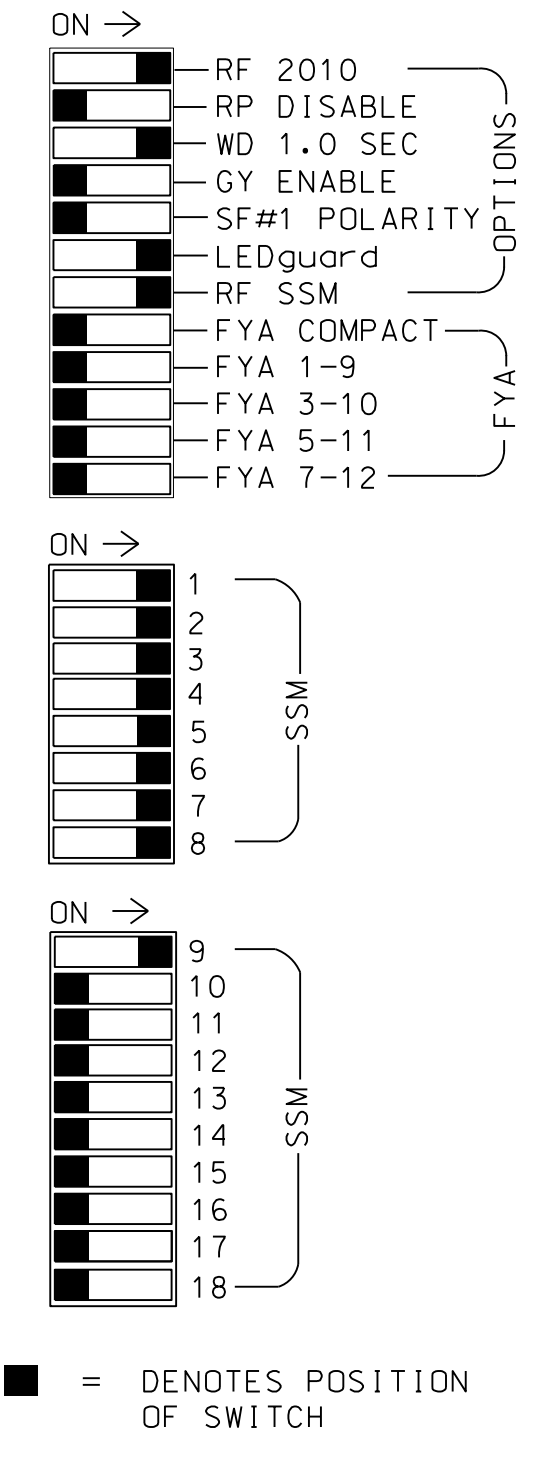
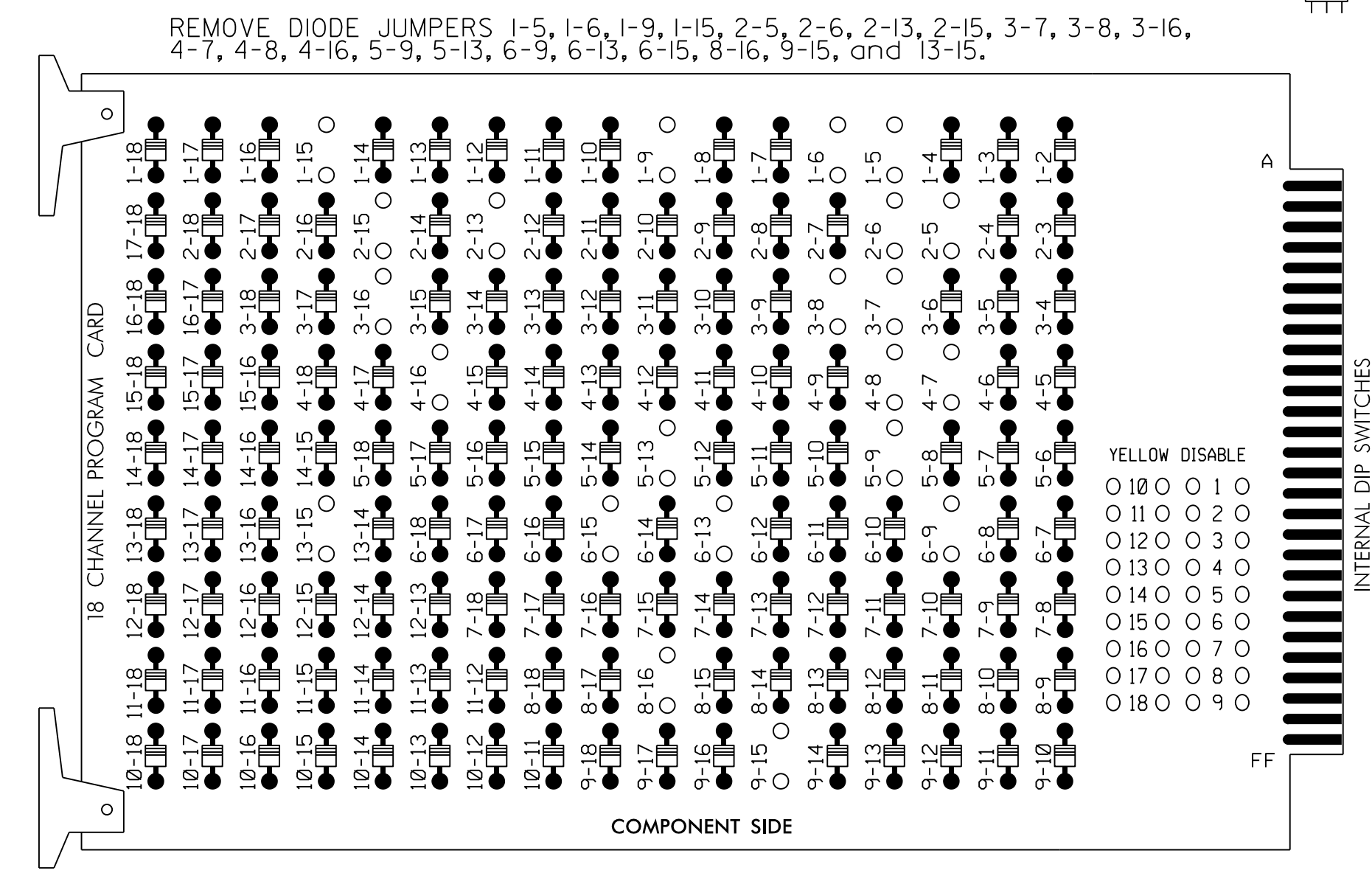


### EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program controller to start up in Phase 2 Walk and Phase 6 Walk.
- The cabinet and controller are part of the Fayetteville Signal System.

### EQUIPMENT INFORMATION

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE ASC/3-2070  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S7,S8,S9,  
 S10,S11,S12,AUX S1  
 PHASES USED.....1,2,2PED,3,4,5,6,6PED,7,  
 8,8PED  
 OVERLAP A.....5  
 OVERLAP B.....NOT USED  
 OVERLAP C.....NOT USED  
 OVERLAP D.....NOT USED

### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CHU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11,12	21,22	P21, P22	31,32	41,42	NU	51,52	61,62	P61, P62	71	81,82	P81, P82	82	NU	NU	NU	NU	NU
RED		128			101			134			107		*					
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW	125				116			131			122							
YELLOW ARROW	126				117			132			123		A122					
GREEN ARROW	127				118			133			124		A123					
Hand icon					113						119		110					
Person icon					115						121		112					

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

### INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2	∅ 2	∅ 3	∅ 3	∅ 4	∅ 4	S	S	S	∅ 2 PED	∅ 6 PED	FS
L	1A	1C	2A	2C	3A	3B	4A	4C	-OR-	-OR-	-OR-	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR
U	∅ 1	NOT USED	∅ 2	NOT USED	NOT USED	NOT USED	∅ 4	NOT USED	∅ 5	∅ 6	∅ 6	∅ 7	∅ 8	RR1
L	1B	USED	2B	USED	3B	4B	NOT USED	NOT USED	5A	6A	6C	7A	8A	AC ISOLATOR
U	∅ 5	∅ 6	∅ 6	-OR-	∅ 7	∅ 8	-OR-	-OR-	-OR-	-OR-	-OR-	-OR-	-OR-	RR2
L	5A	6A	6C	NOT USED	7A	8A	NOT USED	NOT USED	5B	6B	NOT USED	7B	8B	AC ISOLATOR

EX. : 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME  
 RR1,RR2 = RAILROAD PREEMPTS

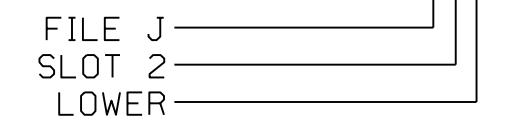
NOTE: The RR1 and RR2 preempt inputs have been remapped as detector inputs for use by the Logic Processor. See sheet 5 for details.

### INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND TIME	DELAY TIME	ADDED INITIAL	DETECTOR TYPE
1A	TB2-1,2	I1U	56	1	1	YES				S
1B	TB2-3,4	I1L	56	1	1	YES				S
1C	TB2-5,6	I2U	39	2	1	YES		15		S
2A	TB2-9,10	I3U	63	32	2	YES				S
2B	TB2-11,12	I3L	76	42	2	YES				S
2C	TB4-1,2	I4U	47	22	2	YES				S
3A	TB4-5,6	I5U	58	3	3	YES		3		S
3B	TB4-9,10	I6U	41	4	3	YES				S
4A	TB6-1,2	I7U	65	34	4	YES				S
4B	TB6-3,4	I7L	78	44	4	YES		10		S
4C	TB6-5,6	I8U	49	24	4	YES		15		S
5A	TB3-1,2	J1U	55	5	5	YES				S
5B	TB3-3,4	J1L	55	5	5	YES				S
6A	TB3-5,6	J2U	40	6	6	YES				S
6B	TB3-7,8	J2L	44	16	6	YES				S
6C	TB3-9,10	J3U	64	36	6	YES				S
7A	TB5-5,6	J5U	57	7	7	YES		3		S
8A	TB5-9,10	J6U	42	8	8	YES				S
8B	TB5-11,12	J6L	46	18	8	YES				S
PED PUSH BUTTONS										
P21,P22	TB8-4,6	I12U	67	PED 2	2 PED					
P61,P62	TB8-7,9	I13U	68	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	PED 8	8 PED					

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.

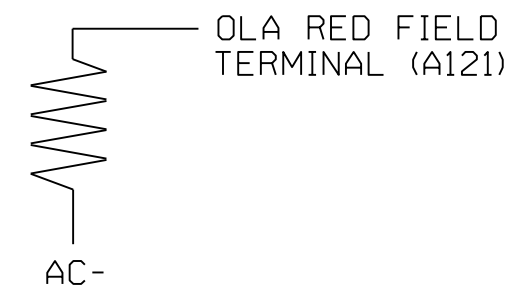
### INPUT FILE POSITION LEGEND: J2L



### LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0054  
 DESIGNED: March 2018  
 SEALED: 03-29-2018  
 REVISED: N/A

Final Design  
 Electrical Detail - Sheet 1 of 6

US 401 Business (Raeford Road)  
 at  
 McPherson Church Road/  
 Owen Drive  
 Division 6 Cumberland County Fayetteville

PLAN DATE: March 2018 REVIEWED BY: L Overn  
 PREPARED BY: G B Spell REVIEWED BY:

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

3/29/2018  
 DATE  
 SIG. INVENTORY NO. 06-0054