

ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP PROGRAMMING DETAIL FOR ALTERNATE PHASING LOOP 5A

(program controller as shown)

IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

- From Main Menu select **8. UTILITIES**
- From UTILITIES Submenu select **1. COPY/CLEAR**
- Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

```

COPY / CLEAR UTILITY
FROM          TO
PHASE TIMING... . > PHASE TIMING... .
TIMING PLAN.... . > TIMING PLAN.... .
PH DET OPT PLAN. . > PH DET OPT PLAN. .
DETECTOR PLAN... 1 > DETECTOR PLAN... 2
TOGGLE TO SELECT A "FROM" AND A "TO"
THEN PRESS ENTER
  
```

- From Main Menu select **6. DETECTORS**
- From DETECTOR Submenu select **2. VEHICLE DETECTOR SETUP**
- Place cursor in VEH DET PLAN [] position and enter "2".

- Place cursor in VEH DETECTOR [] position and enter "5".
- Set delay time to "0".

```

VEH DETECTOR [ 5]  VEH DET PLAN [ 2]
TYPE: S-STANDARD
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
      5 5 . . . . .
EXTEND TIME... 0.0 DELAY TIME... 0.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
  
```

← NOTICE VEH DET PLAN 2

← ENSURE DELAY IS SET TO '0'

- Place cursor in VEH DETECTOR [] position and enter "22".
- Set assigned phase to "0".

```

VEH DETECTOR [22]  VEH DET PLAN [ 2]
TYPE: G-GREEN EXTENSION/DELAY
TS2 DETECTOR..... ECPI LOG..... NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
      22 0 . . . . .
EXTEND TIME... 0.0 DELAY TIME... 3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN..... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
  
```

← NOTICE VEH DET PLAN 2

→ ENSURE PHASE IS SET TO "0"

END PROGRAMMING

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- From Main Menu select **2. CONTROLLER**
- From CONTROLLER Submenu select **2. VEHICLE OVERLAPS**

TOGGLE TWO TIMES
OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```

TMG VEH OVLP...[C] TYPE: ....[PPLT FYA]
PROTECTED LEFT TURN.... PHASE 5
OPPOSING THROUGH..... PHASE 6

FLASHING ARROW OUTPUT.....CH11 ISOLATE
DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE..... 5
  
```

← NOTICE ACTION PLAN SF BIT "5"

END PROGRAMMING

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0334T2
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A

Temporary Design 2 - TMP Phase II
Electrical Detail - Sheet 2 of 3

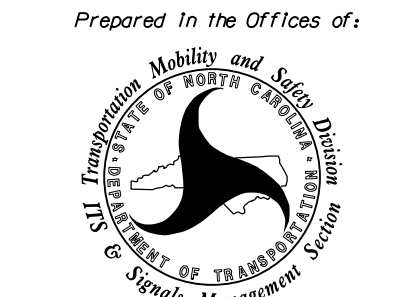
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UNLESS ALL SIGNATURES COMPLETED



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ELECTRICAL AND PROGRAMMING
DETAILS FOR:

Prepared in the Offices of:



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US 401 Business (Raeford Road)
at
Montclair Drive

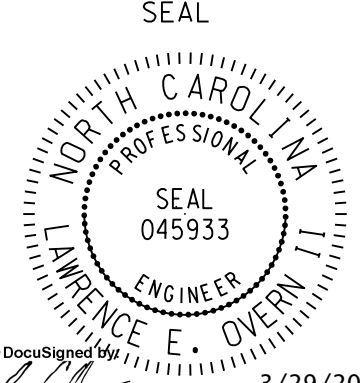
Division 6 Cumberland County Fayetteville

PLAN DATE: March 2018 REVIEWED BY: L Overn

PREPARED BY: R W Muncey REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL



3/29/2018

DATE

SIG. INVENTORY NO. 06-0334T2

DATE: 03/29/2018 10:45:12 AM
USER: rfmuncy