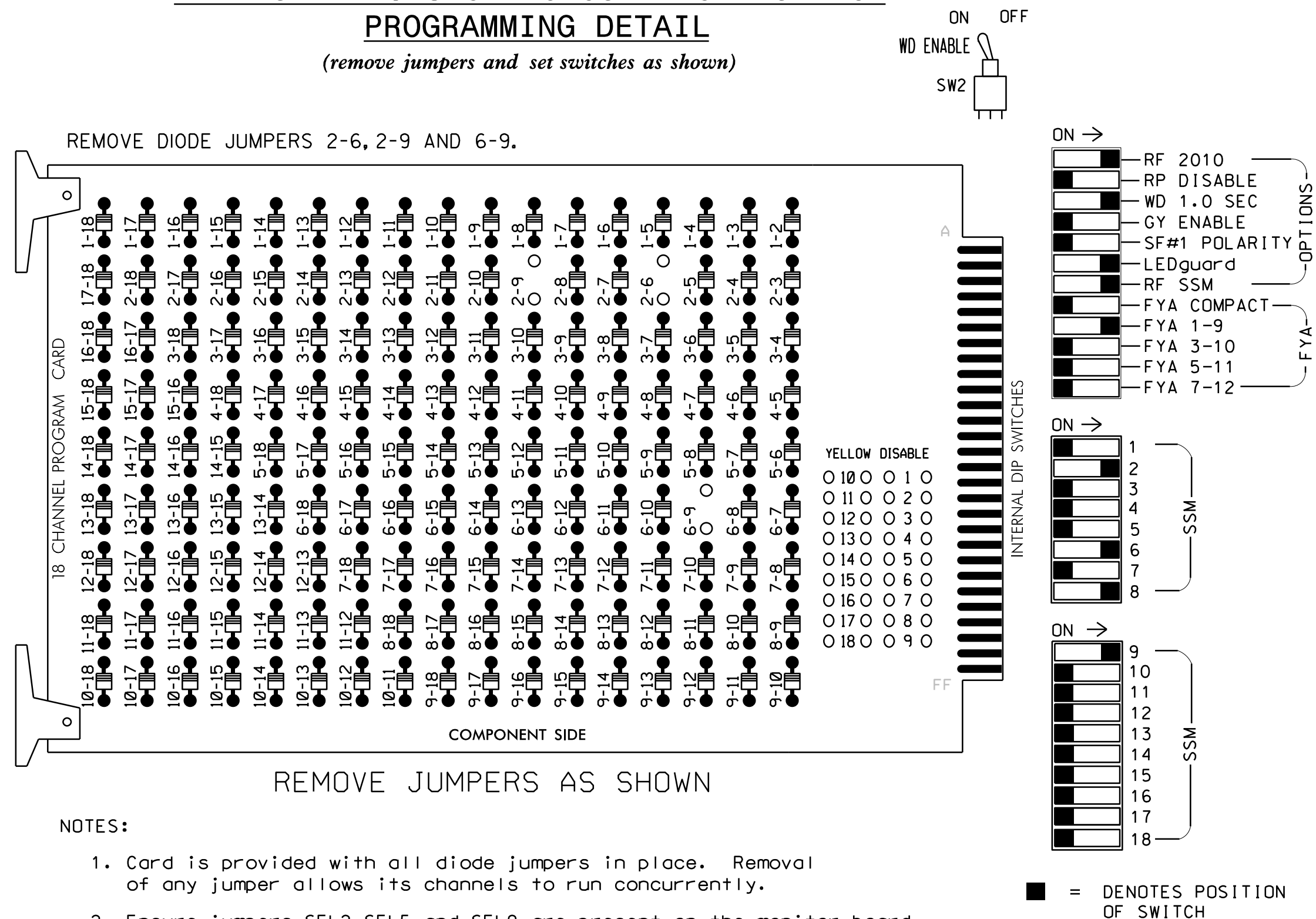


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash and overlaps 1 as Wag Overlaps.
- If this signal will be managed by an ATMS software, enable controller and detector logging for all enabled detectors.
- The cabinet and controller are part of the NC 42 (East of Clayton) Closed Loop System, Signal System #10411.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	21,22	NU	NU	NU	NU	NU	62,63	NU	NU	81,82	NU	61	NU	NU	NU	NU	NU	
RED		128						134			107								
YELLOW		129						135			108								
GREEN		130						136			109								
RED ARROW																		A121	
YELLOW ARROW																			A122
FLASHING YELLOW ARROW																			A123
GREEN ARROW																			

NU = Not Used
 ★ See pictorial of head wiring in detail below.
 NOTE: Signalheads 31, 41, 42 and 51 have been bagged and disconnected for this phase of construction.

EQUIPMENT INFORMATION

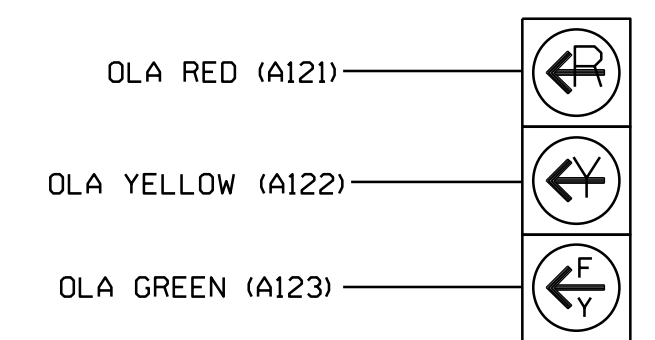
CONTROLLER.....2070
 CABINET332 W/ AUX
 SOFTWAREECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S8,S11,AUX S1
 PHASES USED.....2,6,8
 OVERLAP "A".....2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to the manufacturer's directions and NCDOT engineer approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

FYA SIGNAL WIRING DETAIL

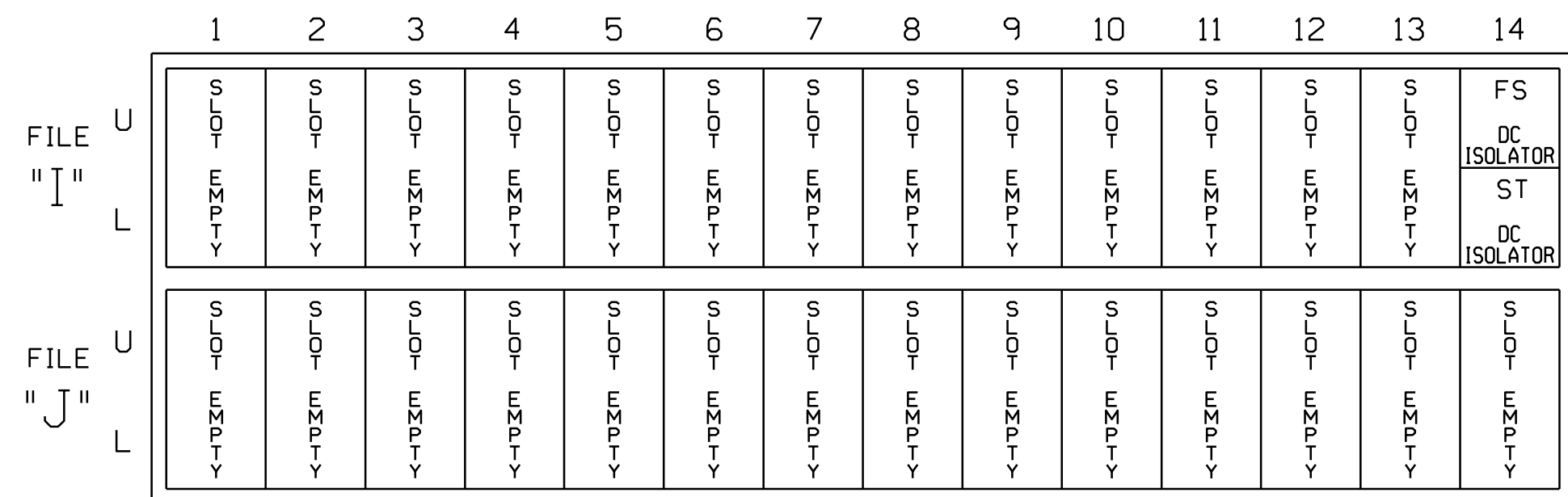
(wire signal head as shown)



61

INPUT FILE POSITION LAYOUT

(front view)



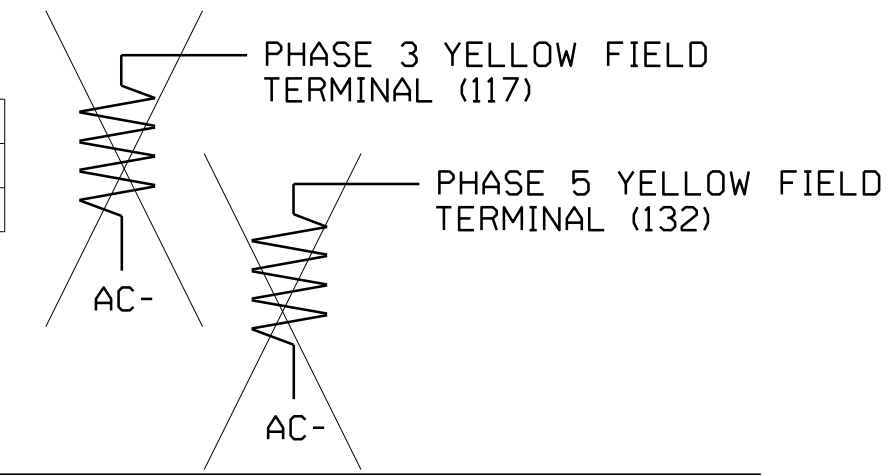
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

LOAD RESISTOR INSTALLATION DETAIL

(remove resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



IMPORTANT: Remove resistors as shown.

Electrical Detail - Sheet 1 of 2
Temporary Design 2 - (TMP Phase 1, Step 1B)

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

NC 42 at Flowers Parkway

Division 4 Johnston County Clayton

PLAN DATE: April 2020 REVIEWED BY: WJ Hamilton

PREPARED BY: JT Stiff RKA PROJ. NO: 19160 (040)

REVISIONS INIT. DATE

Signature: William J. Hamilton, 4/30/20

SIG. INVENTORY NO. 04-1432T2

