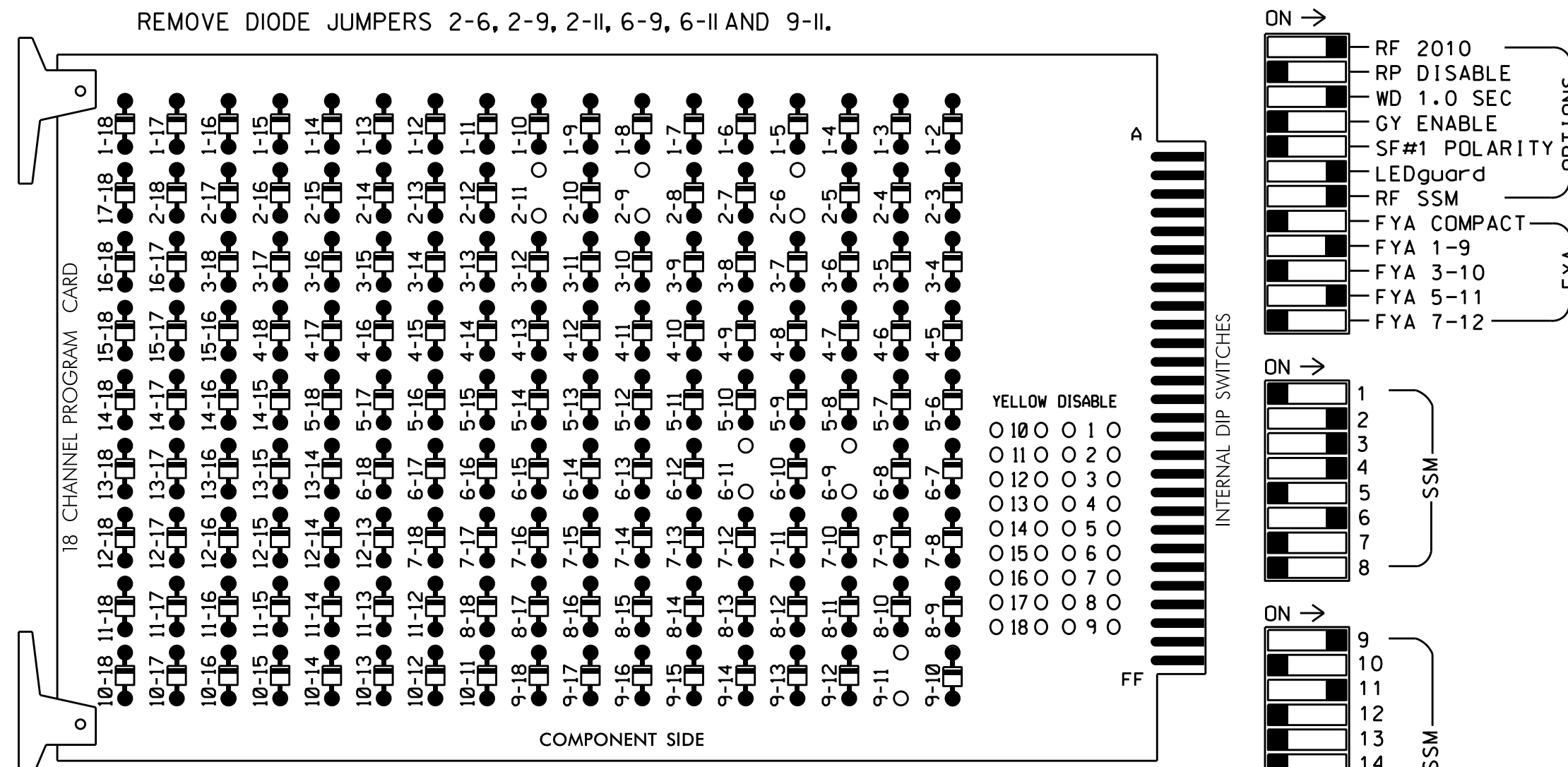


EDI MODEL 2018ECL-NC CONFLICT MONITOR  
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all Phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Startup In Green.
5. Program phases 2 and 6 for Yellow Flash and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the NC 42 (East of Clayton) Closed Loop System.
7. If this signal will be managed by ATMS software, enable controller and detector logging for all enabled detectors.

EQUIPMENT INFORMATION

CONTROLLER.....2070  
CABINET.....332 W/ AUX  
SOFTWARE.....ECONOLITE OASIS  
CABINET MOUNT.....BASE  
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
LOAD SWITCHES USED.....S2,S4,S5,S8,AUX S1, AUX S4  
PHASES USED.....2,3,4,6  
OVERLAP "A".....2  
OVERLAP "B".....NOT USED  
OVERLAP "C".....6  
OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

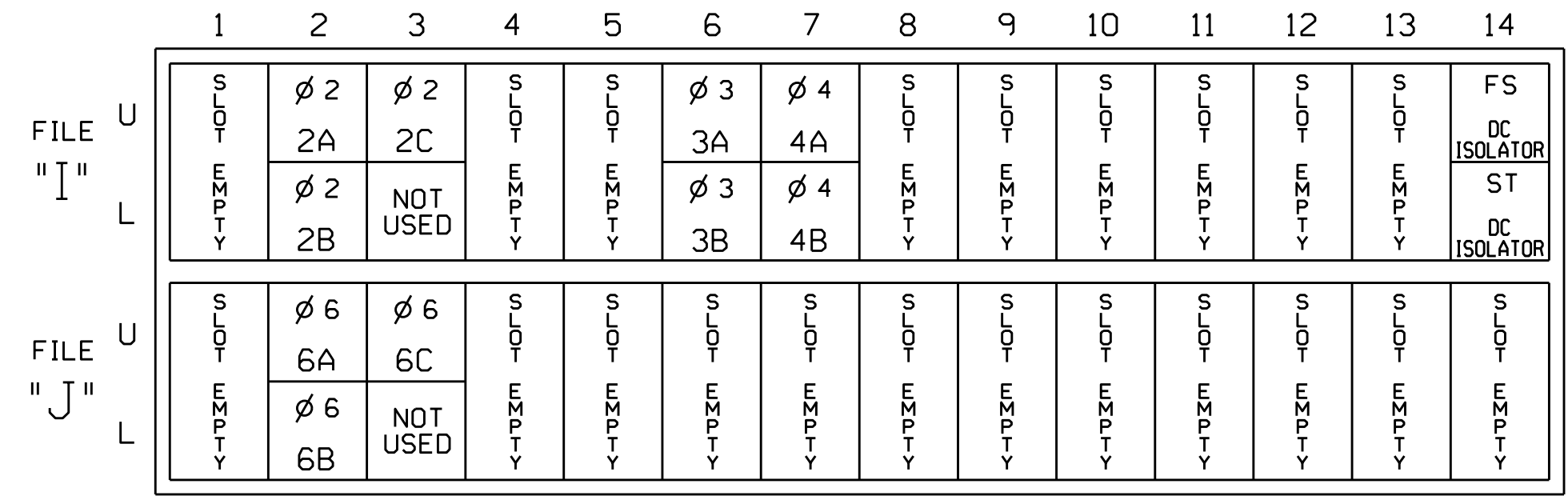
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	22,23	NU	31	32,33	41	42	NU	NU	62,63	NU	NU	61	62	63	21	22	23
RED		128		116	116	101	101			134								
YELLOW		129		117	117	102	102			135								
GREEN		130		118	118	103	103			136								
RED ARROW													A121			A114		
YELLOW ARROW													A122			A115		
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW				118	103													

NU = Not Used

\* See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)



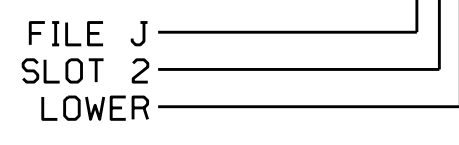
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
2C	TB2-9,10	I3U	63	25	32	2	Y	Y	Y		3
3A	TB4-9,10	I6U	41	3	4	3	Y	Y			
3B	TB4-11,12	I6L	45	7	14	3	Y	Y			15
4A	TB6-1,2	I7U	65	27	34	4	Y	Y			
4B	TB6-3,4	I7L	78	40	44	4	Y	Y			15
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
6C	TB3-9,10	J3U	64	26	36	6	Y	Y	Y		3

INPUT FILE POSITION LEGEND: J2L



OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: _ RED _ YELLOW _ GREEN
FLASH COLORS: _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

PRESS '+' TWICE

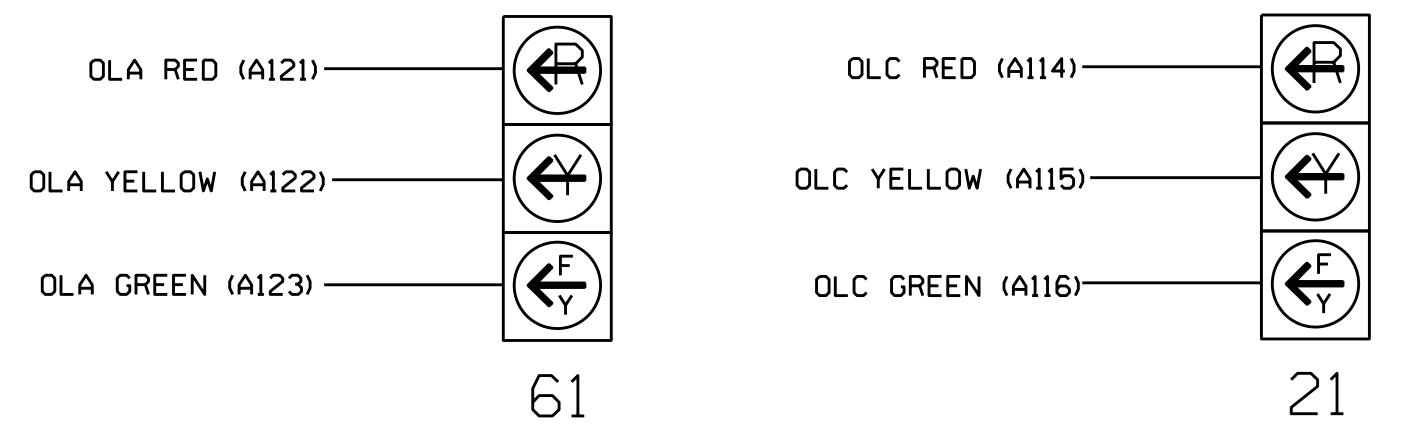
```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: _ RED _ YELLOW _ GREEN
FLASH COLORS: _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

OVERLAP PROGRAMMING COMPLETE

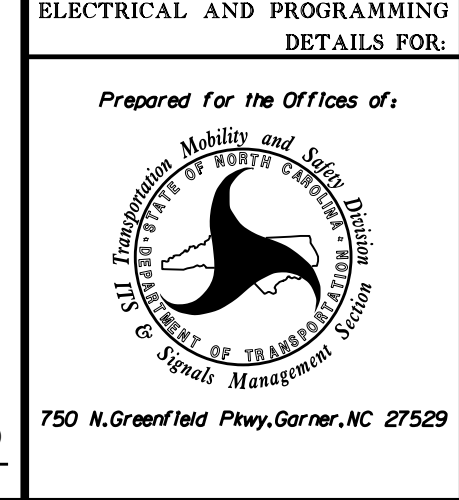
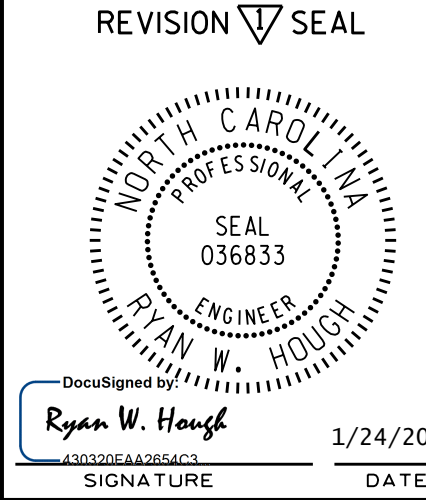
FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1423  
DESIGNED: January 2018  
SEALED: 5-25-18  
REVISED: 01-24-20

Final Design  
Electrical Detail



NC 42 at SR 1704 (Motorcycle Rd)/Portofino Dr

Division 4	Johnston County	Clayton
PLAN DATE: January 2018	REVIEWED BY: J O Deaton	
PREPARED BY: M W Yalch	REVIEWED BY:	
REVISIONS	DATE	
Revised phasing and added lane on phase 4. (JJP)	1/24/20	

Not a certified document as to the Original Document but only as to the Revisions - This document originally issued and sealed by James O. Deaton, PE #07438, on 5-25-18. This document is only certified as to the revisions.