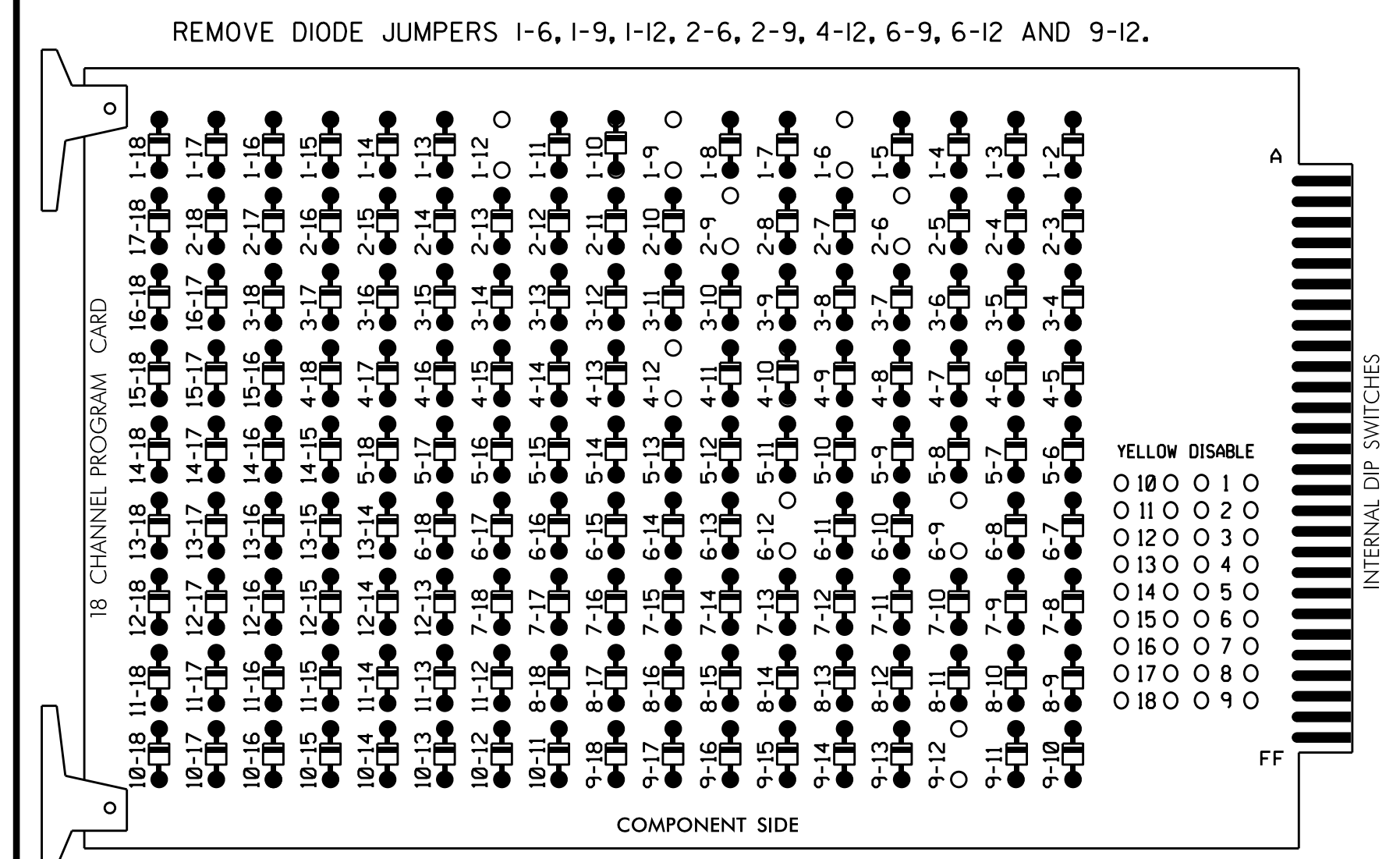


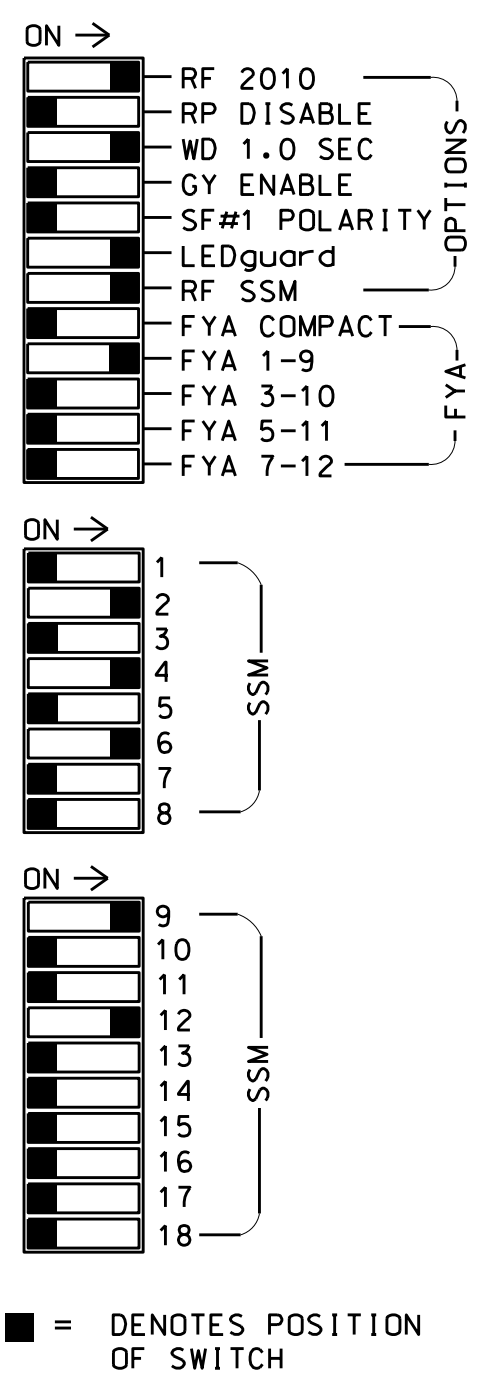
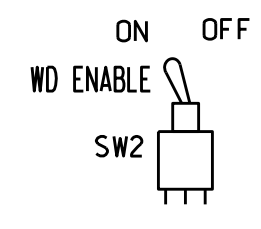
EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the NC 42 (East of Clayton) Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S5,S8,AUX S1,
 AUX S5
 PHASES USED.....1,2,4,6
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....1+4

SIGNAL HEAD HOOK-UP CHART

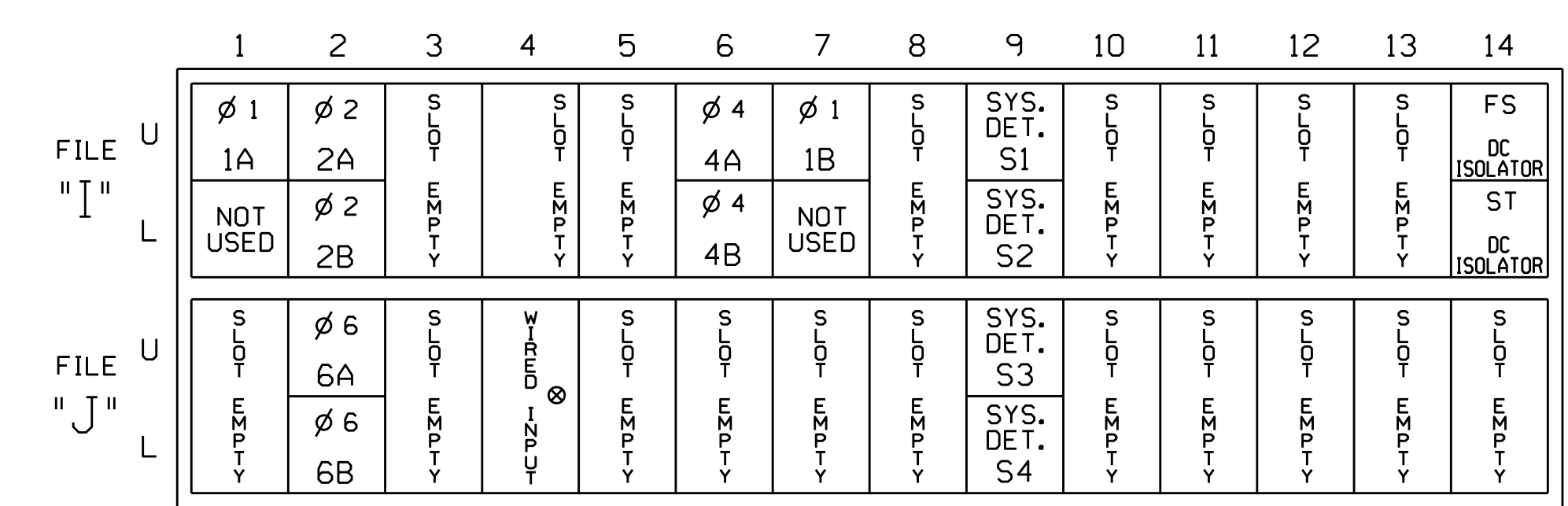
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	NU	41,42	NU	NU	61,62	NU	NU	NU	NU	11	NU	NU	NU	43	NU
RED		128						134										A101
YELLOW	*	129						135										
GREEN		130						136										
RED ARROW					101								A121					
YELLOW ARROW					102								A122				A102	
FLASHING YELLOW ARROW													A123					
GREEN ARROW	127				103													A103

NU = Not Used

- * Denotes install load resistor. See load resistor installation detail this sheet.
- ★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

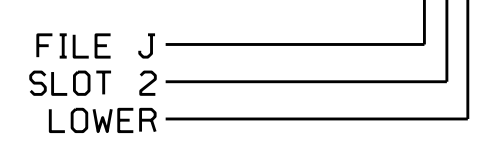
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y			
1B	TB6-1,2	I7U	65	27	34	1	Y	Y			15
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
* S3	TB7-9,10	J9U	59	21	15	SYS					
* S4	TB7-11,12	J9L	61	23	17	SYS					

¹Add jumper from I1-W to J4-W, on rear of input file.

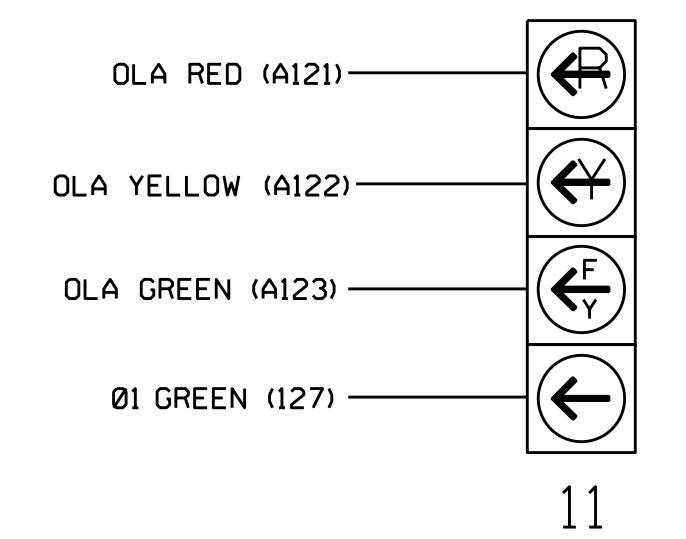
* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal head as shown)

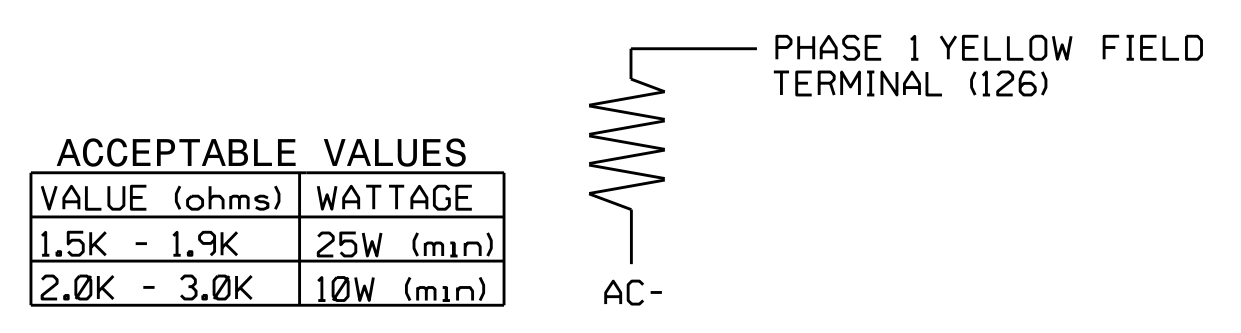


NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)



ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

Electrical Detail - Sheet 1 of 2

REVISION SEAL
 NORTH CAROLINA PROFESSIONAL SEAL 036833
 ENGINEER
 RYAN W. HOUCH
 4/17/2019

ELECTRICAL AND PROGRAMMING DETAILS FOR:
 Prepared for the Offices of:
 NC 42 at Sam Hunt Property
 Division 4 Johnston County Clayton
 PLAN DATE: January 2018 REVIEWED BY: J O Deaton
 PREPARED BY: M W Valch REVIEWED BY:
 REVISIONS
 No change to Electrical Detail. CES
 750 N. Greenfield Pkwy, Garner, NC 27529

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1433
 DESIGNED: January 2018
 SEALED: 5/25/2018
 REVISED: 4/17/2019

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Not a certified document as to the Original Document but Only as to the Revisions - This document originally issued and sealed by James Deaton, 07438, on 5/25/2018. This document is only certified as to the revisions.

17-Apr-2018 10:16
 S:\MITS\041433\Sigs\Signal\work\hgr\oups\51g_Maps\5171-1ck\oups\041433_sml_e.xxx.dgn
 GBSH/CK/and