PROJECT REFERENCE NO.	SHEET NO.
U-4405	SIG-35.2

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL
(program controller as shown)

1. From Main Menu select [2. CONTROLLER]

2. From CONTROLLER Submenu select [2. VEHICLE OVERLAPS]

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```
TMG VEH OVLP...[A] TYPE: .....│PPLT FYA│

PROTECTED LEFT TURN....   PHASE    1
OPPOSING THROUGH.......   PHASE    2

FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 1  ←── NOTICE ACTION
                                            PLAN SF BIT "1"
```
Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: .....│PPLT FYA│

PROTECTED LEFT TURN....   PHASE    5
OPPOSING THROUGH.......   PHASE    6

FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..0.0 CLEARANCE..0.0
ACTION PLAN SF BIT DISABLE........... 5  ←── NOTICE ACTION
                                            PLAN SF BIT "5"
```

END PROGRAMMING

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING <u>FREE RUN</u> — PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1, and 5.

TO RUN ALT. PHASING DURING <u>COORDINATION</u> — SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1, and 5.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN <u>DEFAULT PHASING</u>	1	NONE
ACTIONS REQUIRED TO RUN <u>ALTERNATE PHASING</u>	2	1, 5

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1, AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1,5:   Modifies overlap parent phases for heads 11, and 51 to run protected turns only.

VEH DET PLAN 2:   Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 0 seconds.

                   Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 0 seconds.

```
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0491
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A
```

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Final Design
Electrical Detail - Sheet 2 of 3

Stantec

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ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared In the Offices of:

750 N.Greenfield Pkwy,Garner,NC 27529

US 401 Business (Raeford Road)
at
Sandalwood Drive/
Layfayette Ford Entrance

Division 6	Cumberland County	Fayetteville
PLAN DATE: March 2018	REVIEWED BY: L Overn	
PREPARED BY: R M Muncey	REVIEWED BY:	

SEAL

NORTH CAROLINA
PROFESSIONAL
SEAL
045933
LAWRENCE E. OVERN II
ENGINEER

3/29/2018

REVISIONS	INIT.	DATE

SIG. INVENTORY NO.   06-0491