PROJECT REFERENCE NO.	SHEET NO.
U-4405	SIG-24.2

ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL
(program controller as shown)

1. From Main Menu select | 2. CONTROLLER |

2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

```
TMG VEH OVLP...[A] TYPE: .....│PPLT FYA│

PROTECTED LEFT TURN....    PHASE    1
OPPOSING THROUGH.......    PHASE    2

FLASHING ARROW OUTPUT.....CH9  ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE.......... 1
```
← NOTICE SF BIT DISABLE 1

Toggle Twice

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'

```
TMG VEH OVLP...[C] TYPE: .....│PPLT FYA│

PROTECTED LEFT TURN....    PHASE    5
OPPOSING THROUGH.......    PHASE    6

FLASHING ARROW OUTPUT.....CH11 ISOLATE

DELAY START OF: FYA..O.O CLEARANCE..O.O
ACTION PLAN SF BIT DISABLE.......... 5
```
← NOTICE SF BIT DISABLE 1

END PROGRAMMING

ECONOLITE ASC/3-2070 VEHICLE DETECTOR SETUP
PROGRAMMING DETAIL FOR
ALTERNATE PHASING LOOPS 1A, 5A
(program controller as shown)

IMPORTANT!

Program detectors per the input file connection and programming chart shown on sheet 1 before proceeding.

1. From Main Menu select | 8. UTILITIES |

2. From UTILITIES Submenu select | 1. COPY/CLEAR |

3. Copy from DETECTOR PLAN "1" to DETECTOR PLAN "2".

```
COPY / CLEAR UTILITY
      FROM              TO
PHASE TIMING.... . > PHASE TIMING.... .
TIMING PLAN..... . > TIMING PLAN..... .
PH DET OPT PLAN. . > PH DET OPT PLAN. .
DETECTOR PLAN... 1 > DETECTOR PLAN... 2
  TOGGLE TO SELECT A "FROM" AND A "TO"
        THEN PRESS ENTER
```

4. From Main Menu select | 6. DETECTORS |

5. From DETECTOR Submenu select | 2. VEHICLE DETECTOR SETUP |

6. Place cursor in VEH DET PLAN [ ] position and enter "2".

- Place cursor in VEH DETECTOR [ ] position and enter "1".
- Set delay time to "3.0".

```
VEH DETECTOR [ 1]   VEH DET PLAN [ 2]
TYPE: S-STANDARD
TS2 DETECTOR..... . ECPI LOG.......   NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
         1 1 . . . . . . . . . . . . . . .
EXTEND TIME... 0.0 DELAY TIME...    3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN....... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
```
← NOTICE VEH DET PLAN 2
← ENSURE DELAY IS SET TO '3.0'

- Place cursor in VEH DETECTOR [ ] position and enter "26".
- Set assigned phase to "0".

```
VEH DETECTOR [26]   VEH DET PLAN [ 2]
TYPE: G-GREEN EXTENSION/DELAY
TS2 DETECTOR..... . ECPI LOG.......   NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
        26 0 . . . . . . . . . . . . . . .
EXTEND TIME... 0.0 DELAY TIME...    3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN....... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
```
← NOTICE VEH DET PLAN 2
← ENSURE PHASE IS SET TO "0"

- Place cursor in VEH DETECTOR [ ] position and enter "5".
- Set delay time to "3.0".

```
VEH DETECTOR [ 5]   VEH DET PLAN [ 2]
TYPE: S-STANDARD
TS2 DETECTOR..... . ECPI LOG.......   NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
         5 5 . . . . . . . . . . . . . . .
EXTEND TIME... 0.0 DELAY TIME...    3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN....... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
```
← NOTICE VEH DET PLAN 2
← ENSURE DELAY IS SET TO '3.0'

- Place cursor in VEH DETECTOR [ ] position and enter "22".
- Set assigned phase to "0".

```
VEH DETECTOR [22]   VEH DET PLAN [ 2]
TYPE: G-GREEN EXTENSION/DELAY
TS2 DETECTOR..... . ECPI LOG.......   NO
DET PH - 1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
        22 0 . . . . . . . . . . . . . . .
EXTEND TIME... 0.0 DELAY TIME...    3.0
USE ADDED INITIAL . CROSS SWITCH PH.. 0
LOCK IN....... NONE NTCIP VOL . OR OCC .
PMT QUEUE DELAY. NO
```
← NOTICE VEH DET PLAN 2
← ENSURE PHASE IS SET TO "0"

END PROGRAMMING

ECONOLITE ASC/3-2070 ACTION PLAN PROGRAMMING DETAIL

1. From Main Menu select | 5. TIME BASE |

2. From TIME BASE Submenu select | 2. ACTION PLAN |

```
ACTION PLAN...[ 1]
PATTERN..........AUTO   SYS OVERRIDE.... NO
TIMING PLAN........ 0   SEQUENCE........ 0
VEH DETECTOR PLAN.. 2   DET LOG.......NONE
FLASH.......... --      RED REST........ NO
VEH DET DIAG PLN... 0   PED DET DIAG PLN..0
DIMMING ENABLE.. NO     PRIORITY RETURN. NO
PED PR RETURN..  NO     QUEUE DELAY..... NO
PMT COND DELAY   NO
  PHASE   1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
PED RCL   . . . . . . . . . . . . . . . .
WALK 2    . . . . . . . . . . . . . . . .
VEX 2     . . . . . . . . . . . . . . . .
VEH RCL   . . . . . . . . . . . . . . . .
MAX RCL   . . . . . . . . . . . . . . . .
MAX 2     . . . . . . . . . . . . . . . .
  PHASE   1 2 3 4 5 6 7 8 9 0 1 2 3 4 5 6
MAX 3     . . . . . . . . . . . . . . . .
CS INH    . . . . . . . . . . . . . . . .
OMIT      . . . . . . . . . . . . . . . .
SPC FCT   X . . . X . . . (1-8)
AUX FCT   . . (1-3)
          1 2 3 4 5 6 7 8 9 0 1 2 3 4 5
LP  1-15  . . . . . . . . . . . . . . .
LP 16-30  . . . . . . . . . . . . . . .
LP 31-45  . . . . . . . . . . . . . . .
LP 46-60  . . . . . . . . . . . . . . .
LP 61-75  . . . . . . . . . . . . . . .
LP 76-90  . . . . . . . . . . . . . . .
LP 91-100 . . . . . . . . . . . . . . .
```

ALTERNATE PHASING ACTIVATION DETAIL

TO RUN ALT. PHASING DURING FREE RUN - PROGRAM CHANGES (SHOWN BELOW) IN A TIME BASED ACTION PLAN. SCHEDULE A DAY PLAN THAT INCLUDES THE ACTION PLAN PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

TO RUN ALT. PHASING DURING COORDINATION - SELECT THE TIME BASED ACTION PLAN THAT IS PROGRAMMED TO SELECT VEH DET PLAN 2 AND ENABLE SF BITS 1 and 5.

PHASING	VEH DET PLAN	SF BITS ENABLED
ACTIONS REQUIRED TO RUN DEFAULT PHASING	1	NONE
ACTIONS REQUIRED TO RUN ALTERNATE PHASING	2	1, 5

IMPORTANT: IF ALT. PHASING IS USED DURING FREE RUN AND COORDINATION, DO NOT OPERATE TIME OF DAY EVENTS CONCURRENTLY WITH COORDINATION PLAN EVENTS IN THE EVENT SCHEDULER. (EX. FREE RUN EVENT SHOULD END BEFORE COORDINATION PLAN EVENT STARTS AND VICE-VERSA).

ALTERNATE PHASING CHANGE SUMMARY

THE FOLLOWING IS A SUMMARY OF WHAT TAKES PLACE WHEN SF BITS 1 AND 5 AND VEH DET PLAN 2 ACTIVATE TO CALL THE "ALTERNATE PHASING":

SF BITS 1,5:   Modifies overlap parent phases for heads 11 and 51 to run protected turns only.

VEH DET PLAN 2:   Disables phase 6 call on loop 1A and reduces delay time for phase 1 call on loop 1A to 3 seconds.
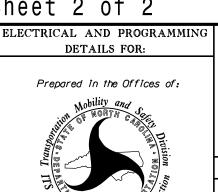
Disables phase 2 call on loop 5A and reduces delay time for phase 5 call on loop 5A to 3 seconds.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0592T2
DESIGNED: March 2018
SEALED: 03-29-2018
REVISED: N/A

Temporary Design 2 - TMP Phase II
Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED
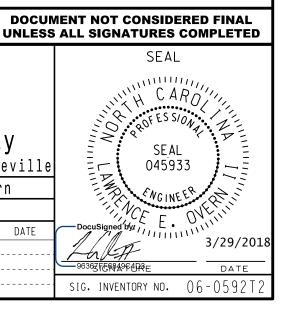
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ELECTRICAL AND PROGRAMMING DETAILS FOR:
Prepared In the Offices of:
750 N.Greenfield Pkwy,Garner,NC 27529

US 401 (Raeford Road) at Revere Street/ Time Warner Cable Driveway
Division 6   Cumberland County   Fayetteville

| PLAN DATE: March 2018 | REVIEWED BY: L Overn |
| PREPARED BY: G B Spell | REVIEWED BY: |

SEAL
NORTH CAROLINA
SEAL 045933
LAWRENCE E. OVERN
3/29/2018

SIG. INVENTORY NO. 06-0592T2