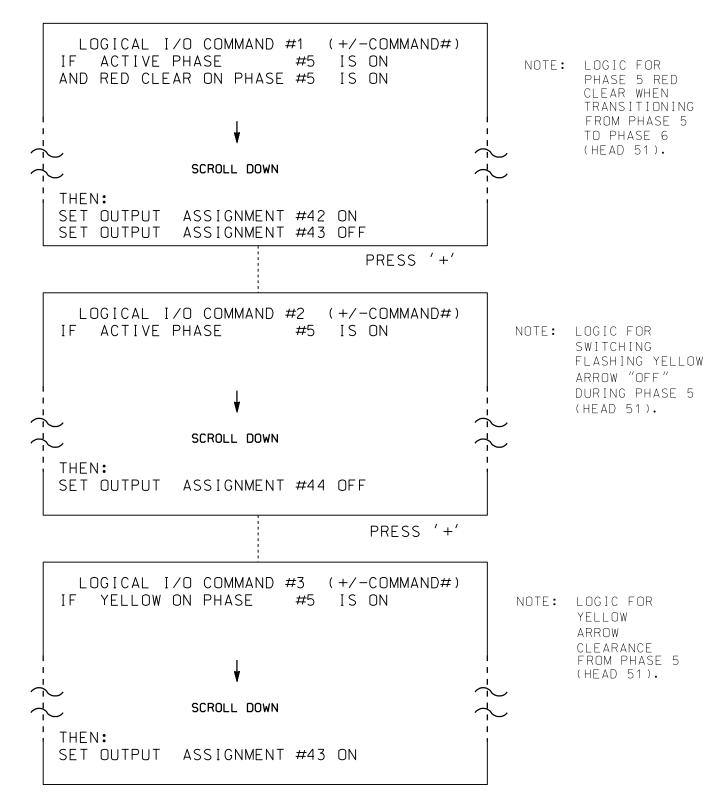
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

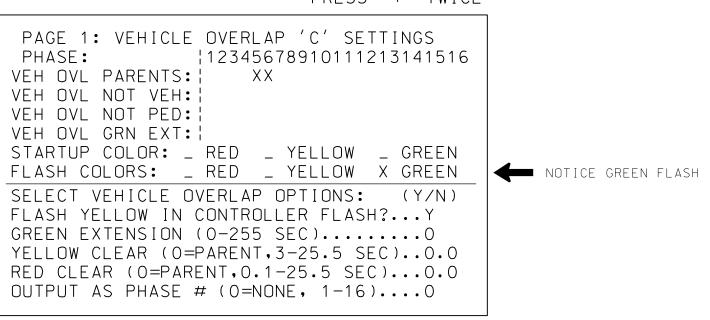
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS "+" TWICE



OVERLAP PROGRAMMING COMPLETE

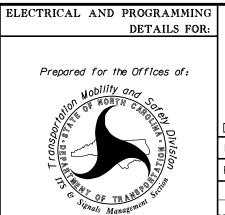
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 06-0464
DESIGNED: May 2018
SEALED:
REVISED: N/A

Electrical Detail - Sheet 2 of 2



Stantec Consulting Services Inc. 801 Jones Franklin Road-Suite 300 Raleigh, NC 27606 Tel. (919) 851-6866 Fax. (919) 851-7024 www.stantec.com

License No. F-0672



750 N.Greenfield Pkwy, Garner, NC 27529

US 74/US76 (Andrew Jackson Hwy) at NC 87 (Old Stage Road)

ision 6	Columbus County			Delco	
DATE:	May 2018	REVIEWED BY:	E D HA	RRIS	
ARED BY:	J HAMBRIGHT	REVIEWED BY:	B L WA	TSON	
REVISIONS			INIT.	DATE	]_
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NG INER

Regina M. Muncy 5/29/2018

C7FESSBBSECTSLIPE.

SIG. INVENTORY NO. 06-0464