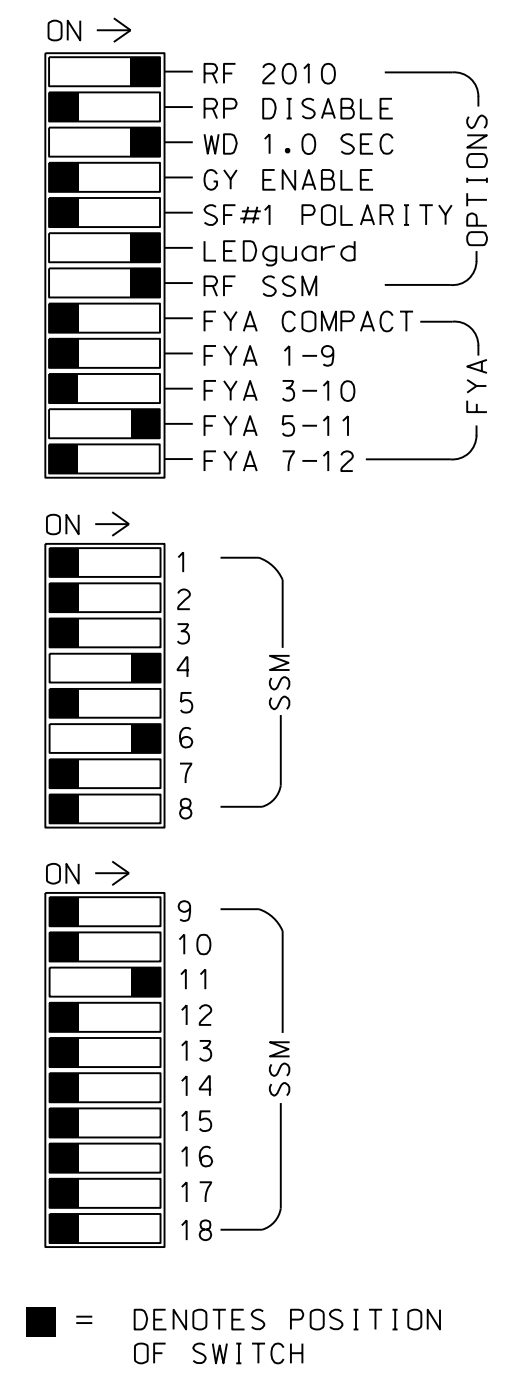
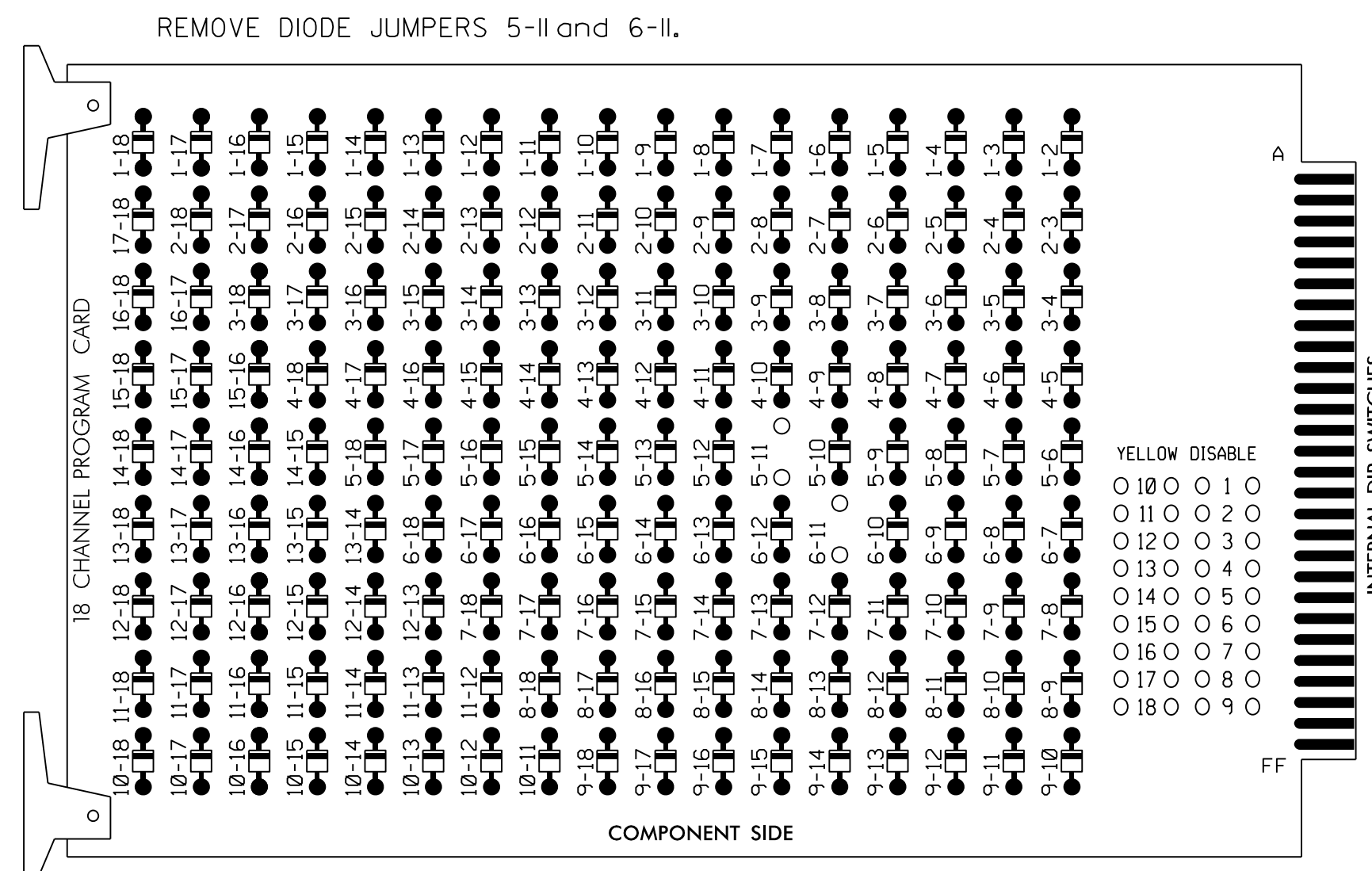


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

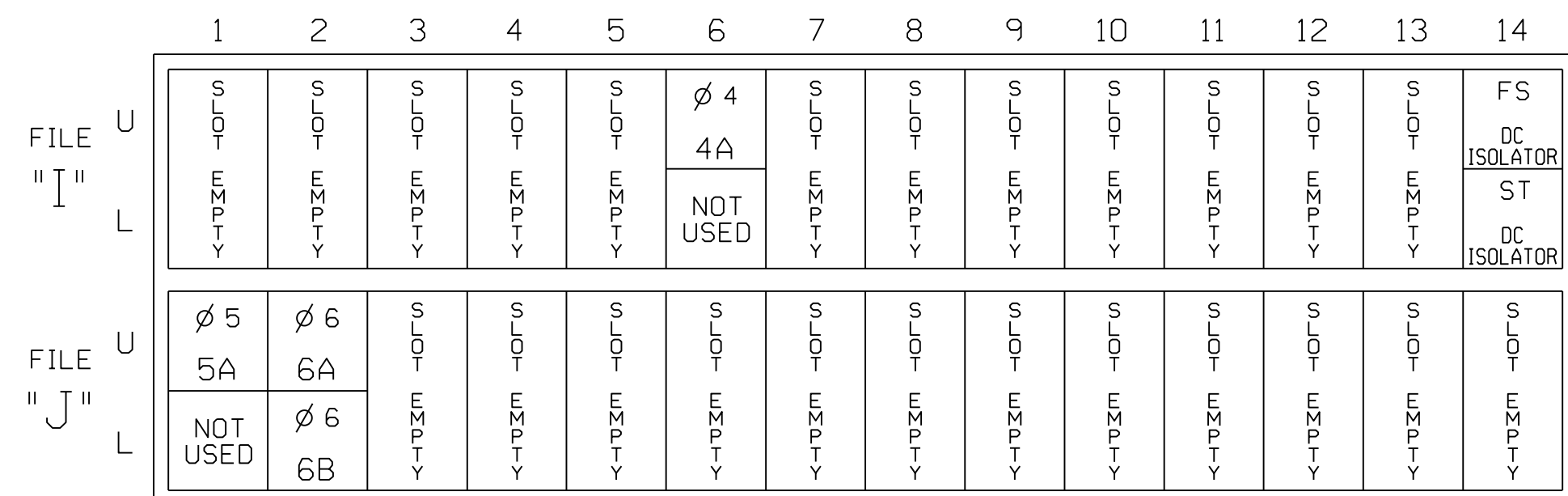


NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all Phases.
3. Program phase 6 for Variable Initial and Gap Reduction.
4. Program phase 6 for Startup In Green.
5. Program phase 6 for Yellow Flash.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	NU	NU	NU	41,42	NU	51*	61,62	NU	NU	NU	NU	NU	NU	NU	51*	NU	NU
RED					101			134										
YELLOW					102		*	135										
GREEN					103			136										
RED ARROW																	A114	
YELLOW ARROW																	A115	
FLASHING YELLOW ARROW																	A116	
GREEN ARROW							133											

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

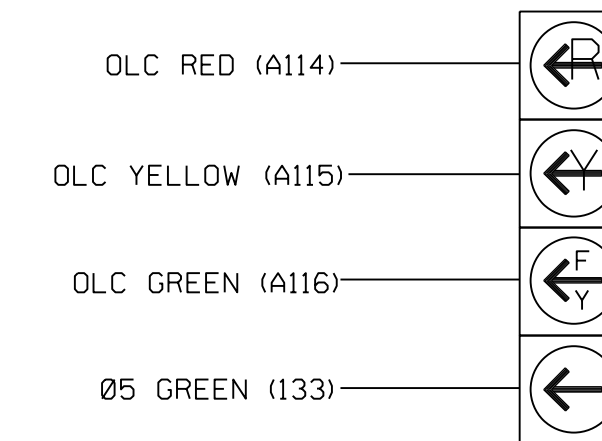
★ See pictorial of head wiring in detail this sheet.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S5,S7,S8,AUX S4
 PHASES USED.....4,5,6
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)



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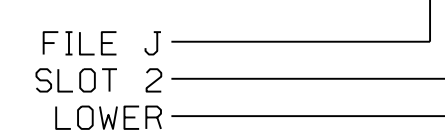
NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 of 2 for programming instructions.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
5A	TB3-1,2	J1U	55	17	5	5	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			

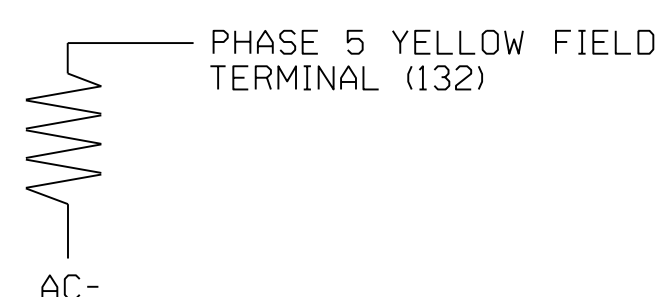
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Sheet 1 of 2

Stantec Consulting Services Inc.
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Electrical and Programming Details For:

US 74/US76 (Andrew Jackson Hwy) at NC 87 (Old Stage Road)

Division 6 Columbus County Delco

PLAN DATE: May 2018 REVIEWED BY: E D HARRIS

PREPARED BY: J HAMBRIGHT REVIEWED BY: B L WATSON

REVISIONS INIT. DATE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 06-0464
 DESIGNED: May 2018
 SEALED:
 REVISED: N/A

SEAL

Regina M. Muncey 5/29/2018

SIG. INVENTORY NO. 06-0464