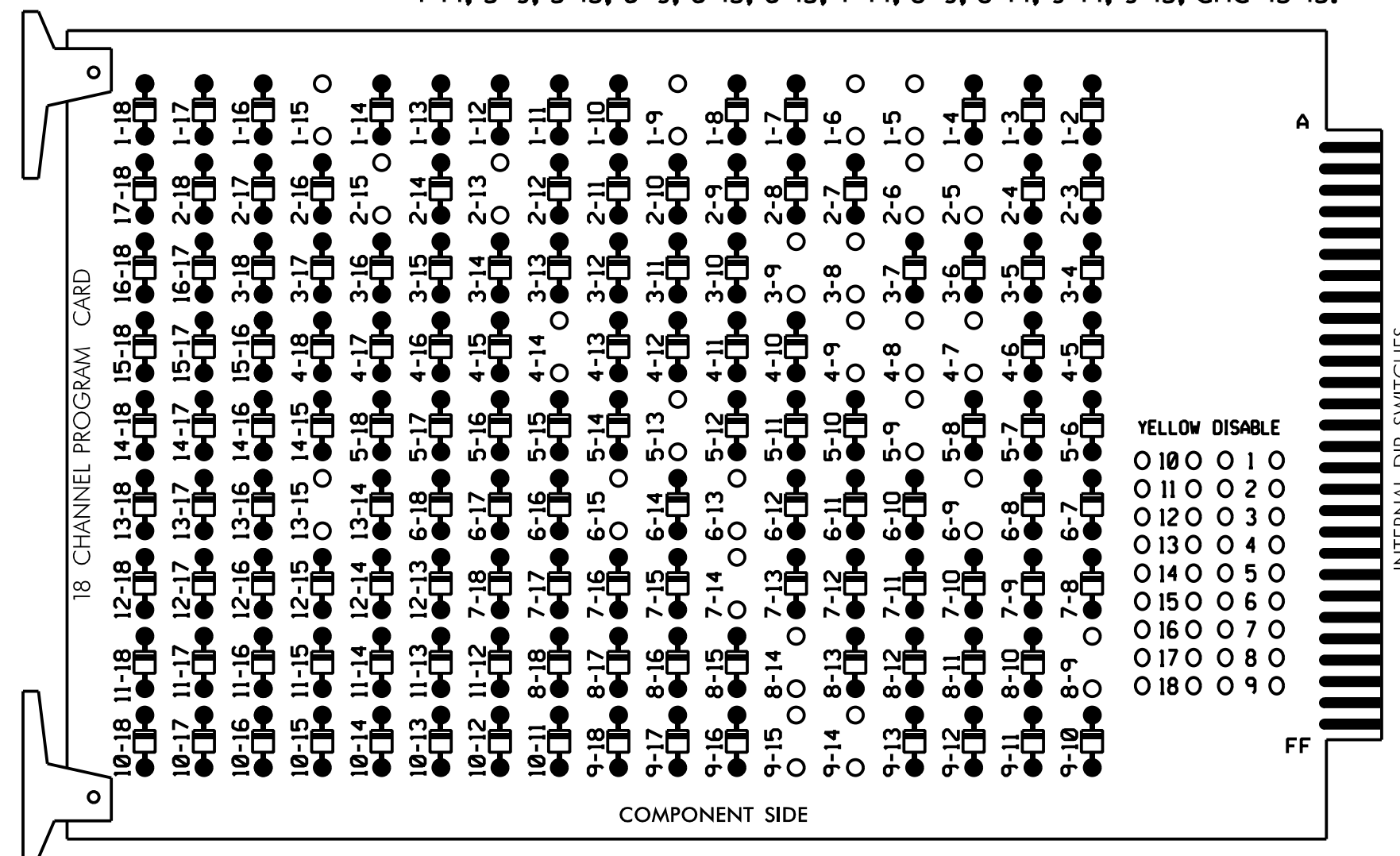


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-15, 2-5, 2-6, 2-13, 2-15, 3-8, 3-9, 4-7, 4-8, 4-9, 4-14, 5-9, 5-13, 6-9, 6-13, 6-15, 7-14, 8-9, 8-14, 9-14, 9-15, and 13-15.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Ensure that jumper SEL9 is removed from the monitor board.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 10,11, 12,13,14,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
3. Enable Simultaneous Gap-Out for all Phases.
4. Program phases 2 and 6 for Startup In Green.
5. Program phases 2, 4, and 6 for 'STARTUP PED CALL'.
6. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
7. The cabinet and controller are part of the Gilead Road Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....EXISTING 332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S2P,S3,S4,S4P,S5,S6,S6P,S7,S8,S9
 PHASES USED.....1,2,2 PED,3,4,4 PED,5,6,6 PED,7,8
 OVERLAP "A".....1+8
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11,12	21,22	P21, P22	31	41,42	P41, P42	51	61,62	P61, P62	62	71,72	81,82	NU	83,84	NU	NU	NU	NU
RED		128			101			134			107		A121					
YELLOW		129			102			135			108							
GREEN		130			103			136			109							
RED ARROW	125				116			131			122							
YELLOW ARROW	126				117			132			123	123		A122				
GREEN ARROW	127				118			133			124	124		A123				
Hand			113			104			119									
Walking			115			106			121									

NU = Not Used

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

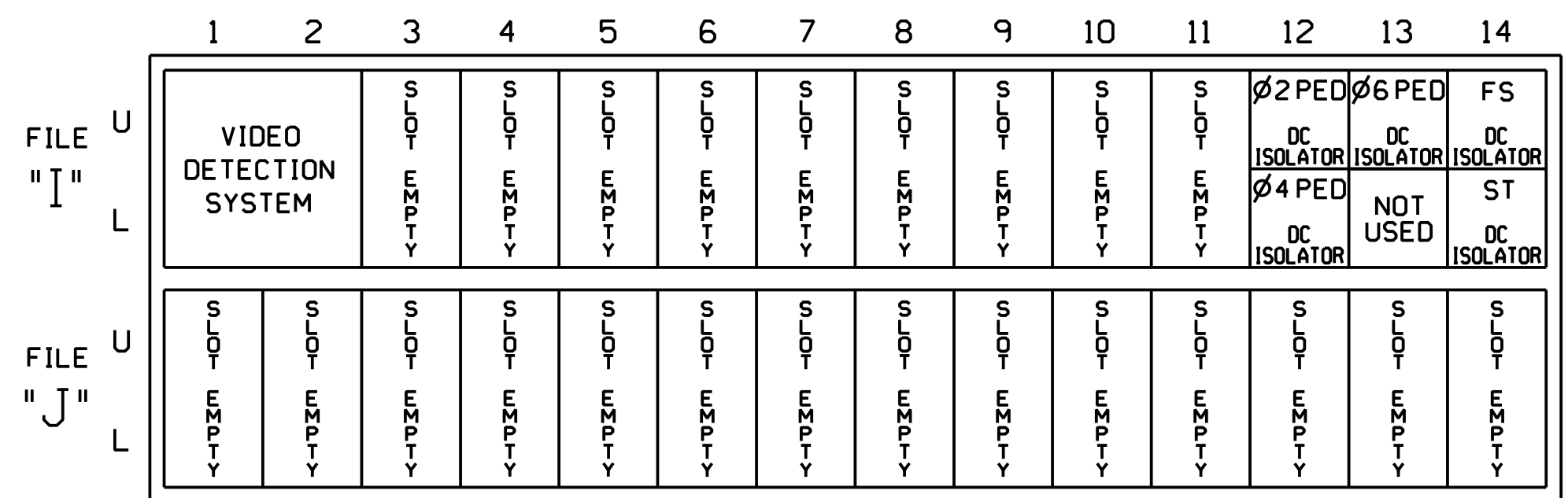
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X X
VEH OVL NOT VEH: X
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-25.5 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE "N"

OVERLAP PROGRAMMING COMPLETE

INPUT FILE POSITION LAYOUT

(front view)



EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

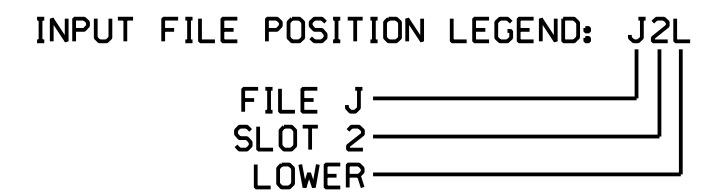
SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
PED PUSH BUTTONS											
P21,P22	T88-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	T88-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	T88-7,9	I13U	68	30	PED 6	6 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS I12 AND I13.



PHASE SEQUENCE PROGRAMMING DETAIL

(program controller as shown below)

FROM OASIS LOCAL CONTROLLER MAIN MENU
 SELECT: 4 PHASE SEQUENCE

PHASE SEQUENCE: PAGE 1	NEXT: PAGES
RNG:LEAD	BARRIER 1 X-LAG:LEAD BARRIER 2 X-LAG
1 : 1	2 0 0 : 0 4 0 3
2 : 5	6 0 0 : 7 8 0 0
3 : 0	0 0 0 : 0 0 0 0
4 : 0	0 0 0 : 0 0 0 0

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-1753 T3
 DESIGNED: December 2017
 SEALED: 04-23-2018
 REVISED: N/A

Electrical Detail - Sheet 1 of 2
 Signal Upgrade
 Temporary Design 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared in the Office of:

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 ENGINEER
 NATASHA R. SIMMONS
 4/23/2018
 SIGNATURE DATE
 SIG. INVENTORY NO. 10-1753 T3