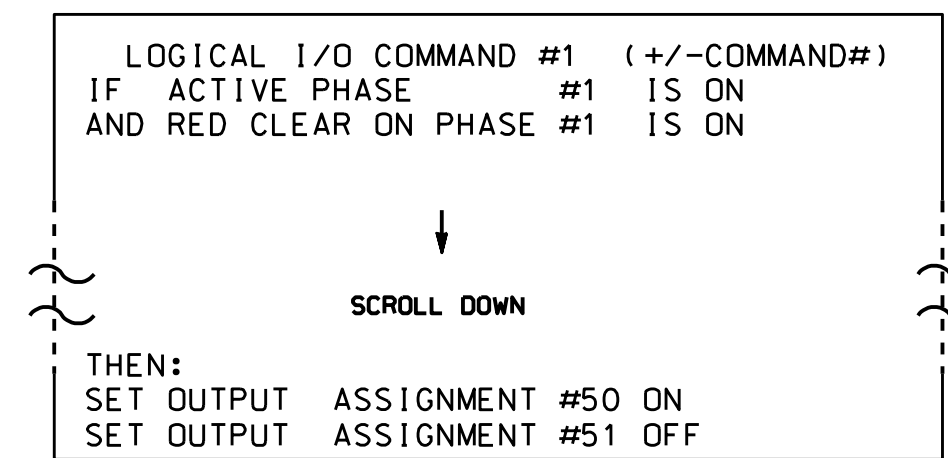


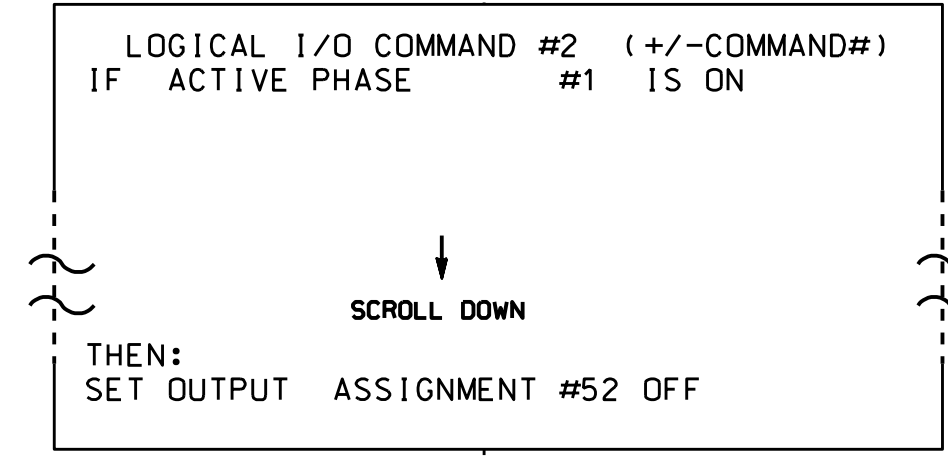
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

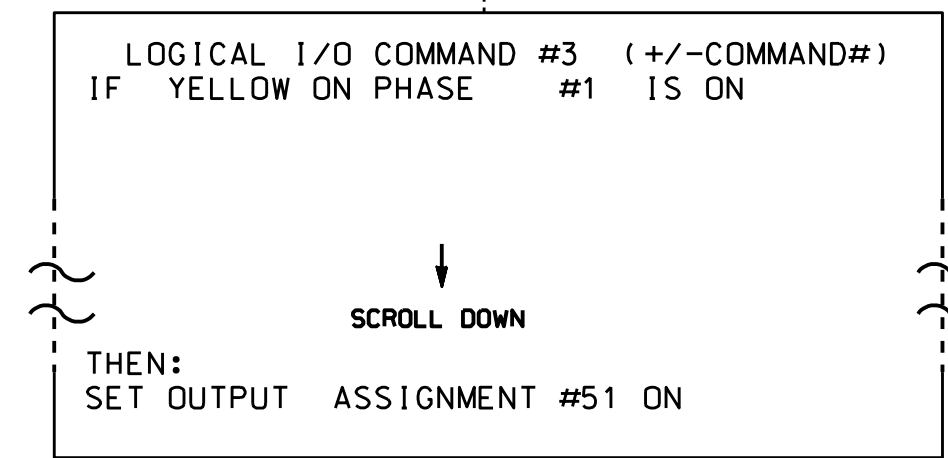
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 and 6.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



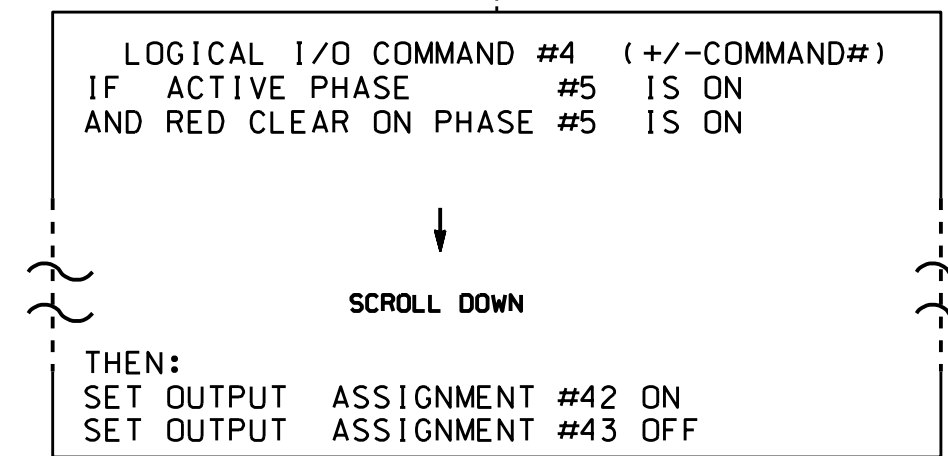
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



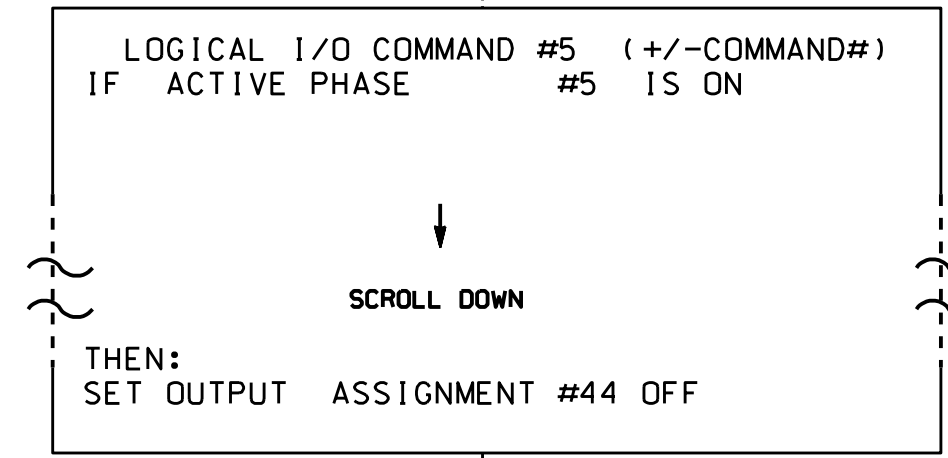
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).



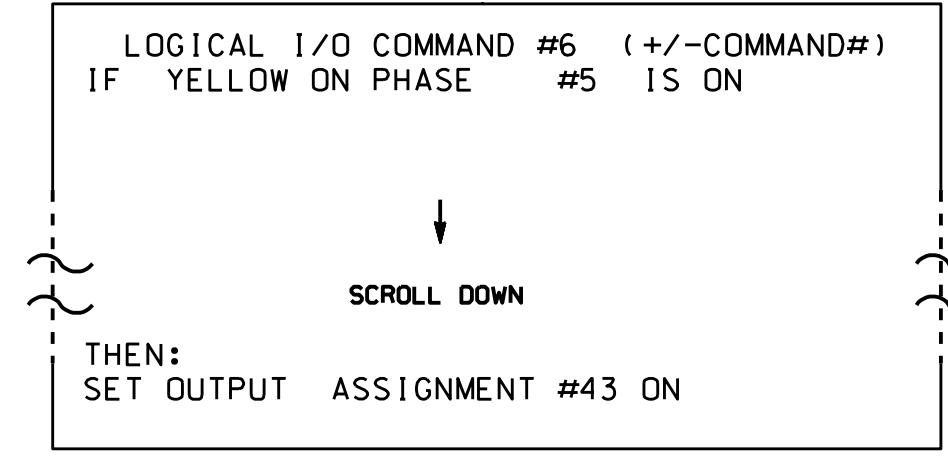
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

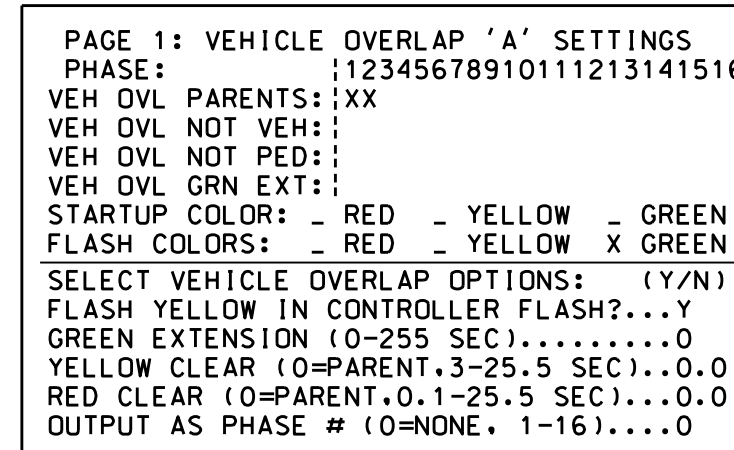
- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

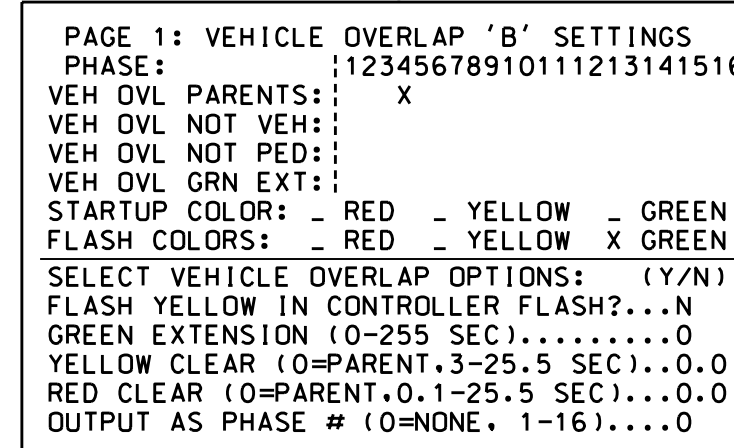
(program controller as shown below)

PROJECT REFERENCE NO. U-5963 SHEET NO. Sig 4.2

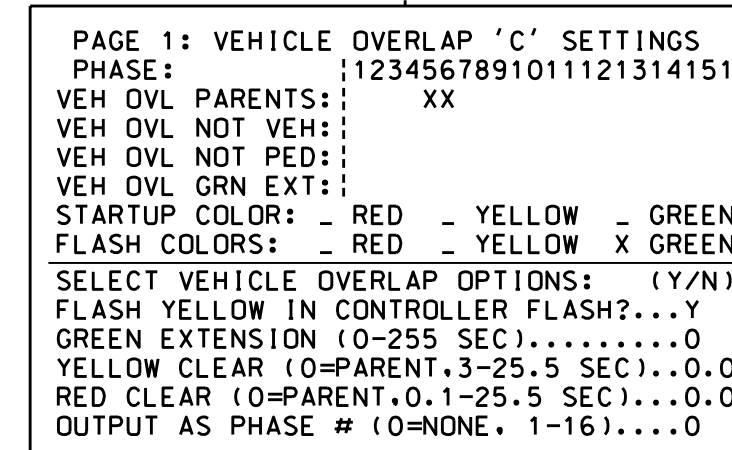
FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



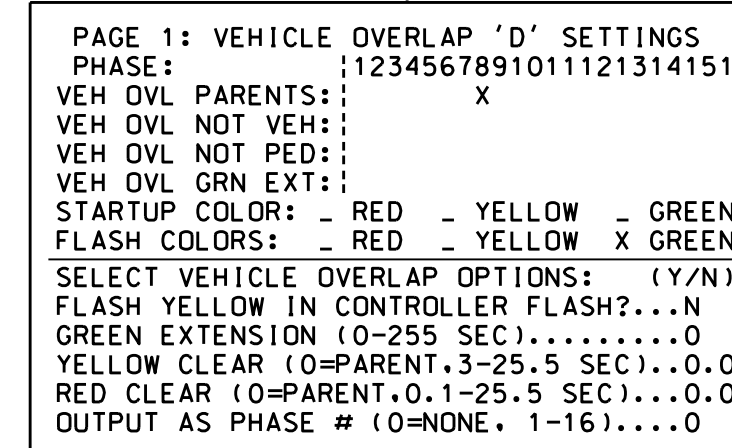
NOTICE GREEN FLASH



NOTICE GREEN FLASH



NOTICE GREEN FLASH



NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

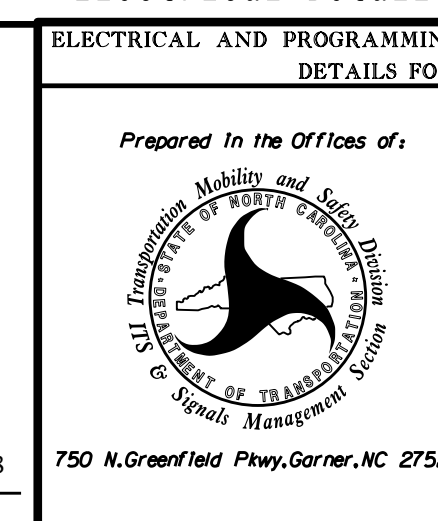
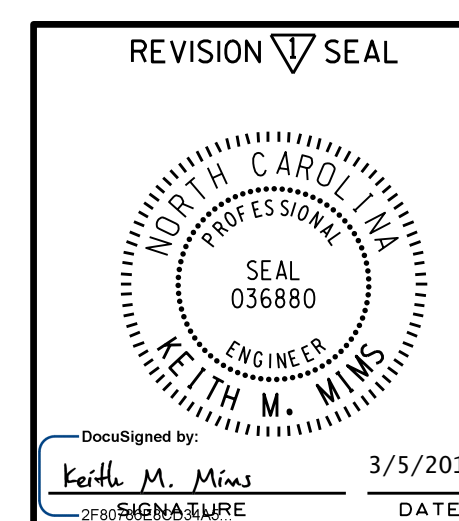
IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 12-1282
DESIGNED: January 2018
SEALED: 02-27-18
REVISED: N/A

Electrical Detail - Sheet 2 of 2



Prepared In the Offices of:		NC 115 (Mecklenburg Highway) at Quality Drive/ SR 1136 (Faith Road)	
Division 12	Iredell County	Mooreville	
PLAN DATE: August 2016	REVIEWED BY: WJ Hamilton		
PREPARED BY: NE Burns	REVIEWED BY: 14298 (040)		
REVISIONS		DATE	
Signal is part of the NC 115 (Mecklenburg Hwy) I.C.S.		KMM	3/5/2018

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

Not a certified document as to the Original Document but only as to the Revisions - This document originally issued and sealed by William J. Hamilton, PE #32396, on 8-17-16. This document is only certified as to the revisions.

SIG. INVENTORY NO. 12-1262