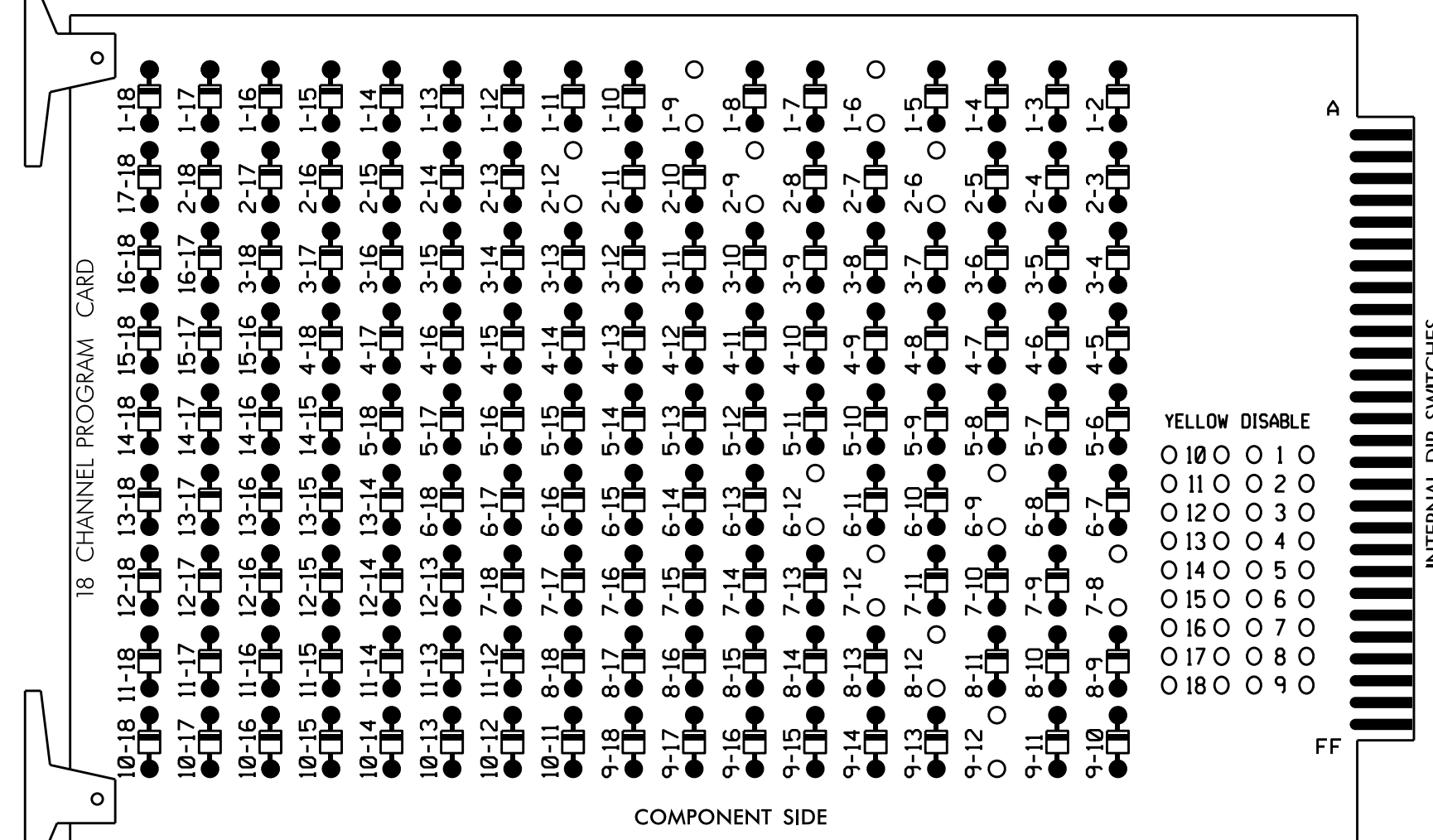


**EDI MODEL 2018ECL-NC CONFLICT MONITOR  
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

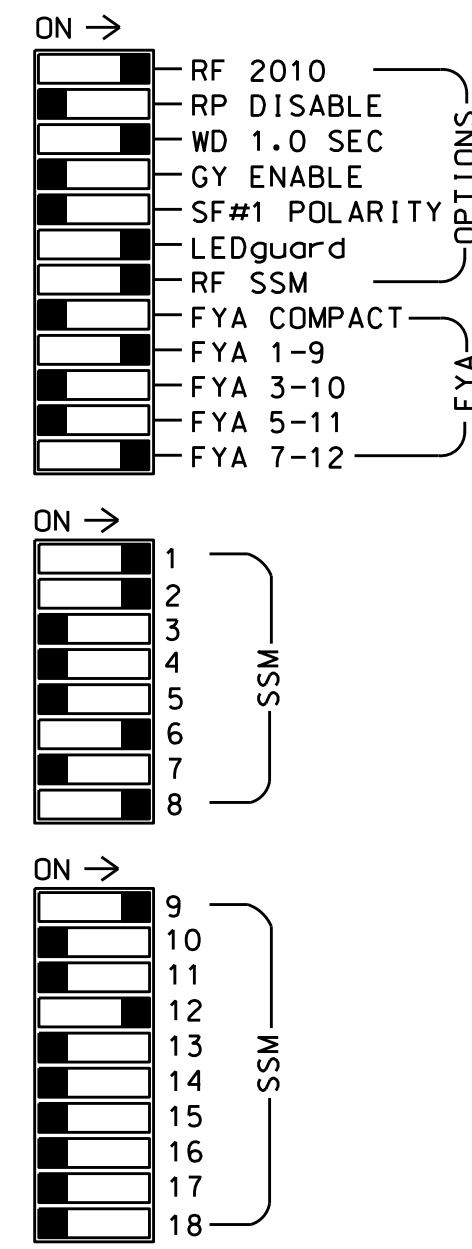
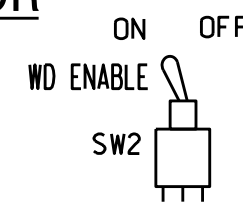
REMOVE DIODE JUMPERS 1-6, 1-9, 2-6, 2-9, 2-12, 6-9, 6-12, 7-8, 7-12, 8-12 and 9-12.



REMOVE JUMPERS AS SHOWN

**NOTES:**

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller.



■ = DENOTES POSITION OF SWITCH

**NOTES**

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all Phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Startup In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the NC 115 (Mecklenburg Hwy.) Closed Loop System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S8,S10,S11,AUX S1,AUX S5  
 PHASES USED.....1,2,6,8  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....NOT USED  
 OVERLAP "D".....2+8  
 OVERLAP "E".....NOT USED  
 OVERLAP "F".....NOT USED  
 OVERLAP "G".....8

**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	PED	3	4	PED	5	6	PED	OLG	8	PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11★	82	21,22	NU	NU	NU	NU	61,62	NU	23★	81,82	NU	11★	NU	NU	NU	23★	NU
RED	*	128						134			107							A101
YELLOW		129						135	*	108								
GREEN		130						136		109								
RED ARROW													A121					
YELLOW ARROW	126												A122					A102
FLASHING YELLOW ARROW													A123					A103
GREEN ARROW	127	127								124								

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	FS
L	NOT USED	∅ 2	∅ 3	∅ 4	∅ 5	∅ 6	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	DC ISOLATOR
U	∅ 6	∅ 6A	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15	∅ 16	∅ 17	∅ 18
L	NOT USED	NOT USED	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14	∅ 15	∅ 16	∅ 17	DC ISOLATOR

EX. : 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

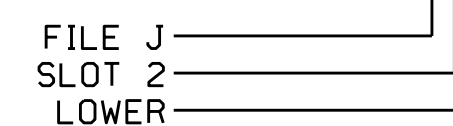
⊗ Wired Input - Do not populate slot with detector cord

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
1B	TB2-5,6	J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-7,8	I2L	43	5	12	2	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			

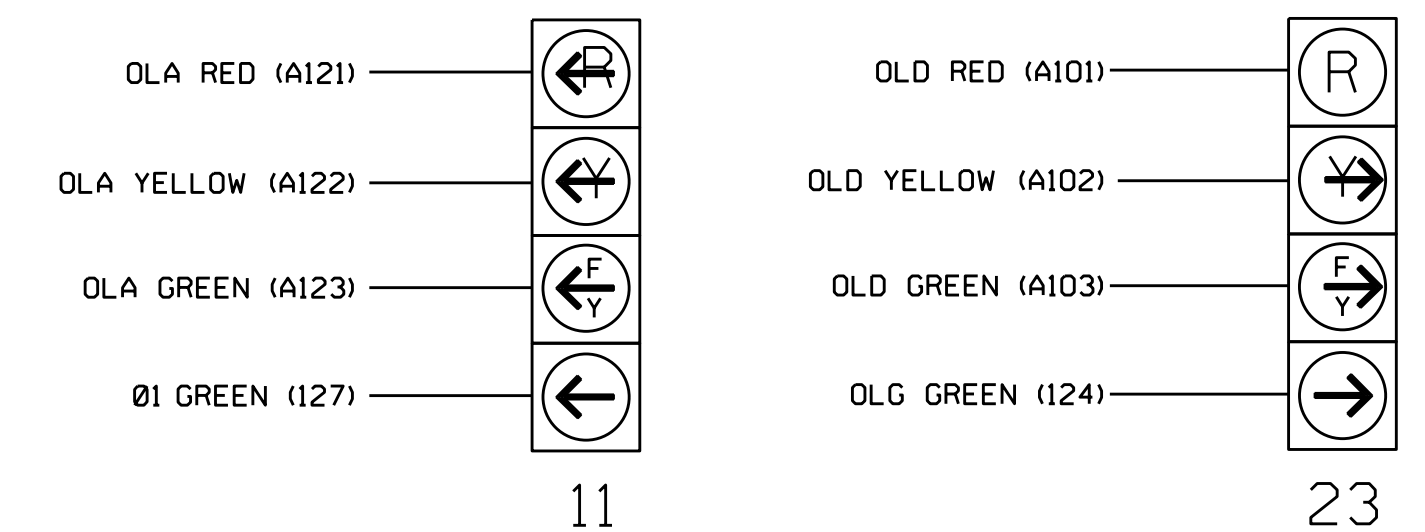
<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

INPUT FILE POSITION LEGEND: J2L



**FYA SIGNAL WIRING DETAIL**

(wire signal heads as shown)

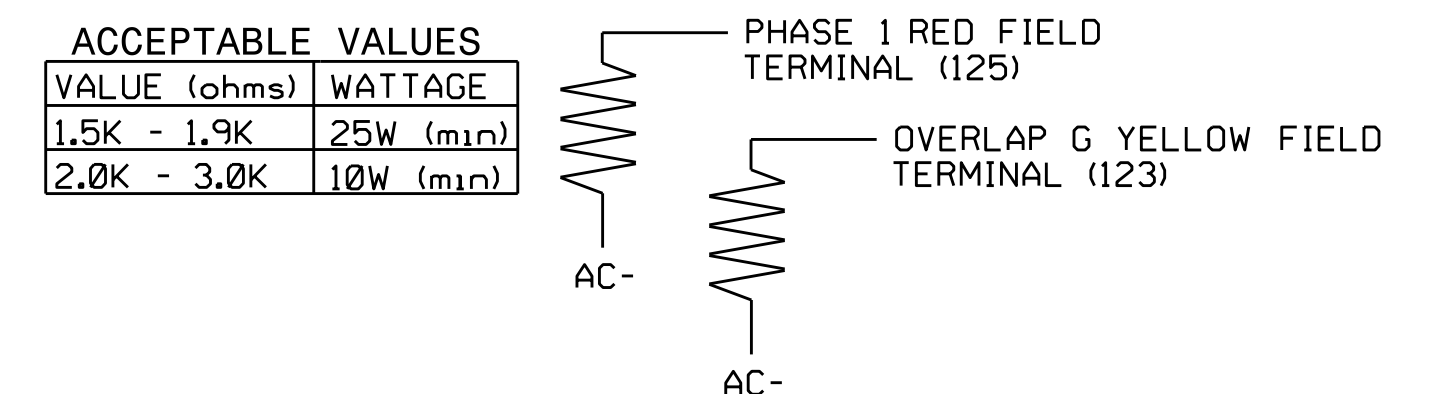


**NOTE**

The sequence display for signal heads 11 and 23 requires special logic programming. See sheet 2 for programming instructions.

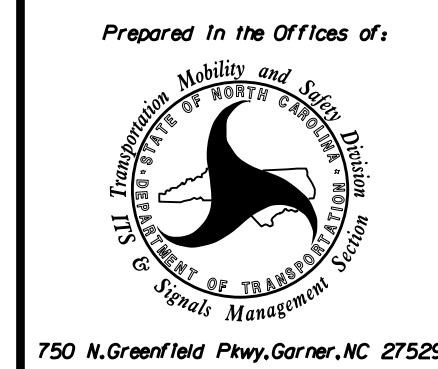
**LOAD RESISTOR INSTALLATION DETAIL**

(install resistors as shown below)



Electrical Detail - Sheet 1 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:



750 N. Greenfield Pkwy, Garner, NC 27529

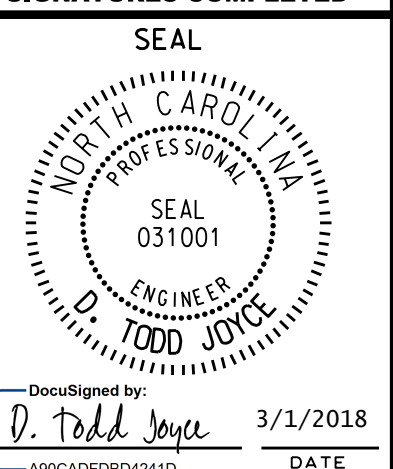
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 12-1761  
 DESIGNED: January 2018  
 SEALED: 2/27/2018  
 REVISED:

NC 115 (Mecklenburg Hwy.)  
 at  
 Yellow Wood Circle

Division 12 Iredell County Mooresville  
 PLAN DATE: February 2018 REVIEWED BY: T. Joyce  
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



DocuSigned by: Todd Joyce 3/1/2018  
 4390CAED804240  
 DATE  
 SIG. INVENTORY NO. 12-1761