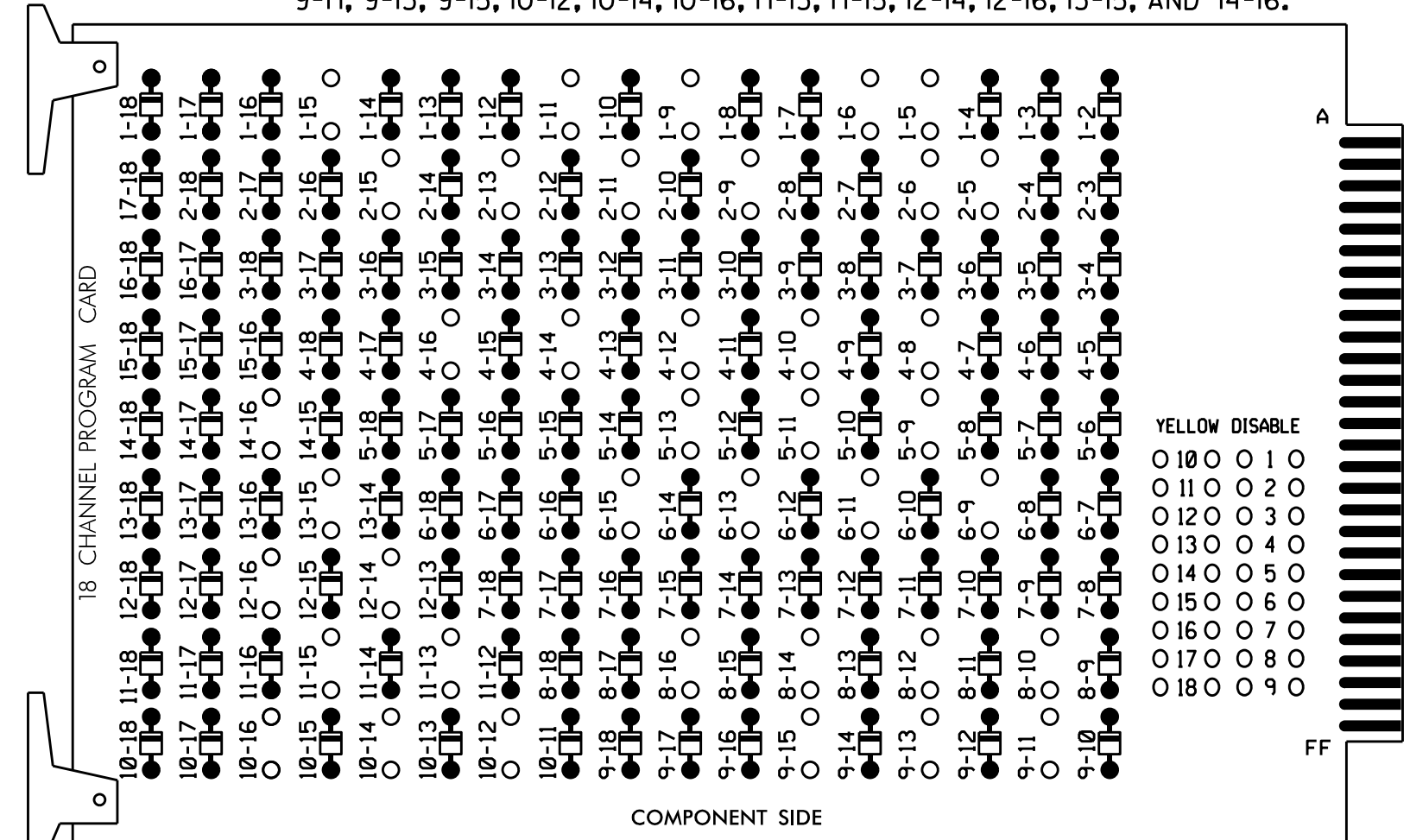


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

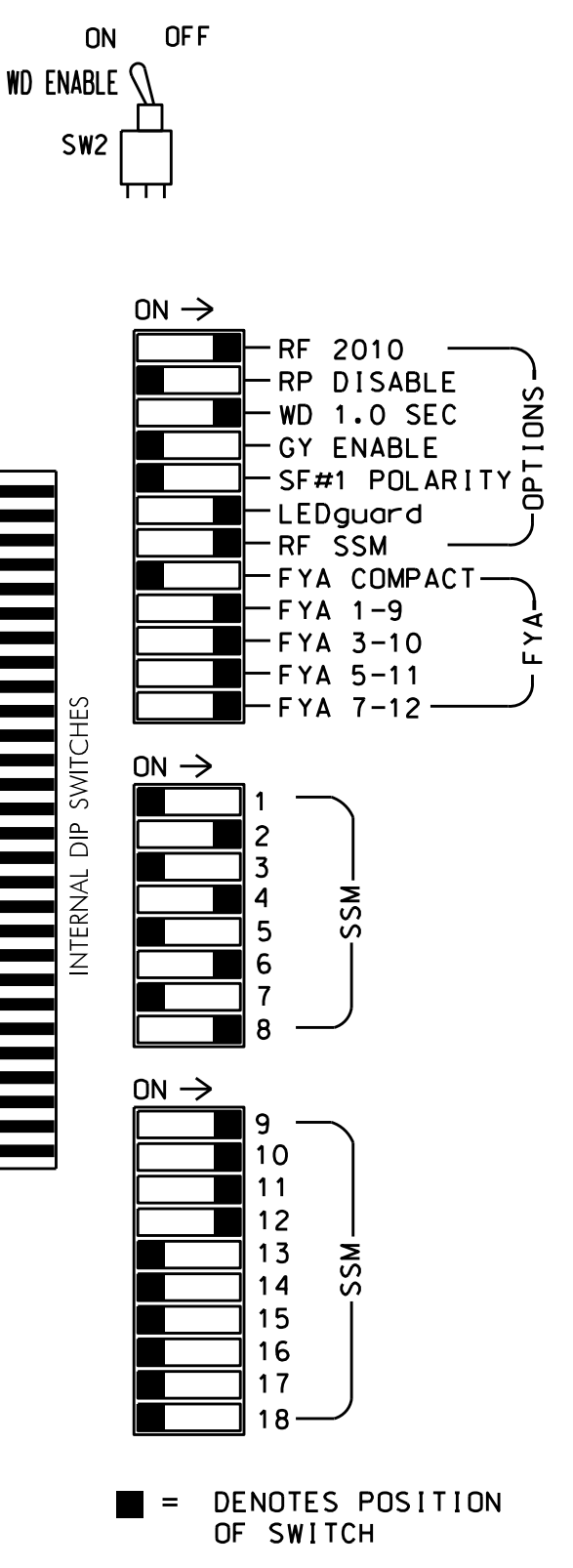
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 8-10, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 10-12, 10-14, 10-16, 11-13, 11-15, 12-14, 12-16, 13-15, AND 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all Phases.
4. Program phases 2 and 6 for Variable Initial and Gap Reduction.
5. Program phases 2 and 6 for Startup In Green.
6. Program phases 2, 4, 6, and 8 for Startup Ped Call.
7. Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
8. The cabinet and controller are part of the High Point Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S9,S11,S12, AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,2PED,4,4PED,5,6,6PED,8,8PED
 OVERLAP "A".....1+2
 OVERLAP "B".....4
 OVERLAP "C".....5+6
 OVERLAP "D".....8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CNU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED SPECIAL EVENT FLASHER	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22,23	P21,P22	101	NU	42,43,44	P41,P42	51	61,62,63	P61,P62	NU	82,83,84	P81,P82	11	81	NU	51	41	
RED		128				101			134			107							
YELLOW	*	129				102		*	135			108							
GREEN		130				103			136			109							
RED ARROW																A121	A124	A114	A101
YELLOW ARROW																A122	A125	A115	A102
FLASHING YELLOW ARROW																A123	A126	A116	A103
GREEN ARROW	127								133										
HAND			113				104		119			110							
PED YELLOW				**	114														
WALKER			115				106		121			112							

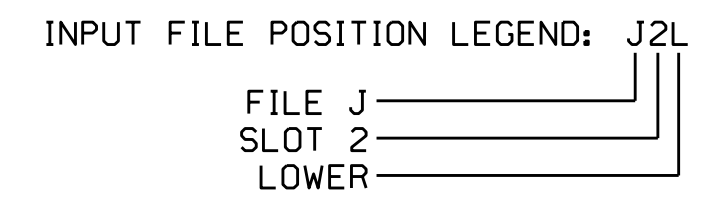
NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ** S3-Y is used for the Special Events Flasher. See sheet 2 for wiring and programming detail.
 ★ See pictorial of head wiring in detail this sheet.

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y	-	-	15
	-	J4U	48	10★	26	6	Y	Y	Y	-	3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y	-	-	-
	-	I1U	56	18★	51	1	Y	Y	-	-	3
2B	TB2-7,8	I2L	43	5	12	2	Y	Y	-	-	-
2C	TB2-9,10	I3U	63	25	32	2	Y	Y	-	-	-
4A	TB4-9,10	I6U	41	3	4	4	Y	Y	-	-	3
4B	TB4-11,12	I6L	45	7	14	4	Y	Y	-	-	10
* S1	TB6-9,10	I9U	60	22	11	SYS	-	-	-	-	-
* S2	TB6-11,12	I9L	62	24	13	SYS	-	-	-	-	-
5A ²	TB3-1,2	J1U	55	17	5	5	Y	Y	-	-	15
	-	I4U	47	9★	22	2	Y	Y	Y	-	3
6A	TB3-5,6	J2U	40	2	6	6	Y	Y	-	-	-
	TB3-7,8	J2L	44	6	16	6	Y	Y	-	-	-
6C	TB3-9,10	J3U	64	26	36	6	Y	Y	-	-	-
8A	TB5-9,10	J6U	42	4	8	8	Y	Y	-	-	3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y	-	-	10
* S3	TB7-9,10	J9U	59	21	15	SYS	-	-	-	-	-
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

¹Add jumper from I1-W to J4-W, on rear of input file.
²Add jumper from J1-W to I4-W, on rear of input file.

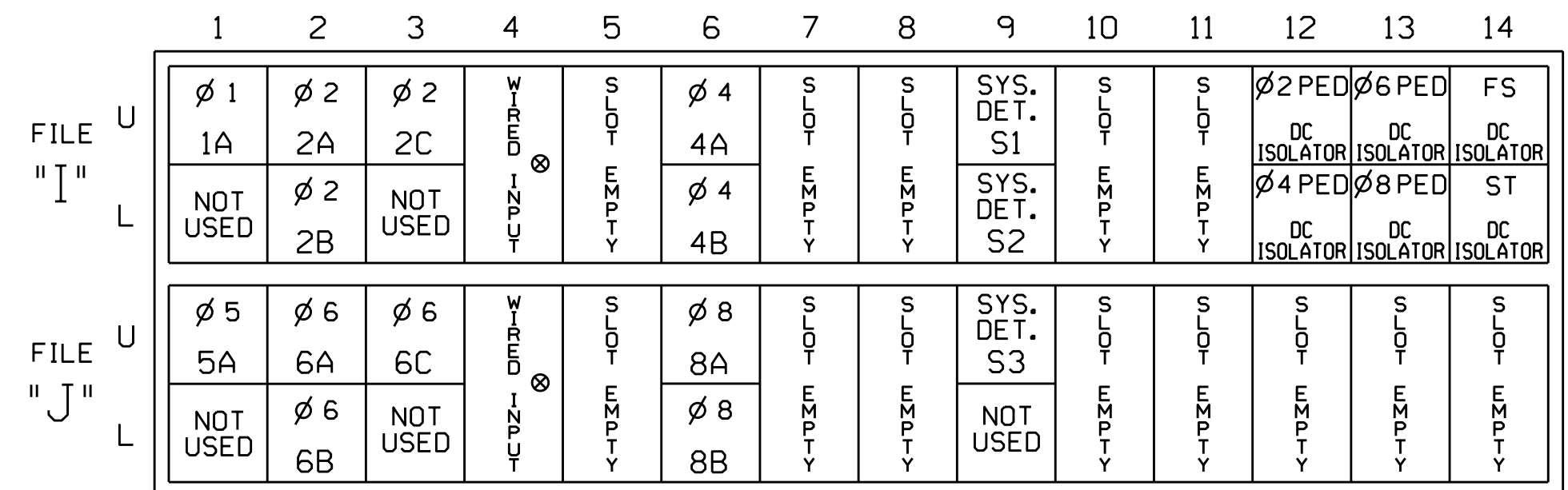
★ See Input Page Assignment programming details on sheets 4 and 5.
 * System detector only. Remove the vehicle phase assigned to this detector in the default programming.



NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

INPUT FILE POSITION LAYOUT

(front view)



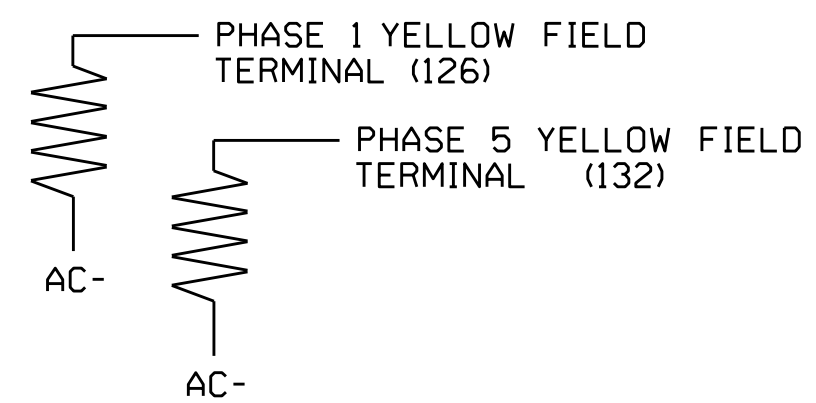
EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



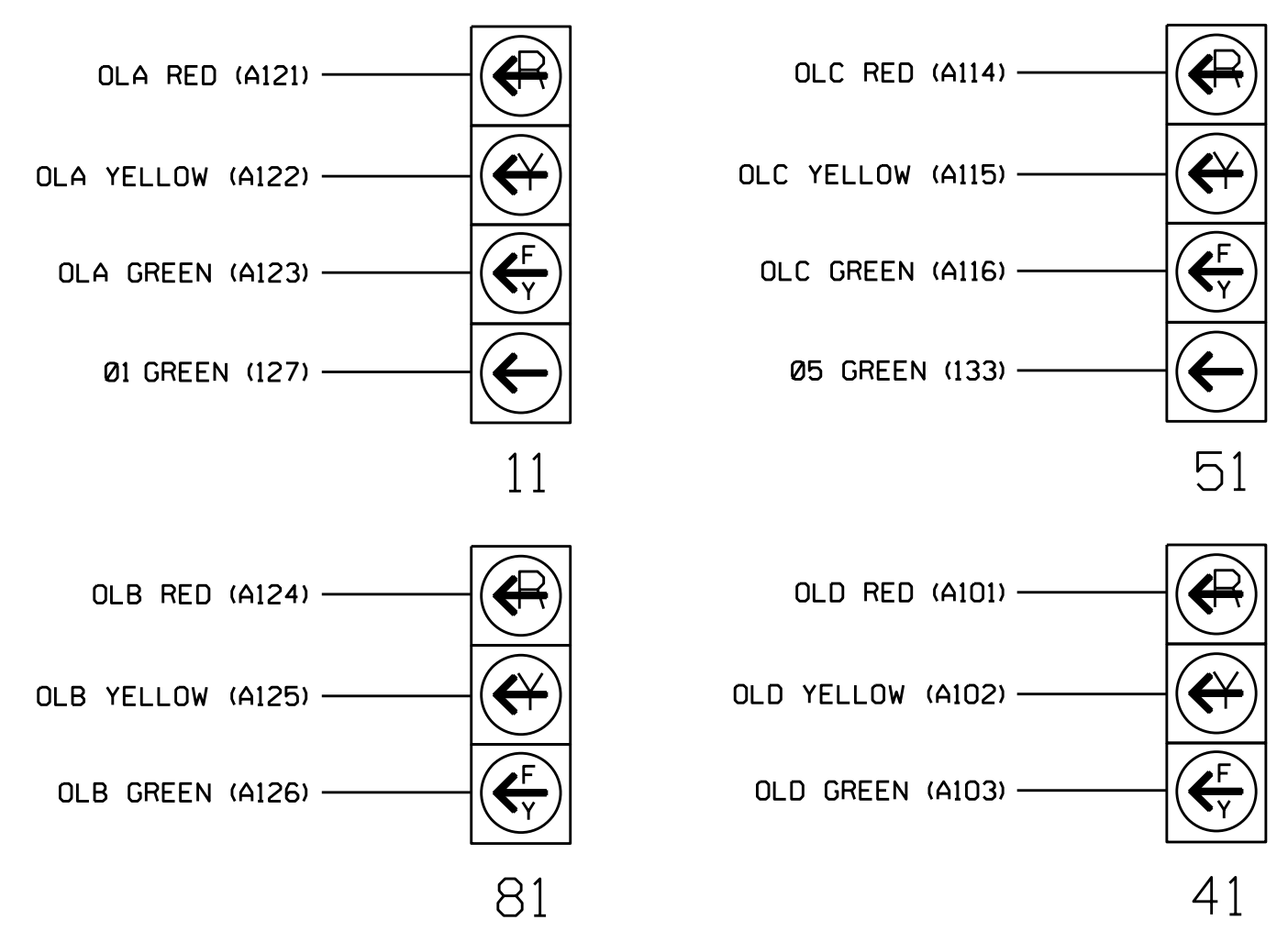
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1470
 DESIGNED: May 2018
 SEALED: May 20, 2018
 REVISED: N/A

Project #: 170908

 HOME OFFICE:
 119 BROOKSTOWN AVENUE, SUITE PH1
 WINSTON-SALEM, NC 27101
 336.744.1636 www.davenportworld.com
 NCBELS FIRM LICENSE NO. C-2522

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



NOTE
 The sequence display for signal heads 11 and 51 requires special logic programming. See sheet 3 for programming instructions.

Electrical Detail - Final Design - Sheet 1 of 6

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

Prepared for:

 NC 68 (Eastchester Dr.)
 at
 Cypress Court

Division 7 Guilford County High Point

PLAN DATE: May 2018 REVIEWED BY: R Hinshaw
 PREPARED BY: L Boyer REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by:

 05/20/2018
 DATE

SIG. INVENTORY NO. 07-1470