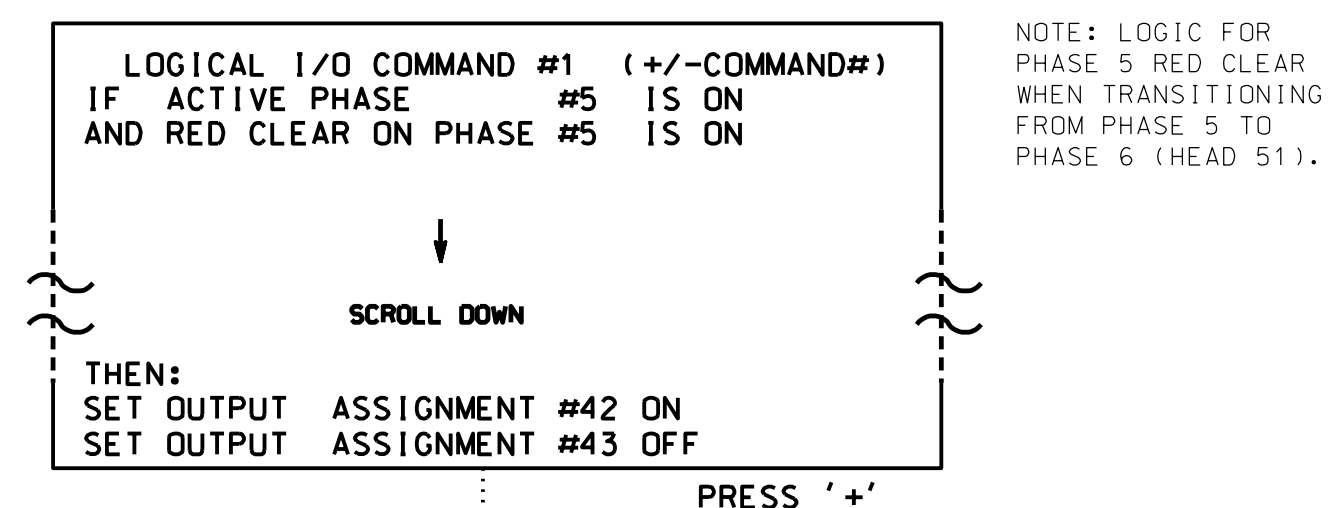


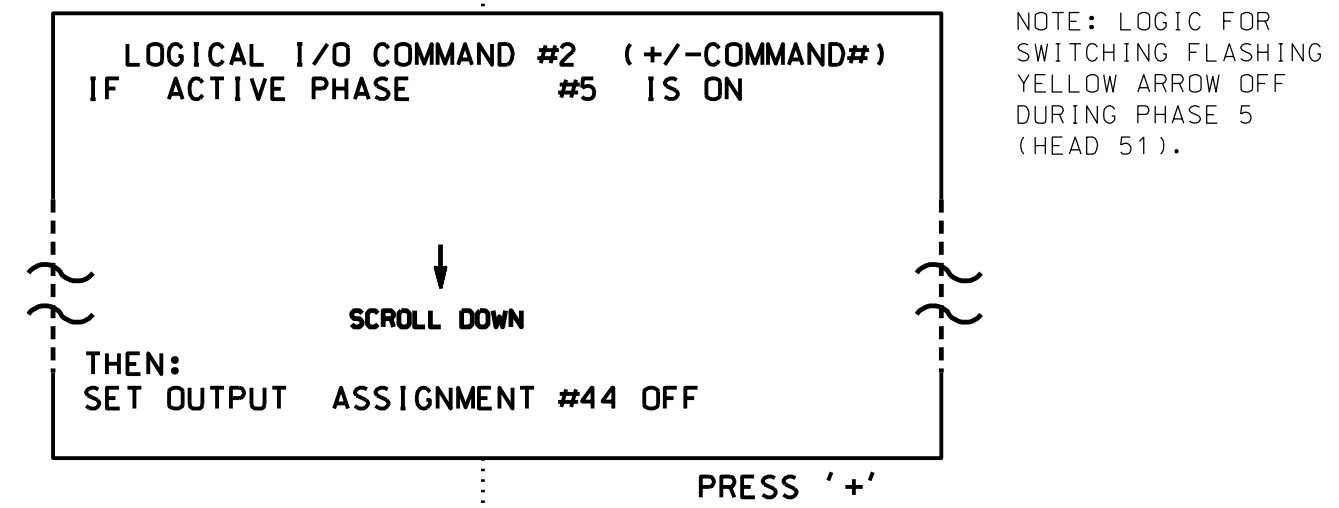
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

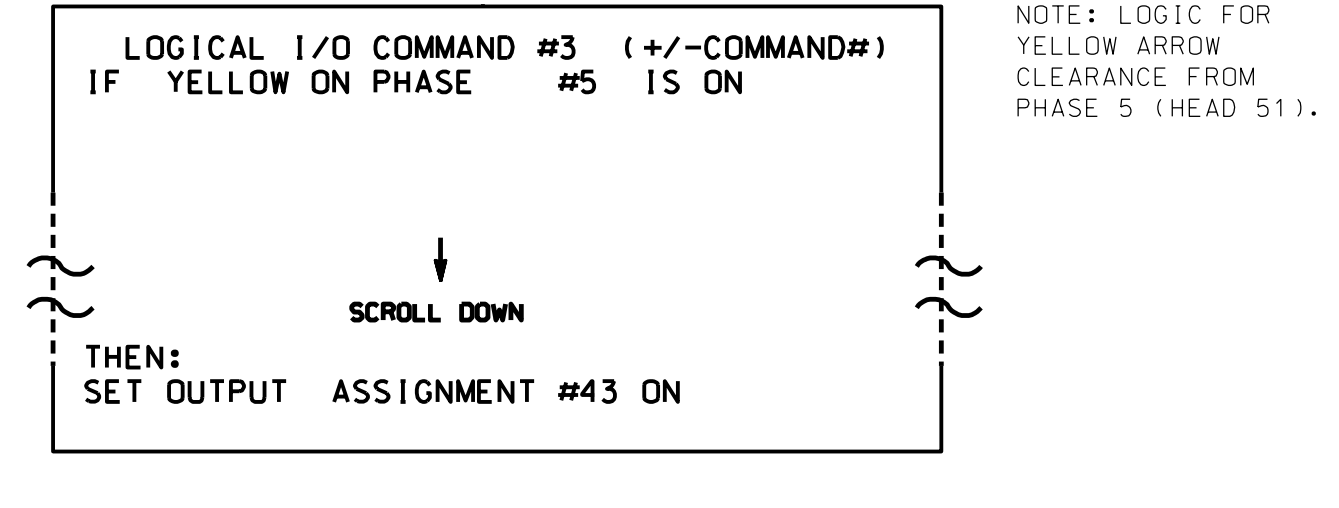
- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEAD 51).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

REFERENCE SCHEDULE
 USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red
 OUTPUT 43 = Overlap C Yellow
 OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS:  XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR:  _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-1623T2
 DESIGNED: May 2018
 SEALED: May 18, 2018
 REVISED: N/A

Project #: 170908



Temporary Design 2; TMP-14
 Electrical Detail Sheet 2 of 2

	<p>NC 68 (Eastchester Drive) at I-74 WB/ US 311 NB Ramps</p>	<p>SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 032117 ROYAL HINSHAW</p>
	<p>Division 7 Guilford County High Point</p> <p>PLAN DATE: May 2018 REVIEWED BY: L. Boyer</p> <p>PREPARED BY: A. Ravipati REVIEWED BY: R. Hinshaw</p>	<p>DocuSigned by: 05/18/2018 Signature: <i>Royal Hinshaw</i> DATE: _____ SIG. INVENTORY NO. 07-1623T2</p>

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED