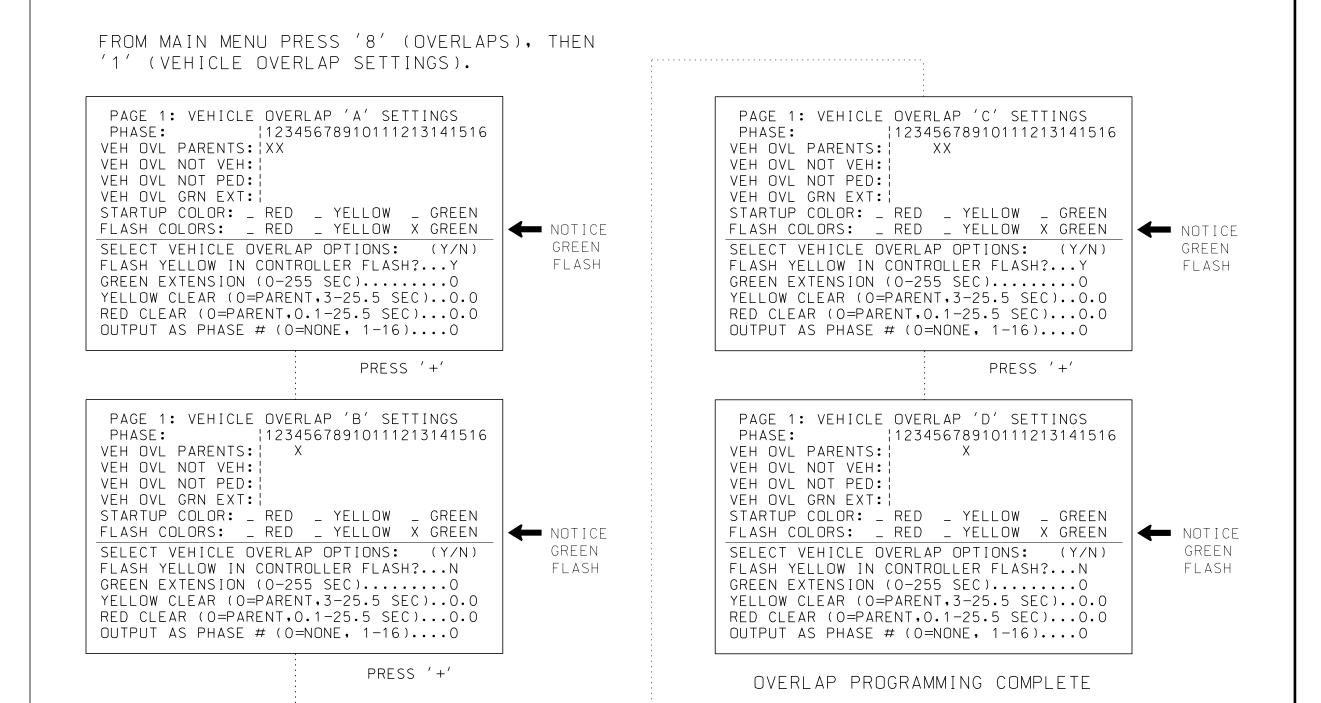
PROJECT REFERENCE NO. | Sig. 9.2 U-2412A

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

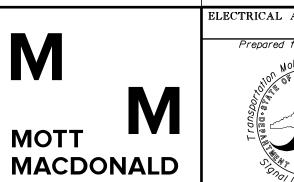


FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.
- THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø7-1793 DESIGNED: February 2018 SEALED: 3/20/2018 REVISED: _____ Electrical Detail - Sheet 2 of 2



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DETAILS FOR:

Prepared for the Offices of:

750 N.Greenfield Pkwy,Garner,NC 27529

SR 4121 (JAMESTOWN PARKWAY) SR 1334 (DILLON ROAD)

IVISION 7 GUILFORD COUNTY HIGH POINT PLAN DATE: February 2018 | REVIEWED BY: T. Pate REPARED BY: B. LEHAN REVIEWED BY: R. Thompson REVISIONS

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL
SIGNATURES COMPLETED 045256 Brendan Lehan 3/20/2018

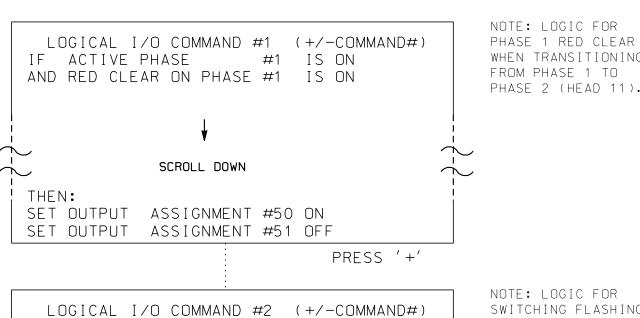
SIG. INVENTORY NO. 07-1793

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

#1 IS ON



IF ACTIVE PHASE

IF ACTIVE PHASE

IF ACTIVE PHASE

WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

NOTE: LOGIC FOR

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEAD 11).

PRESS '+' LOGICAL I/O COMMAND #3 (+/-COMMAND#) IF YELLOW ON PHASE #1 IS ON SCROLL DOWN

SCROLL DOWN

SET OUTPUT ASSIGNMENT #52 OFF

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

SET OUTPUT ASSIGNMENT #51 ON PRESS '+'

LOGICAL I/O COMMAND #4 (+/-COMMAND#)

AND RED CLEAR ON PHASE #5 IS ON

#5 IS ON

#5 IS ON

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

SCROLL DOWN SET OUTPUT ASSIGNMENT #42 ON SET OUTPUT ASSIGNMENT #43 OFF PRESS '+'

LOGICAL I/O COMMAND #5 (+/-COMMAND#)

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEAD 51).

SCROLL DOWN SET OUTPUT ASSIGNMENT #44 OFF PRESS '+' LOGICAL I/O COMMAND #6 (+/-COMMAND#)

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

IF YELLOW ON PHASE #5 IS ON SCROLL DOWN SET OUTPUT ASSIGNMENT #43 ON

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green