## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

## TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

NOTE: LOGIC FOR

FROM PHASE 1 TO

PHASE 1 RED CLEAR

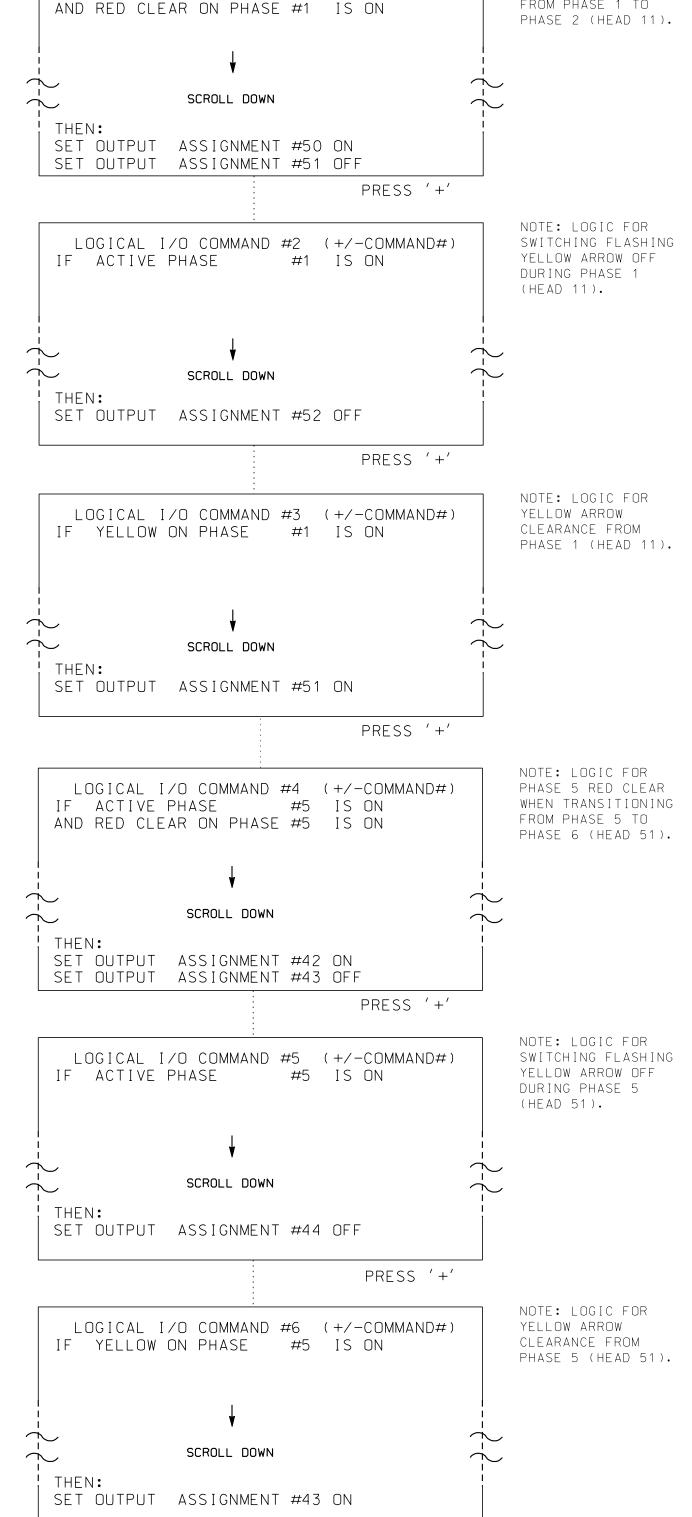
WHEN TRANSITIONING

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

#1 IS ON

LOGICAL I/O COMMAND #1 (+/-COMMAND#)

IF ACTIVE PHASE



**OUTPUT REFERENCE SCHEDULE** USE TO INTERPRET LOGIC PROCESSOR

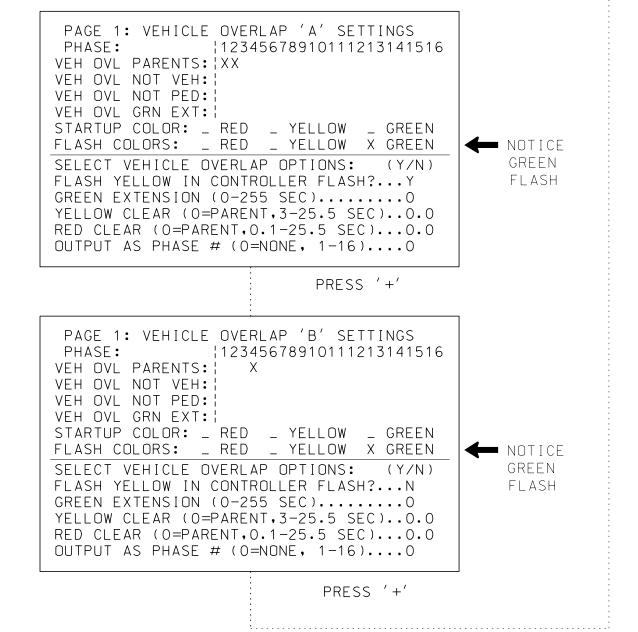
OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow

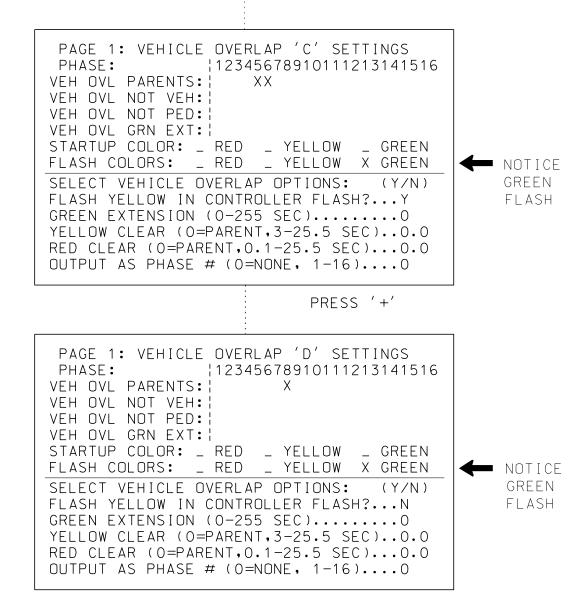
OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).





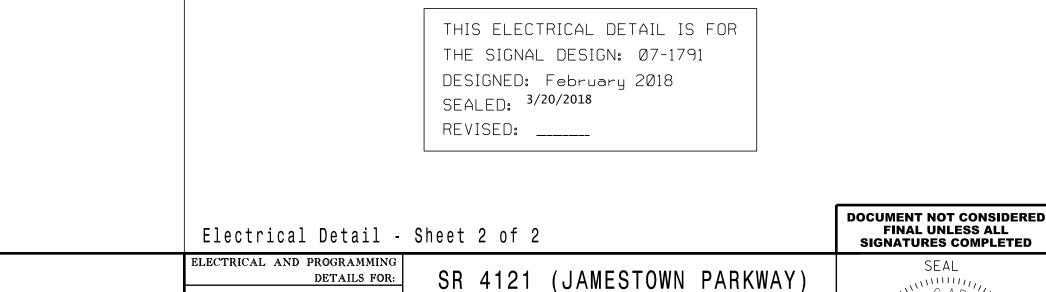
OVERLAP PROGRAMMING COMPLETE

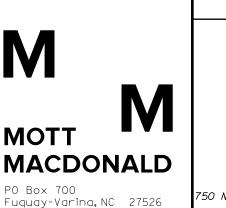
## FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.





Prepared for the Offices of: MANOR DRIVE DIVISION 7 GUILFORD COUNTY

HIGH POINT PLAN DATE: February 2018 | REVIEWED BY: T. Pate PREPARED BY: B. LEHAN REVIEWED BY: R. Thompson REVISIONS

045256 INIT. DATE Brendan Lehan 3/20/2018 SIG. INVENTORY NO. 07-1791

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

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