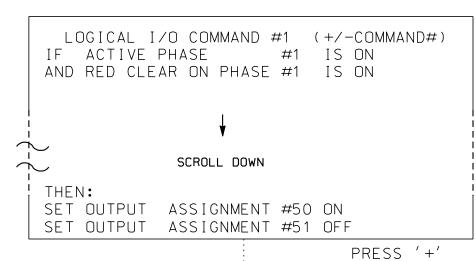
## PROJECT REFERENCE NO. Sig. 5.2 U-2412A

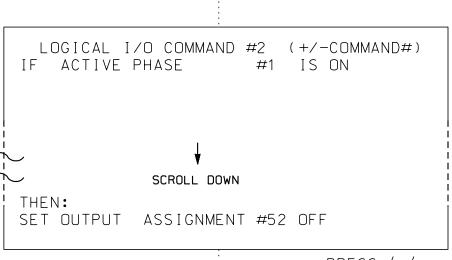
## LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

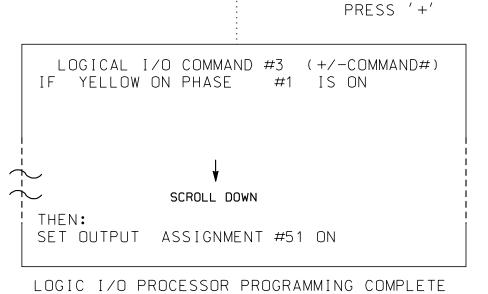
- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).



NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEAD 11).



NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

## **OUTPUT REFERENCE SCHEDULE**

OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

## OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS 12345678910111213141516 VEH OVL PARENTS: XX VEH OVL NOT VEH: | VEH OVL NOT PED: | VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN NOTICE GREEN FLASH FLASH COLORS: \_ RED \_ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø7-1272 DESIGNED: February 2018 SEALED: 3/20/2018 REVISED: \_\_\_\_\_

**MOTT MACDONALD** PO Box 700 Fuquay-Varina,NC 27526 www.mottmac.com License No.F-0669

Electrical Detail - Sheet 2 of 2 ELECTRICAL AND PROGRAMMING Prepared for the Offices of:

750 N.Greenfield Pkwy,Garner,NC 27529

SR 1486 (Greensboro Road) Enterprise Dr./Spencer Street

Division 7 Guilford County PLAN DATE: February 2018 REVIEWED BY: T. PATE PREPARED BY: B. LEHAN REVIEWED BY: R. THOMPSON REVISIONS

High Point Herendan Lehan 3/20/2018 SIG. INVENTORY NO. 07-1272

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED