LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, AND 5.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1   (+/-COMMAND#)
IF   ACTIVE PHASE      #5   IS ON
AND RED CLEAR ON PHASE #5   IS ON

              ↓
         SCROLL DOWN

THEN:
SET OUTPUT   ASSIGNMENT #42 ON
SET OUTPUT   ASSIGNMENT #43 OFF
```
NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #2   (+/-COMMAND#)
IF   ACTIVE PHASE      #5   IS ON

              ↓
         SCROLL DOWN

THEN:
SET OUTPUT   ASSIGNMENT #44 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #3   (+/-COMMAND#)
IF   YELLOW ON PHASE   #5   IS ON

              ↓
         SCROLL DOWN

THEN:
SET OUTPUT   ASSIGNMENT #43 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

```
LOGICAL I/O COMMAND #4   (+/-COMMAND#)
IF   INPUT     ASSIGNMENT #1 IS ON

              ↓
         SCROLL DOWN

THEN:
SET INPUT    ASSIGNMENT #64 OFF
```
NOTE: DETECTOR CONTACTS ARE CLOSED. SO NO CALL IS 'DETECTED'.

PRESS '+'

```
LOGICAL I/O COMMAND #5   (+/-COMMAND#)
IF   INPUT     ASSIGNMENT #1 IS OFF

              ↓
         SCROLL DOWN

THEN:
SET INPUT    ASSIGNMENT #64 ON
```
NOTE: DETECTOR CONTACTS ARE OPEN. SO A CALL IS 'DETECTED'.

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

```
INPUT    1 = Detector Physical Input (Not Enabled)
INPUT   64 = Dummy Detector Input (Detector 2)
OUTPUT  42 = Overlap C Red
OUTPUT  43 = Overlap C Yellow
OUTPUT  44 = Overlap C Green
```

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+' TWICE

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
 PHASE:           |1234567891011121314151 6
VEH OVL PARENTS: |    XX
VEH OVL NOT VEH: |
VEH OVL NOT PED: |
VEH OVL GRN EXT: |
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN      ← NOTICE GREEN FLASH
SELECT VEHICLE OVERLAP OPTIONS:  (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

OVERLAP PROGRAMMING COMPLETE

```
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1592
DESIGNED: February 2018
SEALED:  3/20/2018
REVISED: _____
```
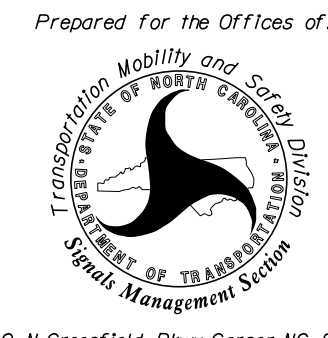
Electrical Detail - Sheet 2 of 3

MOTT MACDONALD

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ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared for the Offices of:
Transportation Mobility and Safety Division
DEPARTMENT OF TRANSPORTATION
Signals Management System Support

750 N.Greenfield Pkwy,Garner,NC 27529

**SR 1486 (Greensboro Road)
at
I-74 WB/US 311 NB Ramps**
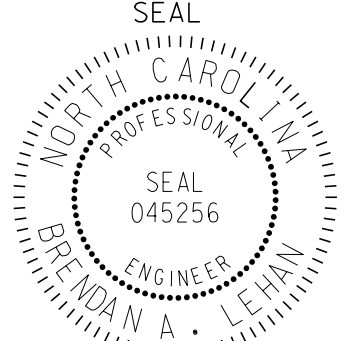
Division 7	Guilford County	High Point
PLAN DATE: February 2018	REVIEWED BY: T. PATE	
PREPARED BY: B. LEHAN	REVIEWED BY: R. THOMPSON	

REVISIONS

SEAL

NORTH CAROLINA
SEAL
045256
ENGINEER
BRENDAN A. LEHAN

Brendan Lehan   3/20/2018
DocuSigned by:
B8170B2F7B54C0...   DATE

SIG. INVENTORY NO. 07-1592