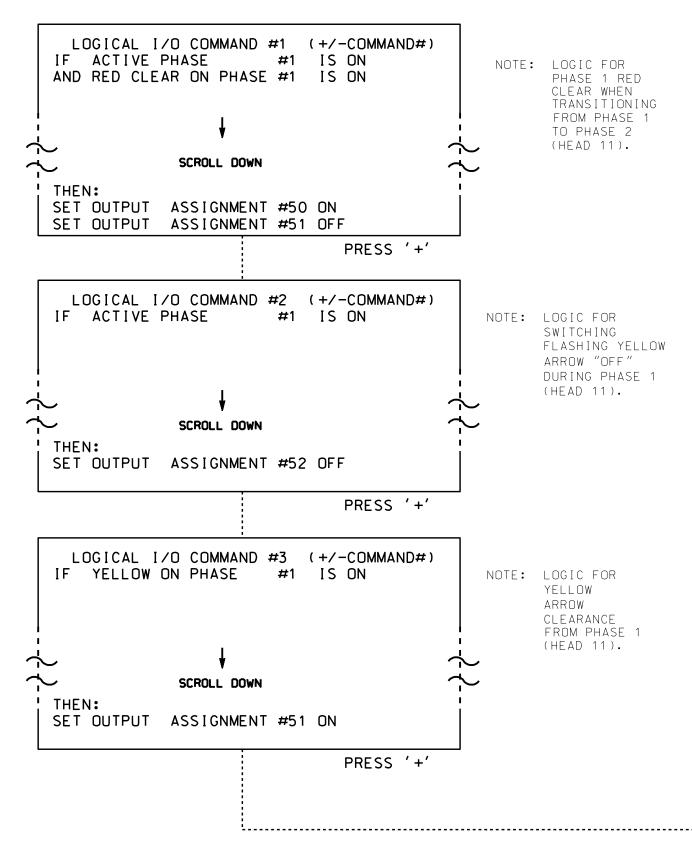
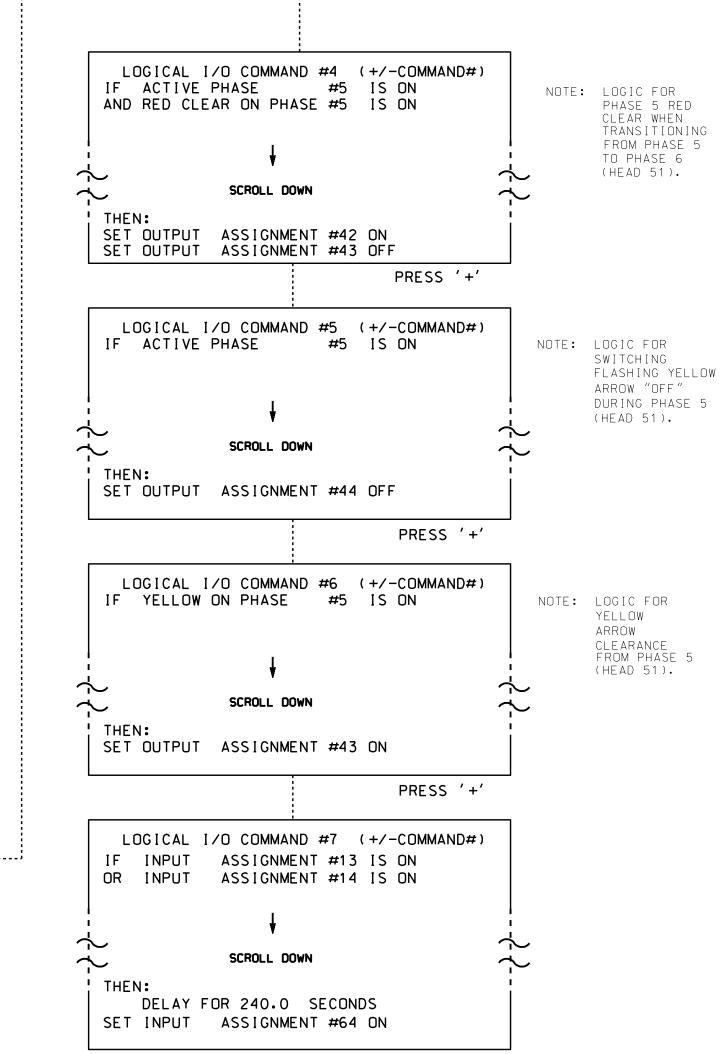
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, AND 7.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).





LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

LO REFERENCE SCHEDULE

INPUT 13 = Input from LVODS #1
INPUT 14 = Input from LVODS #2
INPUT 64 = Preempt 7
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow

OUTPUT 52 = Overlap A Green

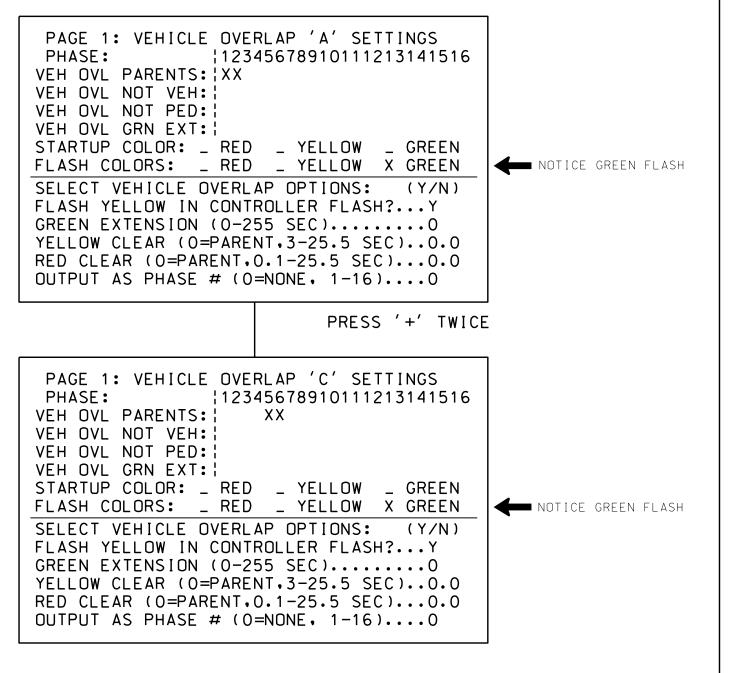
INPUTS 13, 14, AND 64 HAVE BEEN REASSIGNED.
SEE SHEET 6 FOR PROGRAMMING DETAILS.

OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

OVERLAP PROGRAMMING COMPLETE



OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

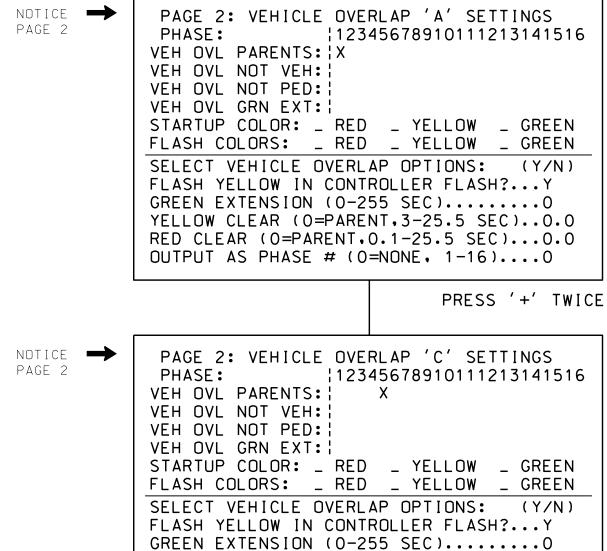
PROJECT REFERENCE NO.

B-5351

Sig. 6.2

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS).
THEN '1' (VEHICLE OVERLAP SETTINGS).
PRESS 'NEXT' TO ADVANCE TO PAGE 2.



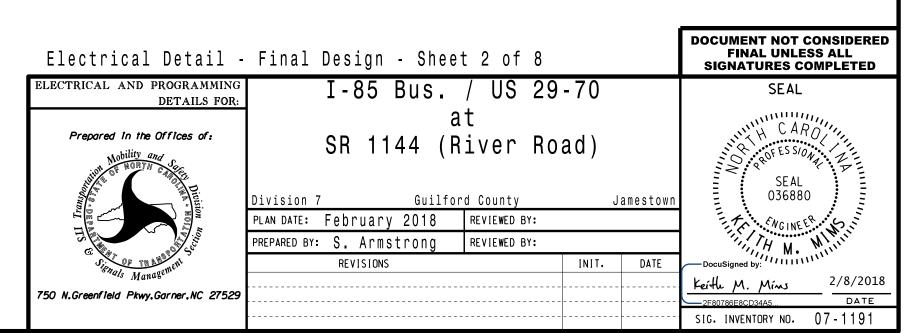
OVERLAP PROGRAMMING COMPLETE

YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0

RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0

OUTPUT AS PHASE # (O=NONE, 1-16)....0

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1191
DESIGNED: January 2018
SEALED: 2/7/2018
REVISED: N/A



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