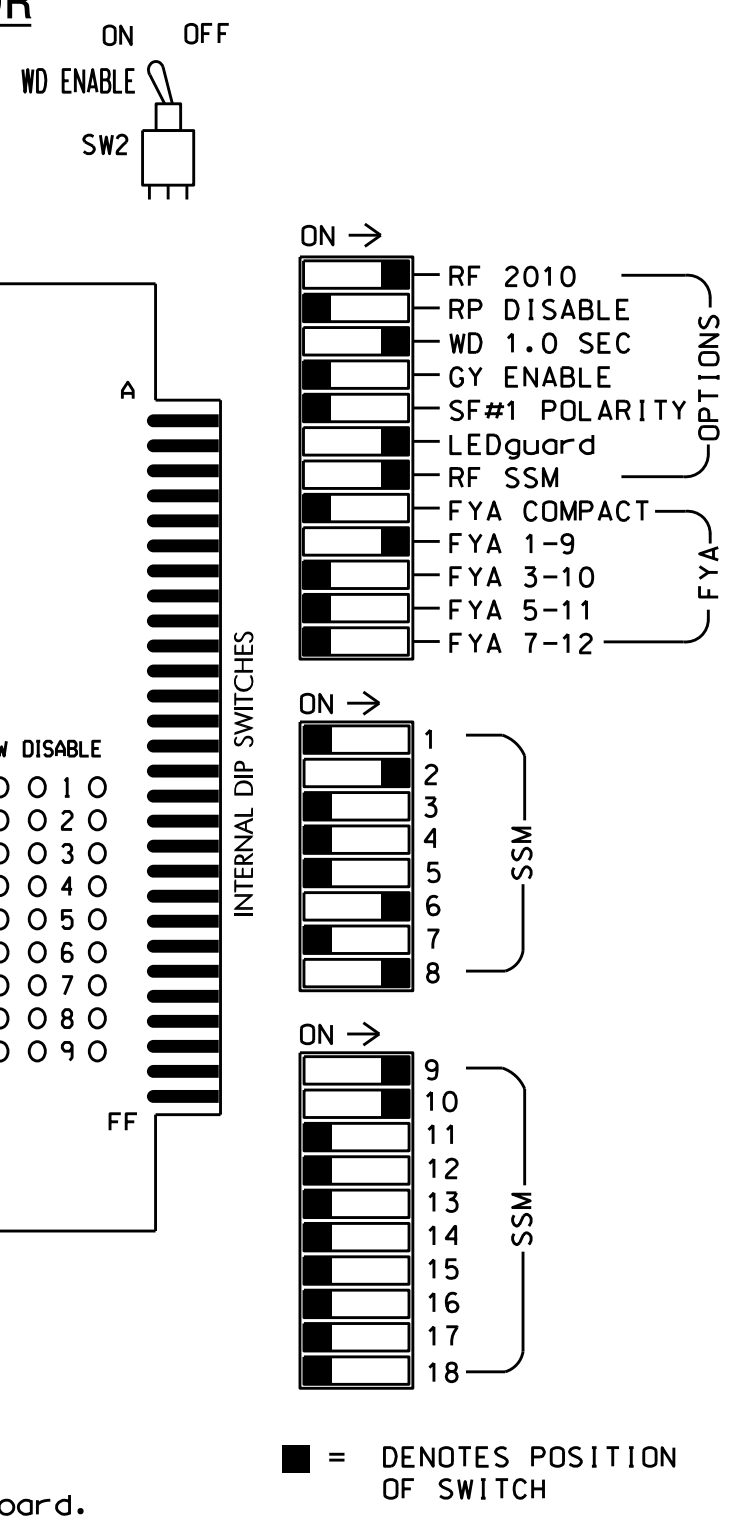
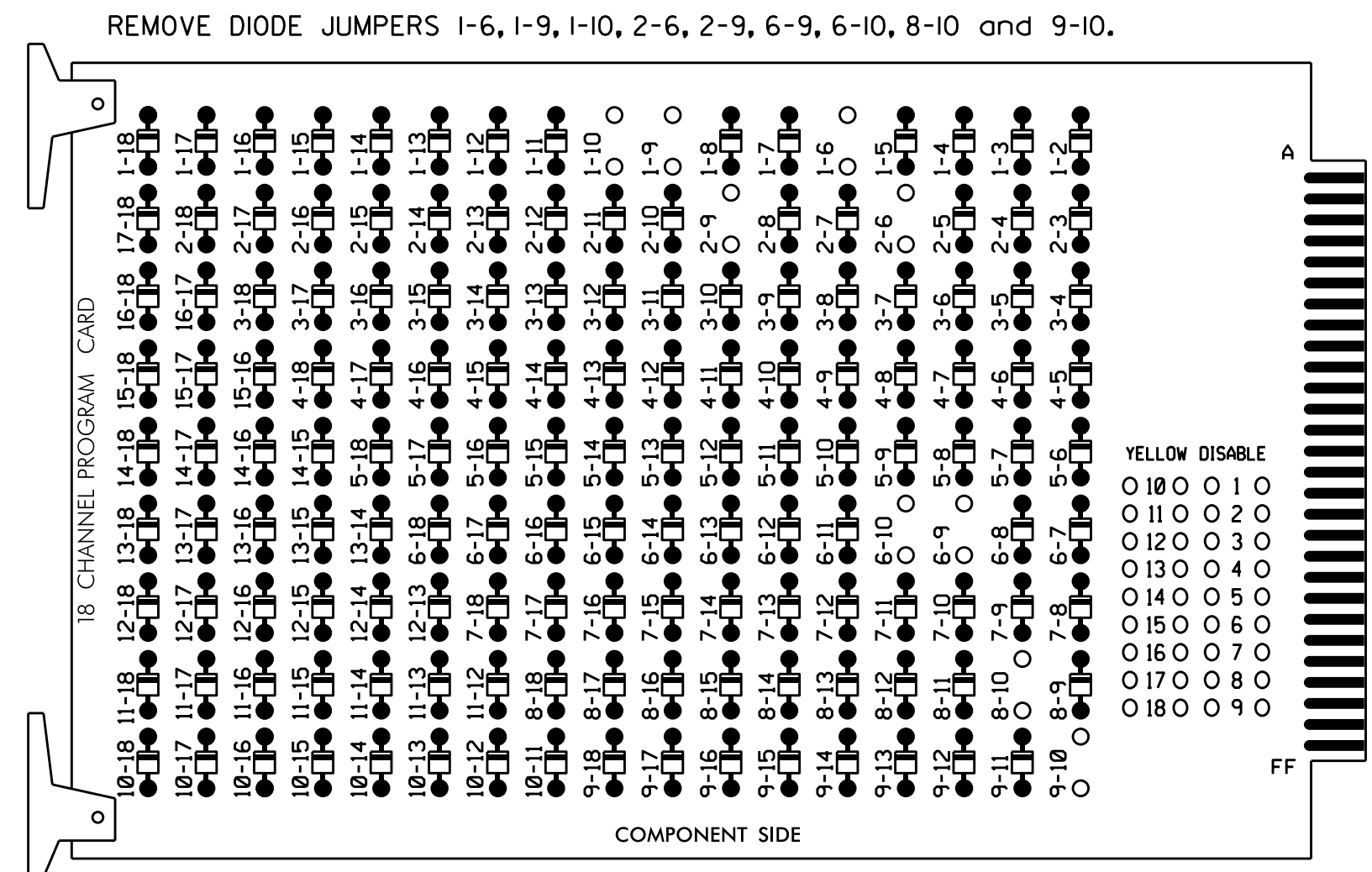


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL
(remove jumpers and set switches as shown)



- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
 - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
 - Ensure that Red Enable is active at all times during normal operation.
 - Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the NC 150 Kernersville Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S1,AUX S2
 PHASES USED.....1,2,6,8
 OVERLAP "A".....1+2
 OVERLAP "B".....1+8
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22,23	NU	NU	NU	NU	NU	61,62,63	NU	NU	22	81,82	11	82	NU	NU	NU	NU
RED		128						134			107			*				
YELLOW	*	129						135										
GREEN		130						136										
RED ARROW																A121		
YELLOW ARROW											108	108				A122	A125	
FLASHING YELLOW ARROW																		A123
GREEN ARROW	127										109	109						A126

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT
(front view)

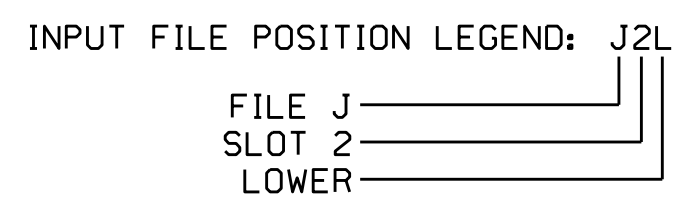
FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 1	∅ 2/SYS	S	S	S	S	S	S	S	S	S	S	FS
L	1A	1B	2A/S1	-	-	-	-	-	-	-	-	-	-	DC ISOLATOR
U	NOT USED	∅ 1	NOT USED	S	S	S	S	S	S	S	S	S	S	ST
L		1C		-	-	-	-	-	-	-	-	-	-	DC ISOLATOR
U	S	∅ 6/SYS	S	S	S	S	S	S	S	S	S	S	S	S
L	-	6A/S2	-	-	-	-	-	-	-	-	-	-	-	-
		NOT USED												

EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME
 ⊗ Wired Input - Do not populate slot with detector card

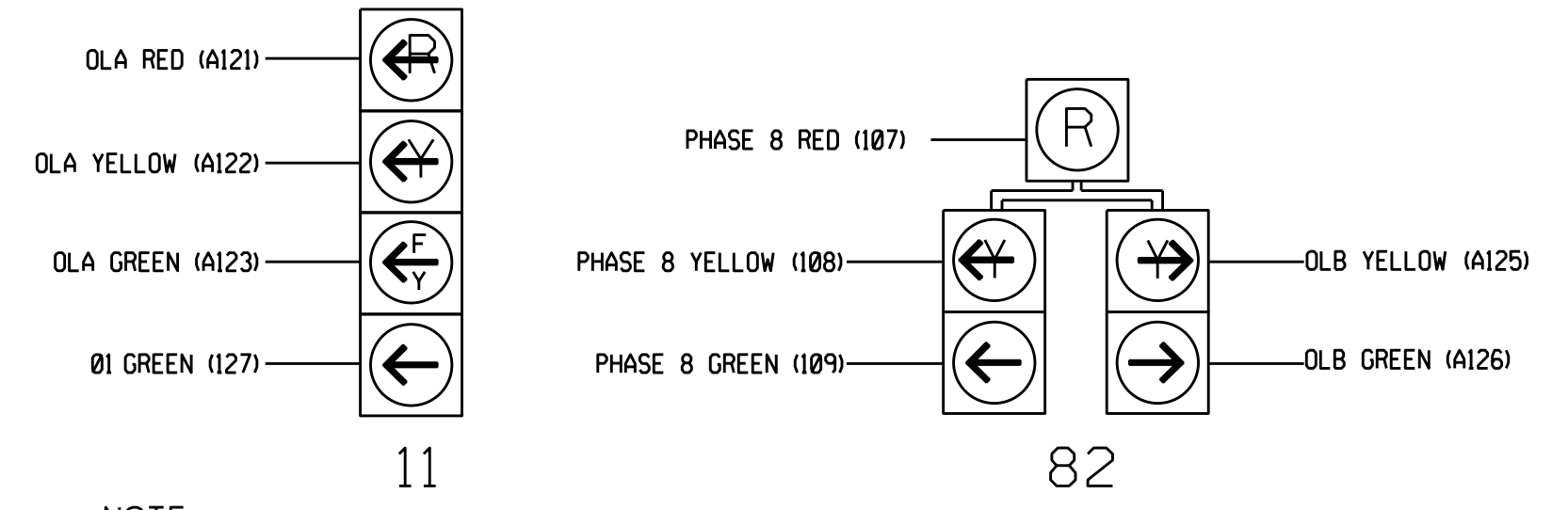
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
		I1U	56	18★	51	1	Y	Y			
1B	TB2-5,6	I2U	39	1	2	1	Y	Y			15
1C	TB2-7,8	I2L	43	5	12	1	Y	Y			15
2A/S1	TB2-9,10	I3U	63	25	32	2/SYS	Y	Y			
6A/S2	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			

¹Add jumper from I1-W to J4-W, on rear of input file.
 ★ See Input Page Assignment programming details on sheet 3.

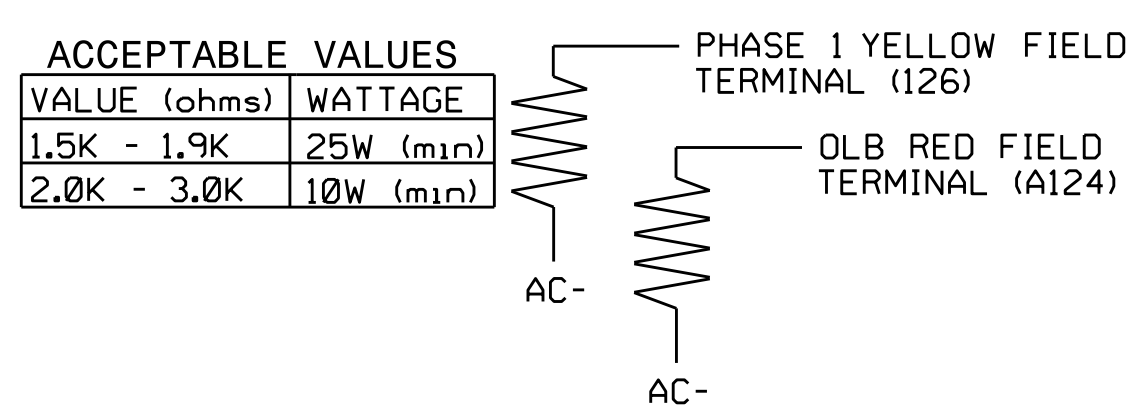


FYA SIGNAL WIRING DETAIL
(wire signal heads as shown)



NOTE
 The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL
(install resistors as shown below)



VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)

Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

Prepared In the Offices of:
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NC 150 (N. Main St.) at SR 2601 (Macy Grove Road)

Division 9 Forsyth County Kernersville

PLAN DATE: January 2018 REVIEWED BY:
 PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: Keith M. Minis 3/5/2018

SIG. INVENTORY NO. 09-1128

09-0458-2018_07-15
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 J.peterson