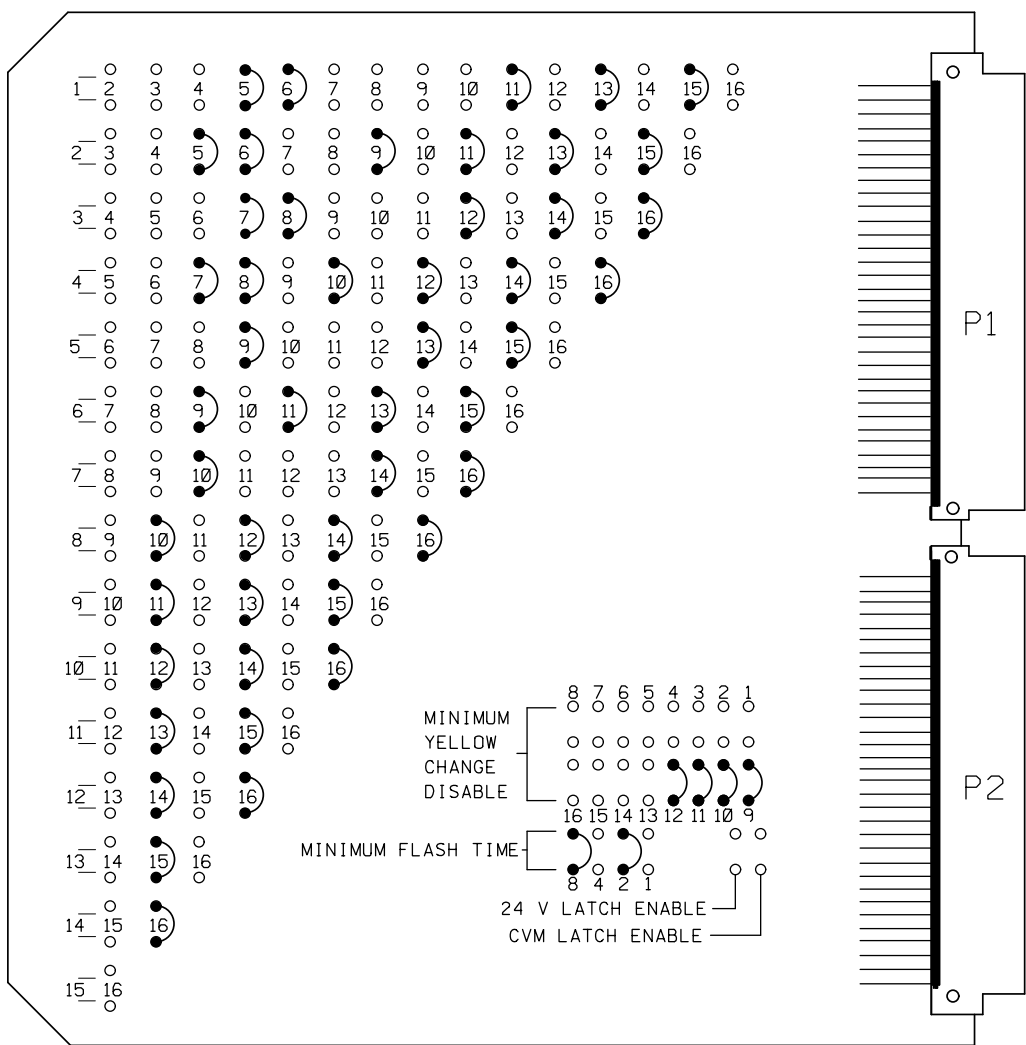


EDI MODEL MMU2-16LEip MALFUNCTION MANAGEMENT UNIT PROGRAMMING DETAIL *(program card and tables as shown)*



MMU PROGRAMMING CARD

CHANNEL NUMBER	ENABLE/DISABLE
1	DISABLE
2	ENABLE
3	ENABLE
4	ENABLE
5	DISABLE
6	ENABLE
7	ENABLE
8	ENABLE
9	ENABLE
10	ENABLE
11	ENABLE
12	ENABLE
13	ENABLE
14	ENABLE
15	ENABLE
16	ENABLE

MMU PROGRAMMING NOTE
ENSURE YELLOW CHANGE PLUS RED CLEARANCE MONITORING IS ENABLED FOR ALL CHANNELS.

OPTION	SETTING
RECURRENT PULSE	ON
WALK DISABLE	OFF
LOG CVM FAULTS	ON
EXTERN WATCHDOG	OFF
24V-2=12VDC	OFF
PGM CARD MEMORY	ON
LEDguard	ON
FORCE TYPE 16	OFF
TYPE12-SDL	OFF
VM 3x/Day Latch	ON

CONFIG MODE	SETTING
ENABLE CHANNEL PAIR, FYA	B
CH 1-13	ON
CH 3-14	ON
CH 5-15	ON
CH 7-16	ON
RED/YEL INPUT ENABLE	
CH 1	ON
CH 3	ON
CH 5	ON
CH 7	ON
FLASH RATE FAULT	ON
FYA TRAP DETECT	ON

NOTES

- To prevent "flash-conflict" problems, wire all unused load switches to flash red. Verify that signal heads flash in accordance with the signal plans.
- Make sure all flash transfer relays are in place.
- Program controller to start up in phase 2 Walk and 6 Walk.
- Set power-up flash time to 10 seconds and implement on the Malfunction Management Unit. Set controller power-up flash time to 0 seconds.
- Enable simultaneous gap-out feature for all phases.
- Program detectors in accordance with the manufacturer's instructions to accomplish the detection schemes shown on the signal design plans.
- Program detector call delay and extension timing on the controller, unless otherwise specified.
- Set all detector card unit channels to "presence" mode.
- Program phases 2 and 6 for volume density operation.
- Program phases 4 and 8 for dual entry.
- The cabinet and controller are a part of the Cary Signal System.

SIGNAL HEAD HOOK-UP CHART

PHASE	1	2	3	4	5	6	7	8	2 PED	4 PED	6 PED	8 PED	OLA	OLB	OLC	OLD		
SIGNAL HEAD NO.	11★	21,22	22	31★	41,42	51★	61,62	62	71★	81,82	P21, P22	P41, P42	P61, P62	P81, P82	11★	31★	51★	71★
RED	*	2R		*	4R	*	6R		*	8R								
YELLOW	*	2Y			4Y	*	6Y			8Y								
GREEN		2G			4G		6G			8G								
RED ARROW															13R	14R	15R	16R
YELLOW ARROW			3Y					7Y							13Y	14Y	15Y	16Y
FLASHING YELLOW ARROW															13G	14G	15G	16G
GREEN ARROW	1G		3G	3G		5G		7G	7G									
Hand icon											9R	10R	11R	12R				
Foot icon											9G	10G	11G	12G				

NU = Not Used
* Denotes install load resistor. See Load Resistor Installation Detail on sheet 3.
★ See pictorial of head wiring detail this sheet.

DETECTOR RACK SET-UP DETAIL

INSERT DETECTOR CARDS IN RACK ACCORDING TO THE DETAIL SHOWN BELOW. PARTICULAR DETECTOR CHANNELS WILL CALL PHASES INDICATED.

RACK #	BIU	CH1	CH1	CH1	CH1	SLOT	CH1	CH1	CH1	SLOT	SLOT	SLOT
RACK #1	BIU	L3	L1	L7	L5	SLOT	L9	L15	L13	SLOT	SLOT	SLOT
		∅2	∅1	∅4	∅3	E M P T Y	∅5	∅7	∅6	E M P T Y	E M P T Y	E M P T Y
RACK #2	BIU	L4	L2	L8	L6	E M P T Y	L10	L16	L14	E M P T Y	E M P T Y	E M P T Y
		∅2	∅6	∅4	∅8	E M P T Y	∅2	∅4	∅6	E M P T Y	E M P T Y	E M P T Y
		L19	L17	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT	SLOT
		∅8	∅8	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y
		L20	L18	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y
		NOT USED	∅8	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y	E M P T Y

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BELOW

ADD JUMPERS FROM:	LOOP NO.	LOOP PANEL TERMINALS
L1A TO L2A, AND L1B TO L2B	1A	L1A, L1B L2A, L2B
L5A TO L6A, AND L5B TO L6B	2A	L3A, L3B L4A, L4B
L9A TO L10A, AND L9B TO L10B	3A	L5A, L5B L6A, L6B
L15A TO L16A, AND L15B TO L16B	7A	L7A, L7B L8A, L8B L9A, L9B L10A, L10B L11A, L11B L12A, L12B L13A, L13B L14A, L14B

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

CONTROLLER DETECTOR NO.	FUNCTION	FEATURE	TIMING TIME (SEC)
1	∅1	DELAY	15
* 2	∅6	DELAY	3
** 3	∅2		
** 4	∅2		
5	∅3	DELAY	15
6	∅8		
7	∅4		
8	∅4	DELAY	10
9	∅5	DELAY	15
* 10	∅2	DELAY	3
11			
12			
** 13	∅6		
** 14	∅6		
15	∅7	DELAY	15
16	∅4		

WIRE LOOPS TO TERMINALS ON LOOP PANEL AS SHOWN IN THE CHART BELOW

LOOP NO.	LOOP PANEL TERMINALS
8A	L17A, L17B
8B	L18A, L18B
8C	L19A, L19B
NU	L20A, L20B
NU	L22A, L22B
NU	L23A, L23B
NU	L24A, L24B
NU	L25A, L25B
NU	L26A, L26B
NU	L27A, L27B
NU	L28A, L28B
NU	L29A, L29B
NU	L30A, L30B
NU	L31A, L31B
NU	L32A, L32B

NOTE
BE SURE TO PROGRAM DETECTOR TYPES AND TIMERS (EXTEND AND DELAY) AS SHOWN ON THE SIGNAL PLANS.

PROGRAM CONTROLLER DETECTORS ACCORDING TO THE SCHEDULE SHOWN IN THE CHART BELOW

CONTROLLER DETECTOR NO.	FUNCTION	FEATURE	TIMING TIME (SEC)
17	∅8		
18	∅8	DELAY	10
19	∅8	DELAY	10
20			
21			
22			
23			
24			
25			
26			
27			
28			
29			
30			
31			
32			

EQUIPMENT INFORMATION

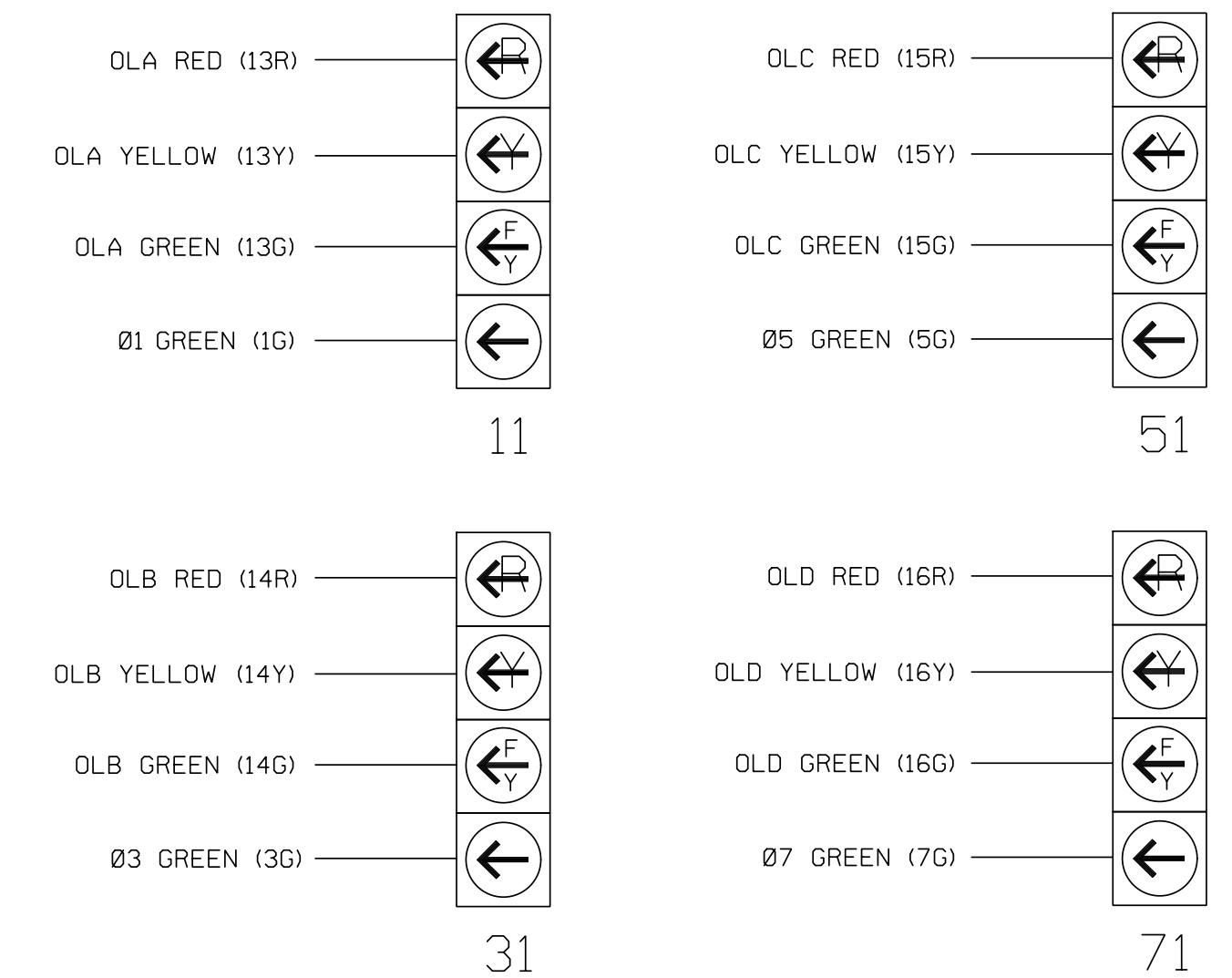
CONTROLLER.....2070LN2
CABINET[TS-2] NC-8
SOFTWAREECONOLITE ASC/3-2070
CABINET MOUNT.....BASE
LOADBAY POSITIONS.....16
LOAD SWITCHES USED.....1,2,3,4,5,6,7,8,9,10,11,12,13,14,15,16
PHASES USED.....1,2,2 PED,3,4,4 PED,5,6,6 PED,7,8,8 PED
OLA.....*
OLB.....*
OLC.....*
OLD.....*

* See overlap programming detail on sheet 2

LOAD SWITCH ASSIGNMENT DETAIL *(program controller according to schedule in chart below)*

LOAD SWITCH NUMBER	FUNCTION
1	∅1
2	∅2
3	∅3
4	∅4
5	∅5
6	∅6
7	∅7
8	∅8
9	∅2 PED
10	∅4 PED
11	∅6 PED
12	∅8 PED
13	OLA
14	OLB
15	OLC
16	OLD

FYA SIGNAL WIRING DETAIL *(wire signal heads as shown)*



NC Dept of Transportation
Division of Highways
Final Drawing Date: 10/10/2016
Disseminated by: [Signature]
ITS & Signals Unit

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 05-2033
DESIGNED: APRIL 2016
SEALED: 9/22/2016
REVISED:

Electrical Detail - Sheet 1 of 3

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared For: [Logo]
SR 3015 (Airport Boulevard) at SR 3127 (McCrimmon Parkway)
Division 5 Wake County Morrisville
PLAN DATE: APRIL 2016 REVIEWED BY: SL PHILLIPS
PREPARED BY: SP PENNINGTON REVIEWED BY:
REVISIONS: INIT. DATE
9/22/2016
SIG. INVENTORY NO. 05-2033

9/22/2016 9:09:44 AM susan.pennington K:\RAL_Roadway\012108004 - MCCrimmon Parkway\Phase 1\WP\ans\signal\sig4 - Signal - Des\ign\02_05-2033_2016\1.dgn