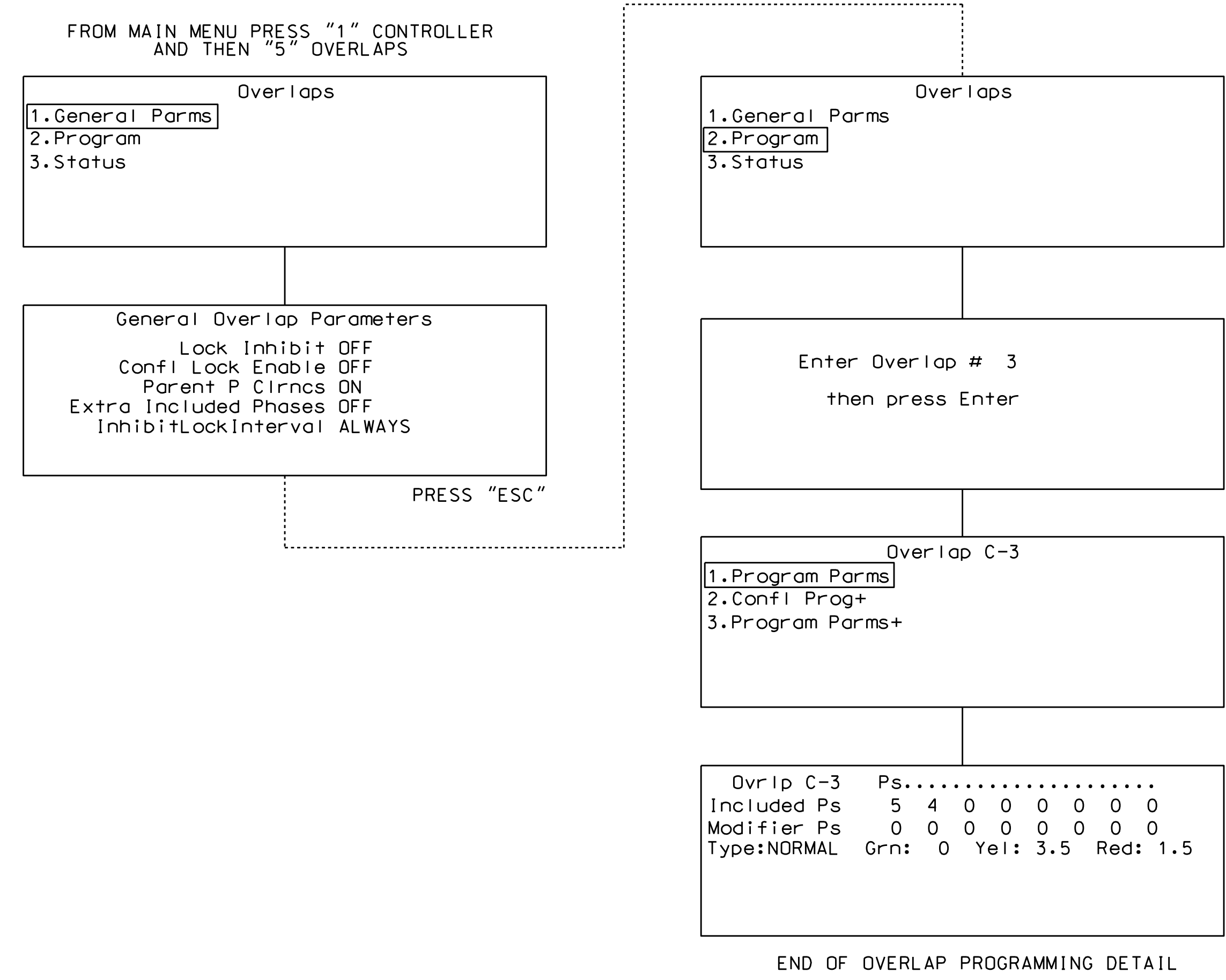## OVERLAP PROGRAMMING DETAIL FOR OVERLAP C*

(program controller as shown below)

*NOTE FOR OVERLAP C: Use Default values for Overlap 'PLUS' programming details.

FROM MAIN MENU PRESS "1" CONTROLLER
AND THEN "5" OVERLAPS

```
         Overlaps
1.General Parms
2.Program
3.Status
```

```
         Overlaps
1.General Parms
2.Program
3.Status
```

```
General Overlap Parameters
     Lock Inhibit OFF
   Confl Lock Enable OFF
    Parent P Clrncs ON
 Extra Included Phases OFF
  InhibitLockInterval ALWAYS
```

PRESS "ESC"

```
Enter Overlap #  3

   then press Enter
```

```
        Overlap C-3
1.Program Parms
2.Confl Prog+
3.Program Parms+
```

```
 Ovrlp C-3   Ps.....................
Included Ps   5  4  0  0  0  0  0  0
Modifier Ps   0  0  0  0  0  0  0  0
Type:NORMAL   Grn:  0  Yel: 3.5  Red: 1.5
```

END OF OVERLAP PROGRAMMING DETAIL
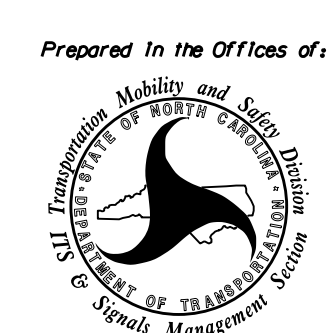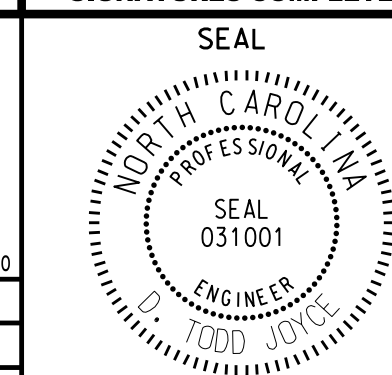
FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE
SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA – REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.

2. ON REAR OF PDA – REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.

3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1721
DESIGNED: September 2017
SEALED: 10/24/2017
REVISED:

Electrical Detail - Sheet 2 of 2

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL

NORTH CAROLINA
PROFESSIONAL
SEAL
031001
ENGINEER
TODD JOYCE

DocuSigned by:
D. Todd Joyce   10/30/2017
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