## OVERLAP PROGRAMMING DETAIL
## FOR OVERLAP C*

(program controller as shown below)

*NOTE FOR ALL OVERLAPS: Use Default values for
Overlap 'PLUS' programming details.

FROM MAIN MENU PRESS "1" CONTROLLER
AND THEN "5" OVERLAPS

```
               Overlaps
        ┌─────────────────┐
        │1.General Parms  │
        └─────────────────┘
         2.Program
         3.Status
```

```
        General Overlap Parameters
           Lock Inhibit OFF
          Confl Lock Enable OFF
          Parent P Clrncs ON
        Extra Included Phases OFF
         InhibitLockInterval ALWAYS
```

PRESS "ESC"

```
               Overlaps
         1.General Parms
        ┌─────────────────┐
        │2.Program        │
        └─────────────────┘
         3.Status
```

```
         Enter Overlap #  3

           then press Enter
```

```
              Overlap C-3
        ┌─────────────────┐
        │1.Program Parms  │
        └─────────────────┘
         2.Confl Prog+
         3.Program Parms+
```

Notice   ┌─────────────────────────────────┐
type     │ Ovrlp C-3   Ps.................. │
FYA-4    │                                 │
         │ Included Ps   5 0 0 0 0 0 0 0   │
         │ Modifier Ps   6 0 0 0 0 0 0 0   │
→        │ Type:FYA-4  Grn: 0  Yel: 3.5  Red: 1.5 │
         └─────────────────────────────────┘

END OF OVERLAP PROGRAMMING DETAIL

Electrical Detail - Sheet 2 of 3

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-1724
DESIGNED: August 2017
SEALED: 10/24/2017
REVISED:

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

SEAL

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DATE