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Project: U-4762

STATE OF NORTH CAROLINA
DIVISION OF HIGHWAYS

Project No. Sheet No.

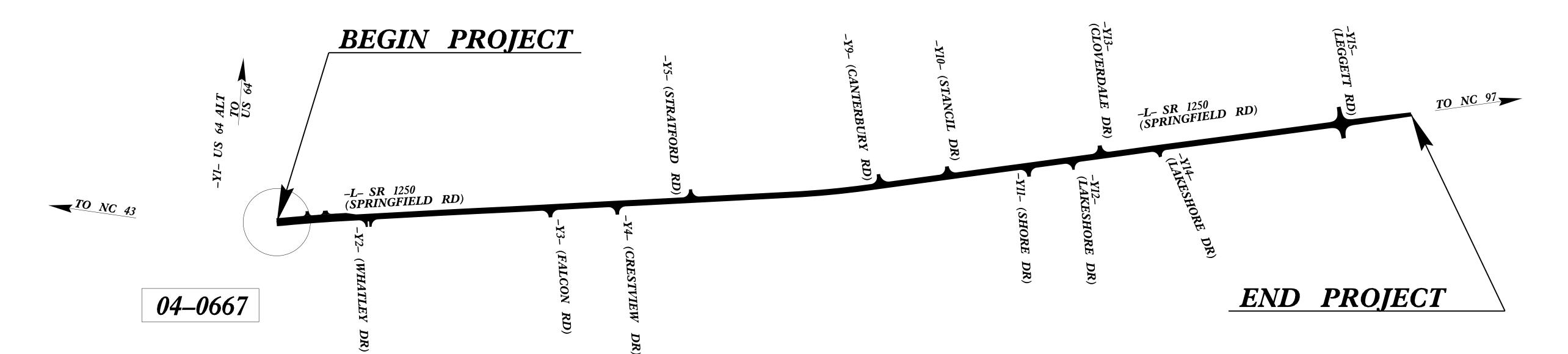
U-4762 Sig. 1.0

# EDGECOMBE COUNTY

LOCATION: SR 1250 (SPRINGFIELD ROAD) FROM US 64
ALTERNATE TO SR 1243 (LEGGETT ROAD)

TYPE OF WORK: TRAFFIC SIGNALS





Refer to "Roadway Standard Drawings NCDOT" dated January 2012 and "Standard Specifications for Roads and Structures" dated January 2012.

Sheet #
Sig. 1.0
Sig. 2.0-2.4

Reference #
----04-0667

VICINITY MAP

Index of Plans

Location/Description

Title Sheet
US 64 Alt. at NC 43 Bypass / SR 1250 (Springfield Road)

INTELLIGENT TRANSPORTATION AND SIGNALS UNIT Contacts:

Jason P. Galloway, PE - State Signals Engineer Mohd A. Aslami, PE - Signals Management Engineer DIVISION

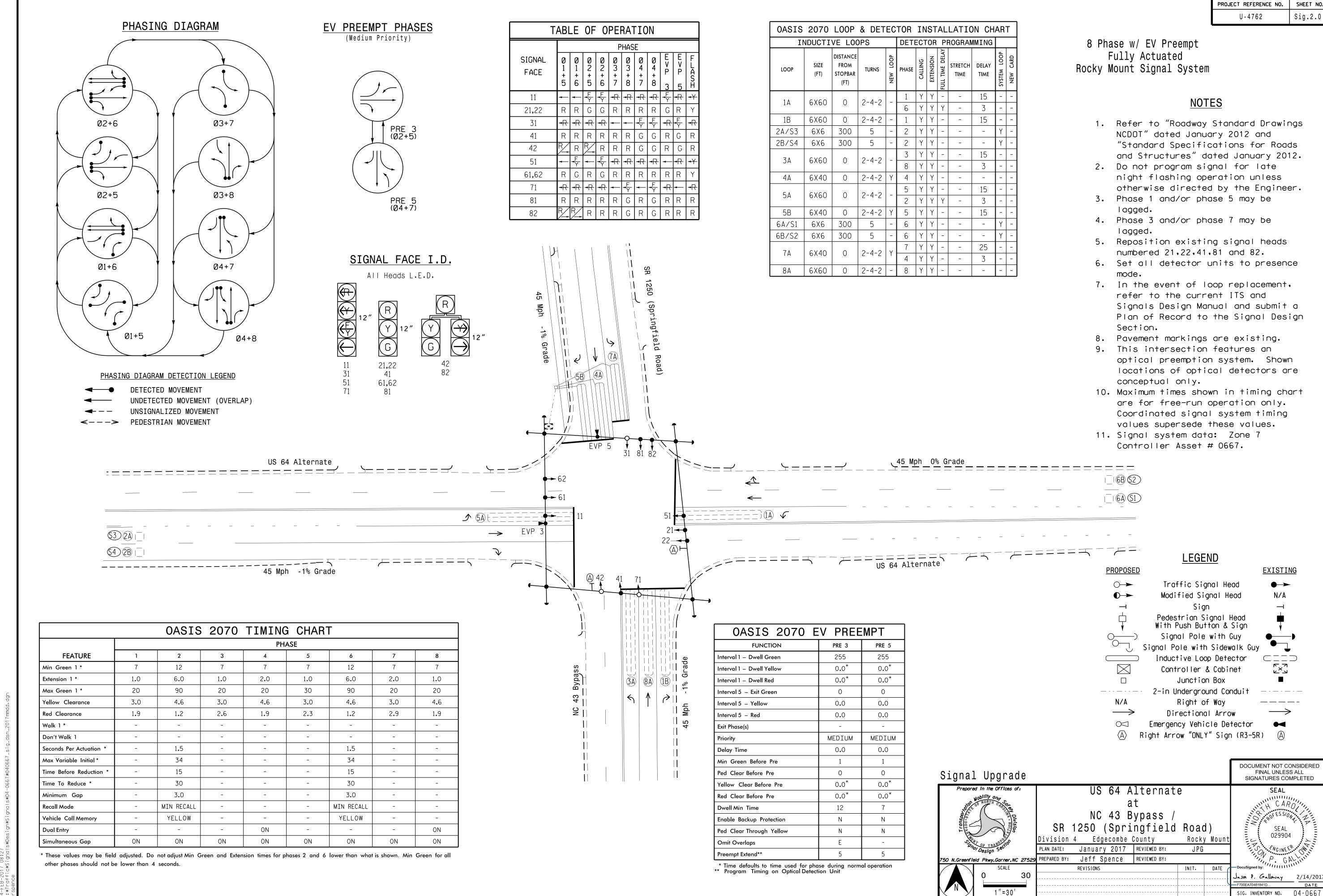
Mobility and Signals

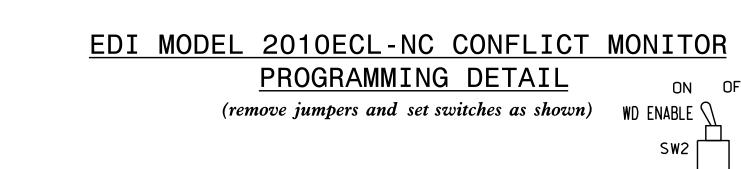
and Signals

Prepared in the Office of:
DIVISION OF HIGHWAYS
TRANSPORTATION MOBILITY AND SAFETY

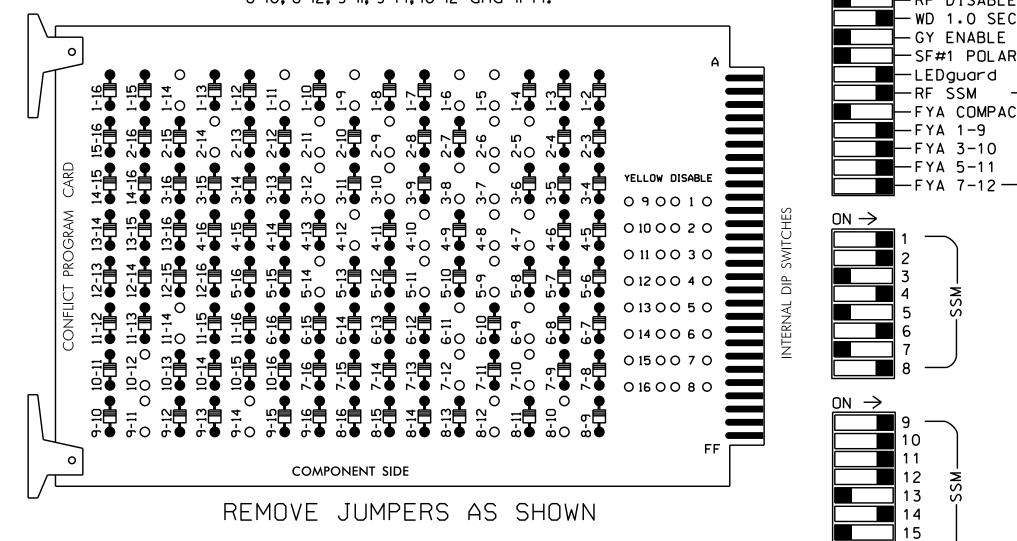
750 N. Greenfield Parkway, Garner, NC 27529

ZI-AFK-ZUII 13:21 R:\Traff1c\Signals\Design\Signals\04-0667\04-0667 irspense





REMOVE DIODE JUMPERS I-5, I-6, I-9, I-II, I-I4, 2-5, 2-6, 2-9, 2-II, 2-I4, 3-7, 3-8, 3-IO, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 5-14, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11, 9-14, 10-12 and II-14.



### NOTES:

- 1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- 2. Make sure jumpers SEL2-SEL5 are present on the monitor board.

# INPUT FILE POSITION LAYOUT

(front view)

_	1	2	3	4	5	6	7	8	9	10	11	12	13	14
file <sup>U</sup> "I" L	Ø 1 1A NOT USED	<ul><li>Ø2/SYS</li><li>2A/S3</li><li>Ø2/SYS</li><li>2B/S4</li></ul>	SLOT EMPTY	%-RED - ZPJF	Ø 3 3A NOT USED	Ø 4 4A NOT USED	SLOT EXPTY	%→RED →ZPJF	Ø 1 1B NOT USED	SLOT EMPTY	SLOT EMPTY	SLOT EXPTY	SLOT EMPTY	FS DC ISOLATOR ST DC ISOLATOR
FILE U	Ø 5 5A NOT USED	ø6/SYS 6A/S1 ø6/SYS 6B/S2	SLOT EMPTY	W-RED IZPJ-	Ø 7 7A NOT USED	Ø 8 8A NOT USED	SLOT EXPTY	%-RED 1260+ ⊗	Ø 5 5B NOT USED	SLOT EMPTY	l h	I PRE3     Opticom     2 Ch. Card     PRE5	SLOT EMPTY	SLOT EXPTY

EX.: 1A, 2A, ETC. = LOOP NO.'S

- $^{igotimes}$  Wired Input Do not populate slot with detector card
- \*See Opticom Field Wire Detail below.

FS = FLASH SENSE ST = STOP TIME PRE3.5 = EV PREEMPTS

-RP DISABLE - WD 1.0 SEC

-LEDguard

-FYA 1-9 —FYA 3-10

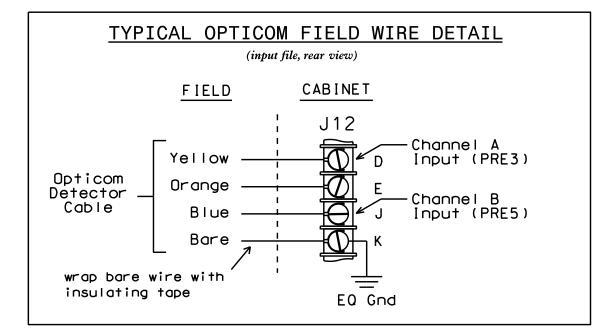
= DENOTES POSITION

OF SWITCH

—FYA 5−11

-SF#1 POLARITY &

-FYA COMPACT-



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0667 DESIGNED: January 2017 SEALED: 2/14/17 REVISED: N/A

# **NOTES**

- 1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- 2. Ensure that Red Enable is active at all times during normal operation. To prevent Red Failures on unused monitor channels, tie unused red monitor inputs 3,5,7, 13,15 & 16 to load switch AC+ per the cabinet manufacturer's instructions.
- 3. Program phases 4 and 8 for Dual Entry.
- 4. Enable Simultaneous Gap-Out for all phases.
- 5. Program phases 2 and 6 for Variable Initial and Gap Reduction.
- 6. Program phases 2 and 6 for Start Up In Green.
- 7. Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- 8. The cabinet and controller are part of the Rocky Mount City System.

Loadswitch S4P requires output

remapping. See sheet 4. -

PROJECT REFERENCE NO. Sig. 2.1 U-4762

				S	IGN	IAL	HE	AD	HC	OK	- UF	C	HAF	RT					
LOAD SWITCH NO.	S1		S2	S2P	S3	S4	S4P	S5	S6	S6P	S7	S8	S8P	S9	S10	S11	S12	S13	S14
PHASE	1	l	2	2 PED	3	4	OLE	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	82	21,22	NU	31	41,42	42	<b>★</b> 51	61,62	NU	71 <b>★</b>	81,82	NU	11	<b>★</b> 31	NU	<b>★</b> 51	<b>7</b> 1	NU
RED		*	128			101	*		134			107							
YELLOW			129		*	102		*	135		*	108							
GREEN			130			103			136			109							
RED ARROW														A121	A124		A114	A1Ø1	
YELLOW ARROW		126					105							A122	A125		A115	A102	
FLASHING YELLOW ARROW														A123	A126		A116	A103	
GREEN ARROW	127	127			118		106	133			124								

NU = Not Used

- \* Denotes install load resistor. See load resistor installation detail on sheet 3.
- ★ See pictorial of head wiring in detail below.

# EQUIPMENT INFORMATION

CABINET......332 W/ AUX SOFTWARE......ECONOLITE OASIS

CABINET MOUNT.....BASE

OUTPUT FILE POSITIONS...18 (12-STD; 6-AUX)

LOAD SWITCHES USED......S1,S2,S3,S4,S4P,S5,S6,S7,

\$8,\$9,\$10,\$12,\$13 

OVERLAP "A".....1+2 OVERLAP "B".....3+4

OVERLAP "C".....5+6 OVERLAP "D".....7+8

OVERLAP "E".....5

# INPUT FILE CONNECTION & PROGRAMMING CHART

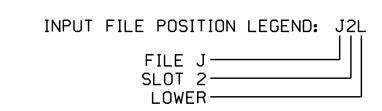
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A 1	TB2-1,2	I1U	56	18	1	1	Υ	Y			15
IH	-	J4U	48	10	26	6	Y	Υ	Y		3
1B	TB6-9,10	I9U	60	22	11	1	Υ	Y			15
2A/S3	TB2-5 <b>,</b> 6	I2U	39	1	2	2/SYS	Υ	Υ			
2B/S4	TB2-7 <b>,</b> 8	I2L	43	5	12	2/SYS	Y	Υ			
3A <sup>2</sup>	TB4-5 <b>,</b> 6	I5U	58	20	3	3	Y	Y			15
3H	-	J8U	50	12	28	8	Υ	Υ			3
4A	TB4-9,10	I6U	41	3	4	4	Υ	Υ			
5A <sup>3</sup>	TB3-1,2	J1U	55	17	5	5	Υ	Υ			15
) SH	-	I4U	47	9	22	2	Y	Υ	Υ		3
5B	TB7-9,10	J9U	59	21	15	5	Y	Y			15
6A/S1	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S2	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
7A <sup>4</sup>	TB5-5,6	J5U	57	19	7	7	Υ	Y			25
<sup>/</sup> H	_	I8U	49	11	24	4	Υ	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

<sup>2</sup>Add jumper from I5-W to J8-W, on rear of input file.

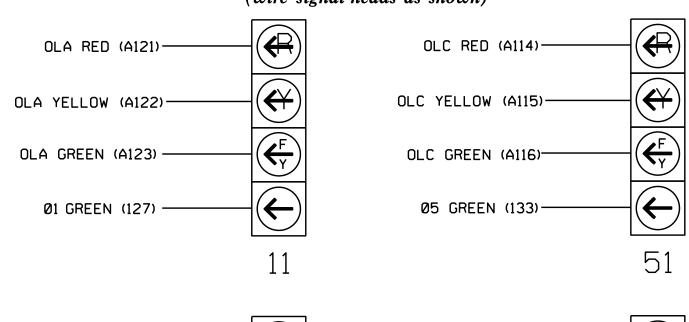
<sup>3</sup>Add jumper from J1-W to I4-W, on rear of input file.

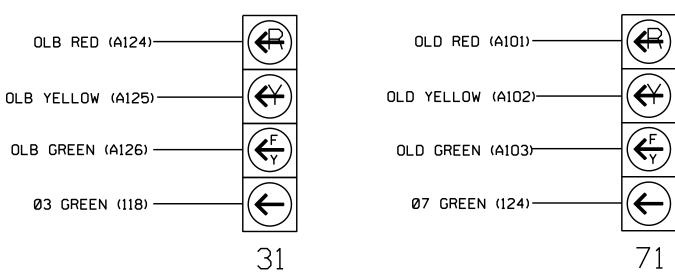
⁴Add jumper from J5-W to I8-W, on rear of input file.



# FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)





<u>NOTE</u>

The sequence display for these signals require special logic programming. See sheet 2 for programming instructions.

Electrical Detail - Sheet 1 of 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of:

NC 43 Bypass/ SR 1250 (Springfield Road)

US 64 Alternate

PLAN DATE: February 2017 REVIEWED BY: PREPARED BY: B. SIMMONS REVIEWED BY:  $\mathsf{K}\mathsf{M}\mathsf{M}$ REVISIONS INIT. DATE

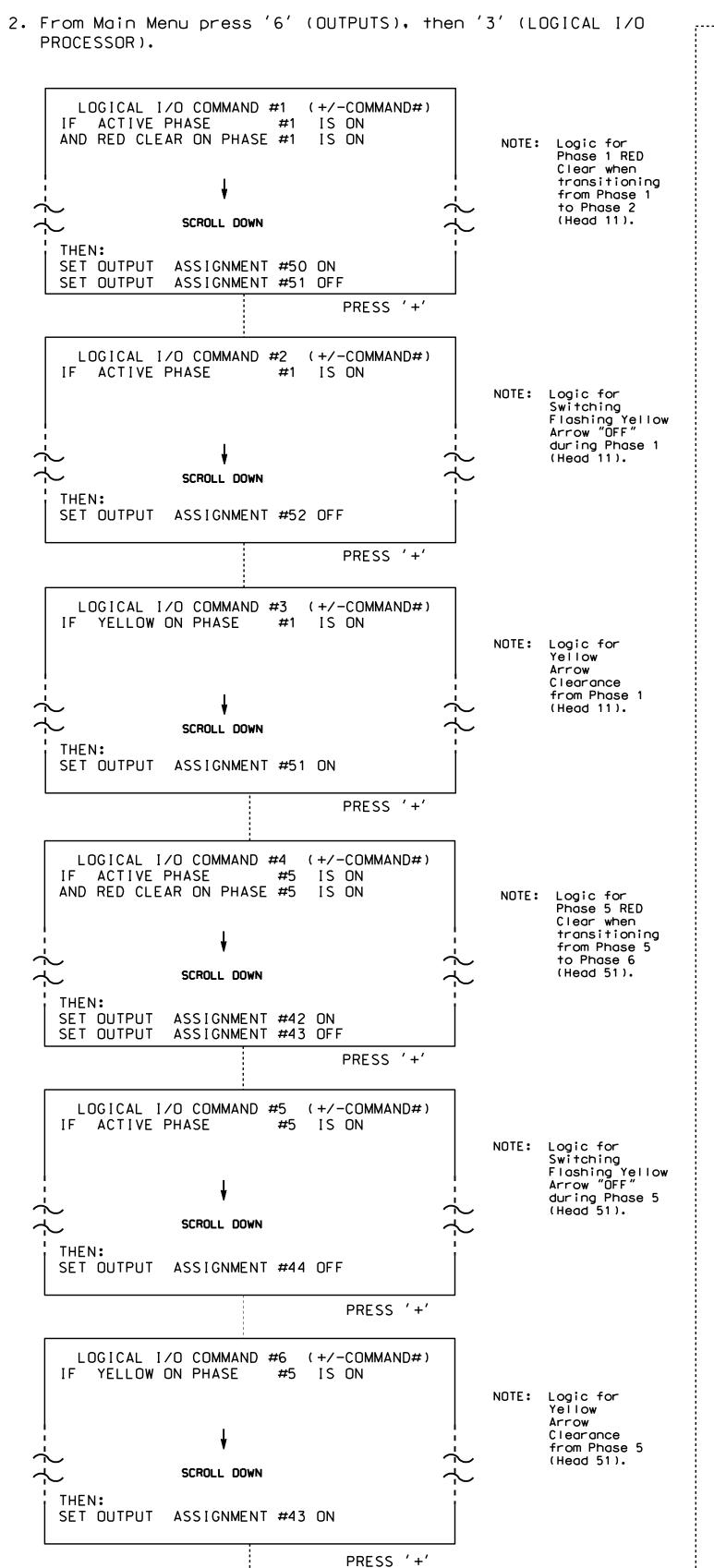
750 N.Greenfield Pkwy, Garner, NC 27529

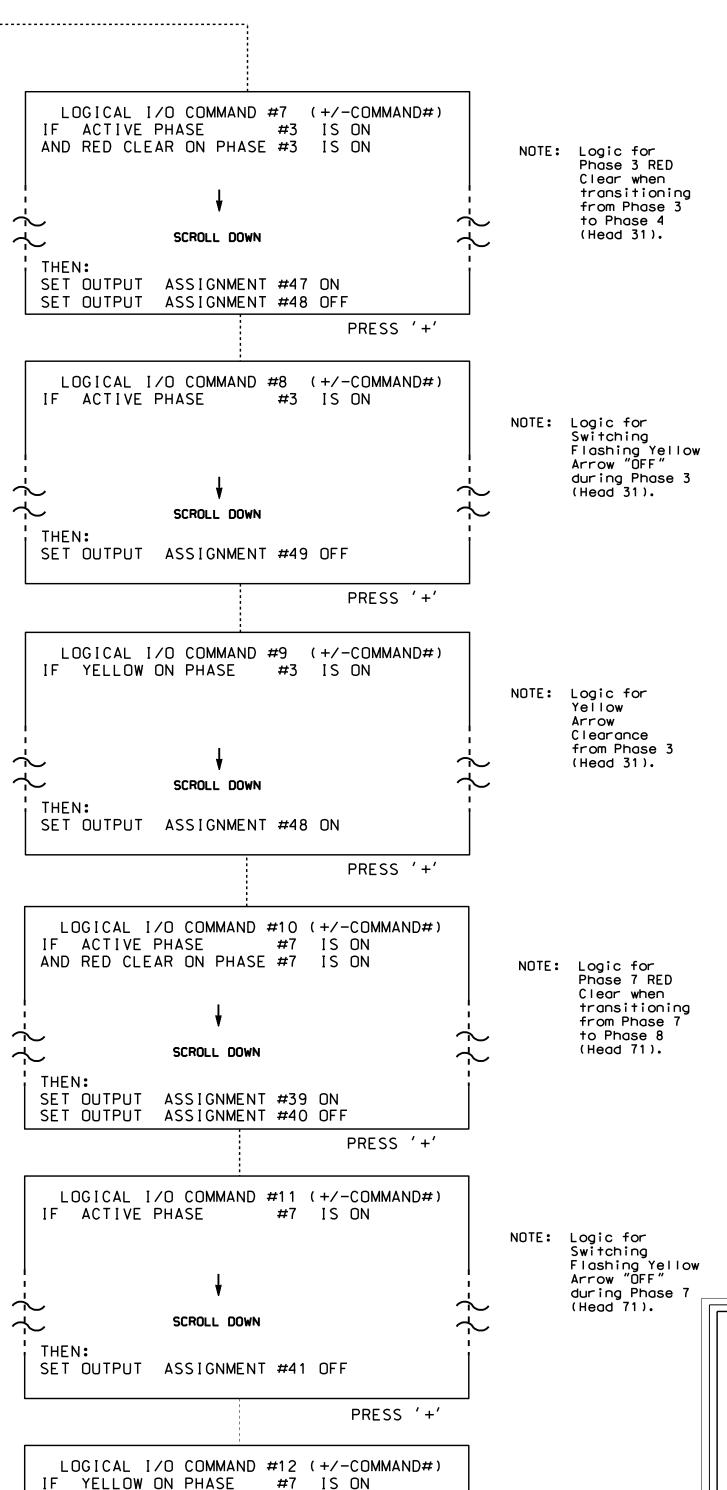
SIG. INVENTORY NO. 04-0667

# LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. From Main Menu press '2' (PHASE CONTROL), then '1' (PHASE CONTROL FUNCTIONS). Scroll to the bottom of the menu and Enable ACT Logic Commands 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11 and 12.





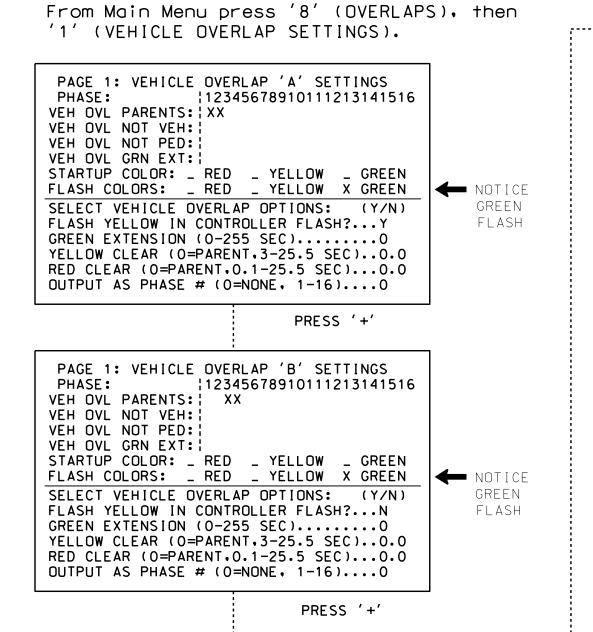
SCROLL DOWN

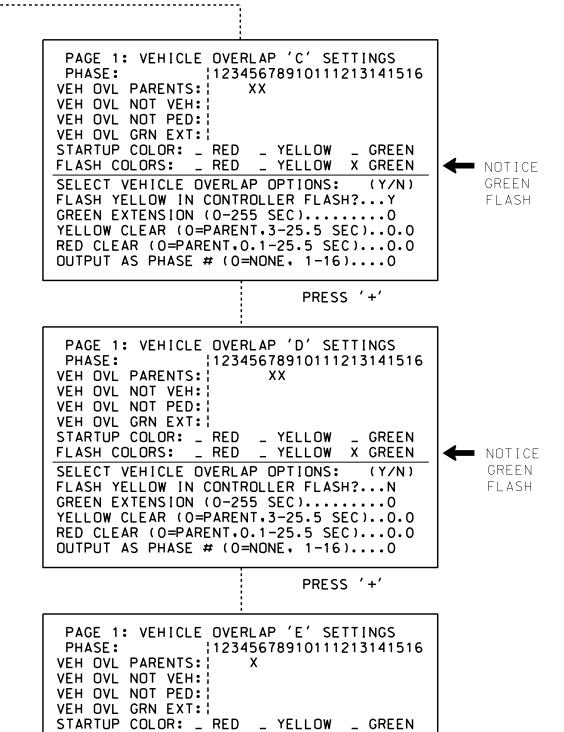
LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

SET OUTPUT ASSIGNMENT #40 ON

# OVERLAP PROGRAMMING DETAIL

(program controller as shown below)





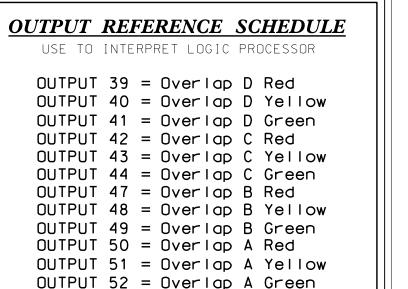
OVERLAP PROGRAMMING COMPLETE

FLASH COLORS: \_ RED \_ YELLOW \_ GREEN

SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N

GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT.3-25.5 SEC)..0.0 RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0

OUTPUT AS PHASE # (0=NONE, 1-16)....0



NOTE: Logic for

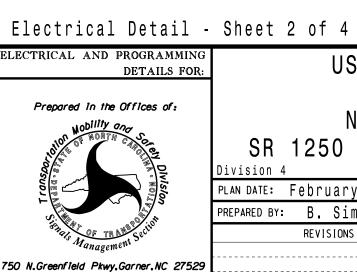
Yellow

Clearance

(Head 71).

from Phase 7

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0667 DESIGNED: January 2017 SEALED: 2/14/17 REVISED: N/A



US 64 Alternate NC 43 Bypass/

SR 1250 (Springfield Road) Fdaecombe County Rockv Mount PLAN DATE: February 2017 | REVIEWED BY: BAS PREPARED BY: B. SIMMONS REVIEWED BY: KMMREVISIONS INIT. DATE

SEAL 032108

Moled Aslami SIG. INVENTORY NO. 04-0667

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UNLESS ALL SIGNATURES COMPLETED

SEAL

(program controller as shown below)

From Main Menu press 'A' (Preemption), then '1' (Standard Preemptions). Press 'NEXT' to advance to Preemption #3.

PREEMPTION #3 SETTINGS (NEXT:1-10) INTERVAL/TIMING | CLEAR/DWELL PHASES GRN YEL RED | 12345678910111213141516 
1 255 0.0 0.0 | X X 2 0 0.0 0.0 ; 3 0 0.0 0.0 ; 4 0 0.0 0.0 ; 5 0 0.0 0.0 | EXIT CALLS PRIORITY (Y/N TO SELECT) .....MED DELAY TIMER (0-255 SEC) .....0 MIN GREEN BEFORE PRE (O= DEFAULT)....1 PED CLEAR BEFORE PRE (O= DEFAULT)....O YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0 RED CLEAR BEFORE PRE (O= DEFAULT)....O.O DWELL MIN TIMER (0-255 SEC) .....12 DWELL MAX TIMER (0=OFF,1-255MIN) ....0 DWELL HOLD-OVER TIMER (0-255) .....0 LATCH CALL? .....N LINK TO NEXT PREEMPT? ...... ENABLE BACKUP PROTECTION? .....N HOLD CLEAR 1 PHASES DURING DELAY? ...N FAST GREEN FLASH DWELL PHASES? .....N PED CLEARANCE THROUGH YELLOW? .....N INHIBIT OVERLAP GREEN EXTENSION? ....N SERVICE DURING SOFTWARE FLASH? .....N REST IN RED DURING DWELL INTERVAL? .. N FLASH DWELL INTERVAL? ...... ALLOW PEDS IN DWELL INTERVAL? .....N RE-TIME DWELL INTERVAL? .....N OVERLAPS: ABCDEFGHIJKLMNOP DWELL INT FLASH YELLOW OMIT OVERLAPS:

PRESS 'NEXT' TWICE

PREEMPTION #5 SETTINGS (NEXT:1-10) INTERVAL/TIMING ; CLEAR/DWELL PHASES GRN YEL RED | 12345678910111213141516 1 255 0.0 0.0 | X X 2 0 0.0 0.0 ; 3 0 0.0 0.0 4 0 0.0 0.0 5 0 0.0 0.0 | EXIT CALLS PRIORITY (Y/N TO SELECT) .....MED DELAY TIMER (0-255 SEC) ...... MIN GREEN BEFORE PRE (O= DEFAULT)....1 PED CLEAR BEFORE PRE (O= DEFAULT)....O YELLOW CLEAR BEFORE PRE (0= DEFAULT).0.0 RED CLEAR BEFORE PRE (0= DEFAULT)....O.O DWELL MIN TIMER (0-255 SEC) ......7 DWELL MAX TIMER (0=OFF.1-255MIN) ....0 DWELL HOLD-OVER TIMER (0-255) .....0 LATCH CALL? .....N LINK TO NEXT PREEMPT? .....N ENABLE BACKUP PROTECTION? .....N HOLD CLEAR 1 PHASES DURING DELAY? ...N FAST GREEN FLASH DWELL PHASES? .....N PED CLEARANCE THROUGH YELLOW? .....N INHIBIT OVERLAP GREEN EXTENSION? ....N SERVICE DURING SOFTWARE FLASH? .....N REST IN RED DURING DWELL INTERVAL? .. N FLASH DWELL INTERVAL? .....N ALLOW PEDS IN DWELL INTERVAL? .....N RE-TIME DWELL INTERVAL? .....N ABCDEFGHIJKLMNOP OVERLAPS: DWELL INT FLASH YELLOW OMIT OVERLAPS:

PREEMPT PROGRAMMING COMPLETE

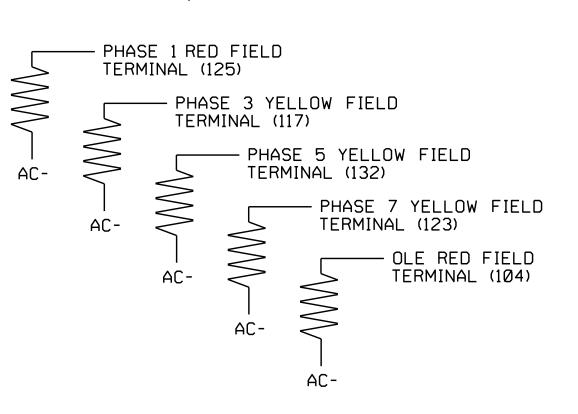
Program extend time on optical detector units for 5.0 sec for EVP3 and EVP5.

PROJECT REFERENCE NO. Sig. 2.3 U-4762

# LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES VALUE (ohms) WATTAGE 1.5K - 1.9K 25W (min) 2.0K - 3.0K | 10W (min)



# PREEMPT ONLY PHASE OMIT NOTE

(program controller as shown below)

From Main Menu press '2' Phase Control). Then '1' (Phase Control Functions) Program Phase 5 for 'Omit Phase' and Phases 1, 2, 4, 6, and 8 for 'Startup Colls'. This is to prevent Phase 5 from being served when not in Preempt.

> THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0667 DESIGNED: January 2017 SEALED: 2/14/17 REVISED: N/A

Electrical Detail - Sheet 3 of 4

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ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of:

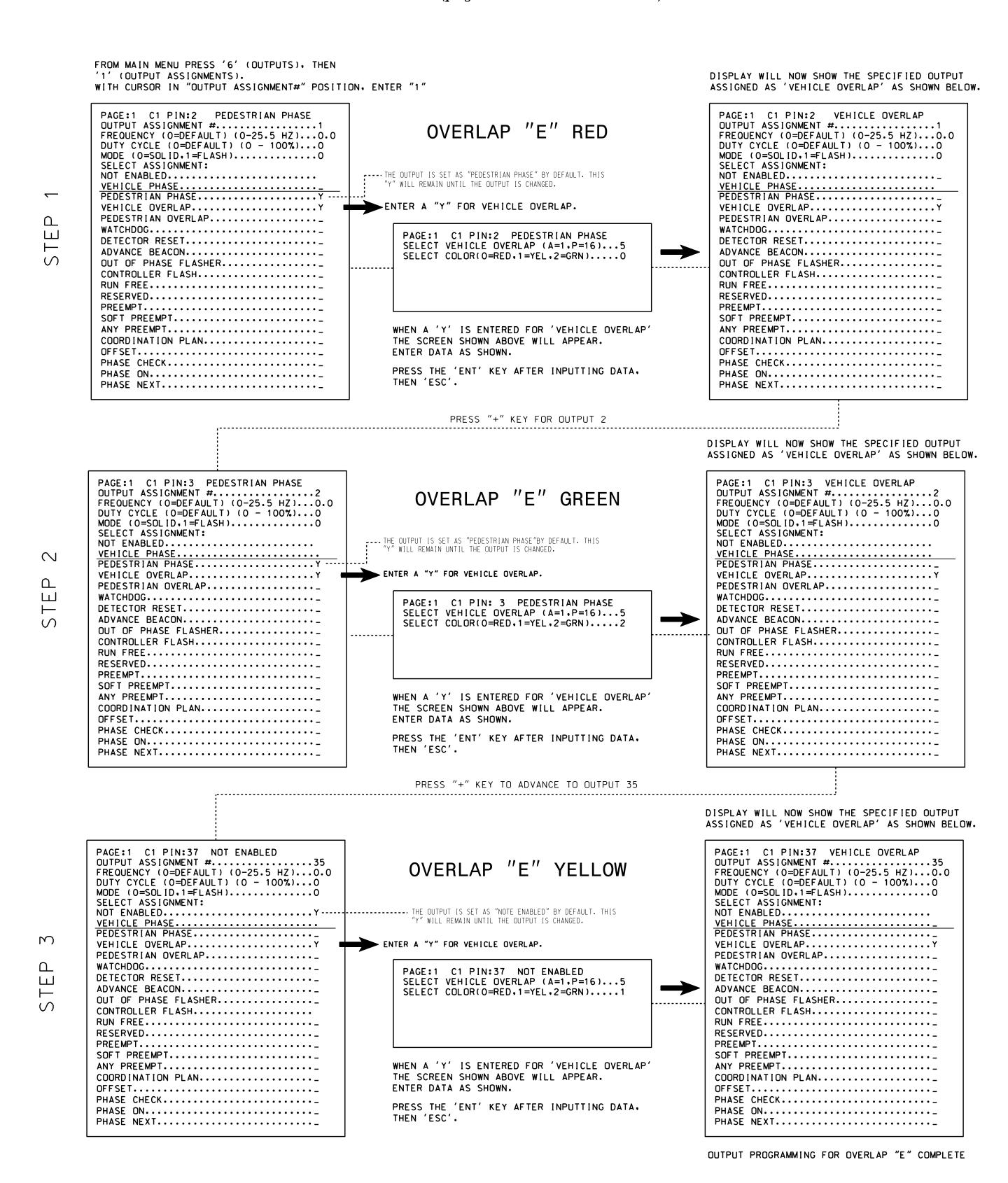
750 N.Greenfield Pkwy, Garner, NC 27529

NC 43 Bypass/ SR 1250 (Springfield Road)

US 64 Alternate

PLAN DATE: February 2017 REVIEWED BY: PREPARED BY: B. SIMMONS REVIEWED BY:  $\mathsf{K}\mathsf{M}\mathsf{M}$ REVISIONS INIT. DATE

SIG. INVENTORY NO. 04-0667



PROJECT REFERENCE NO. SHEET NO. U-4762 Sig. 2.4

# CONFLICT MONITOR WIRING DETAIL FOR LOAD SWITCH S4P YELLOW

LOAD SWITCH S4P-YELLOW IS NOT NORMALLY WIRED TO THE CONFLICT MONITOR WHEN AN AUX. OUTPUT FILE IS INSTALLED. THEREFORE, THIS CHANGE IS NECESSARY TO FACILITATE USING THIS SLOT FOR A VEHICLE OVERLAP, INSTEAD OF A PED SIGNAL.

(Make wiring change as shown below)

STEP 1 = FIND UNUSED BUNDLE OF CONFLICT MONITOR WIRES BEHIND REAR PANEL OF MAIN OUTPUT FILE.

STEP 2 = FIND WIRE LABED "CMU-11."

STEP 3 = TERMINATE "CMU-11." USING PROVIDED RING CONNECTOR. TO OUTPUT FILE TERMINAL 105.

# FLASHER CIRCUIT MODIFICATION DETAIL

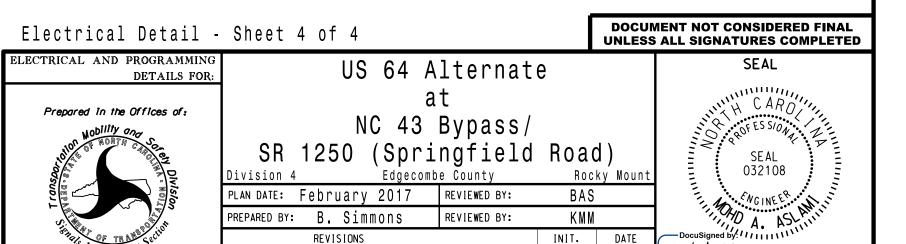
IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

750 N.Greenfield Pkwy,Garner,NC 27529

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0667 DESIGNED: January 2017 SEALED: 2/14/17 REVISED: N/A



SIG. INVENTORY NO. 04-0667

S:\*ITS&SU\*ITS Signals\*Workgroups\*Sig Man\*Simmons\*Working Folder\*Electrical Details\*Division 04\*040667\_sm\_ bjsimmons —