

**This electronic collection of documents is provided
for the convenience of the user
and is Not a Certified Document –**

**The documents contained herein were originally issued
and sealed by the individuals whose names and license
numbers appear on each page, on the dates appearing
with their signature on that page.**

**This file or an individual page
shall not be considered a certified document.**

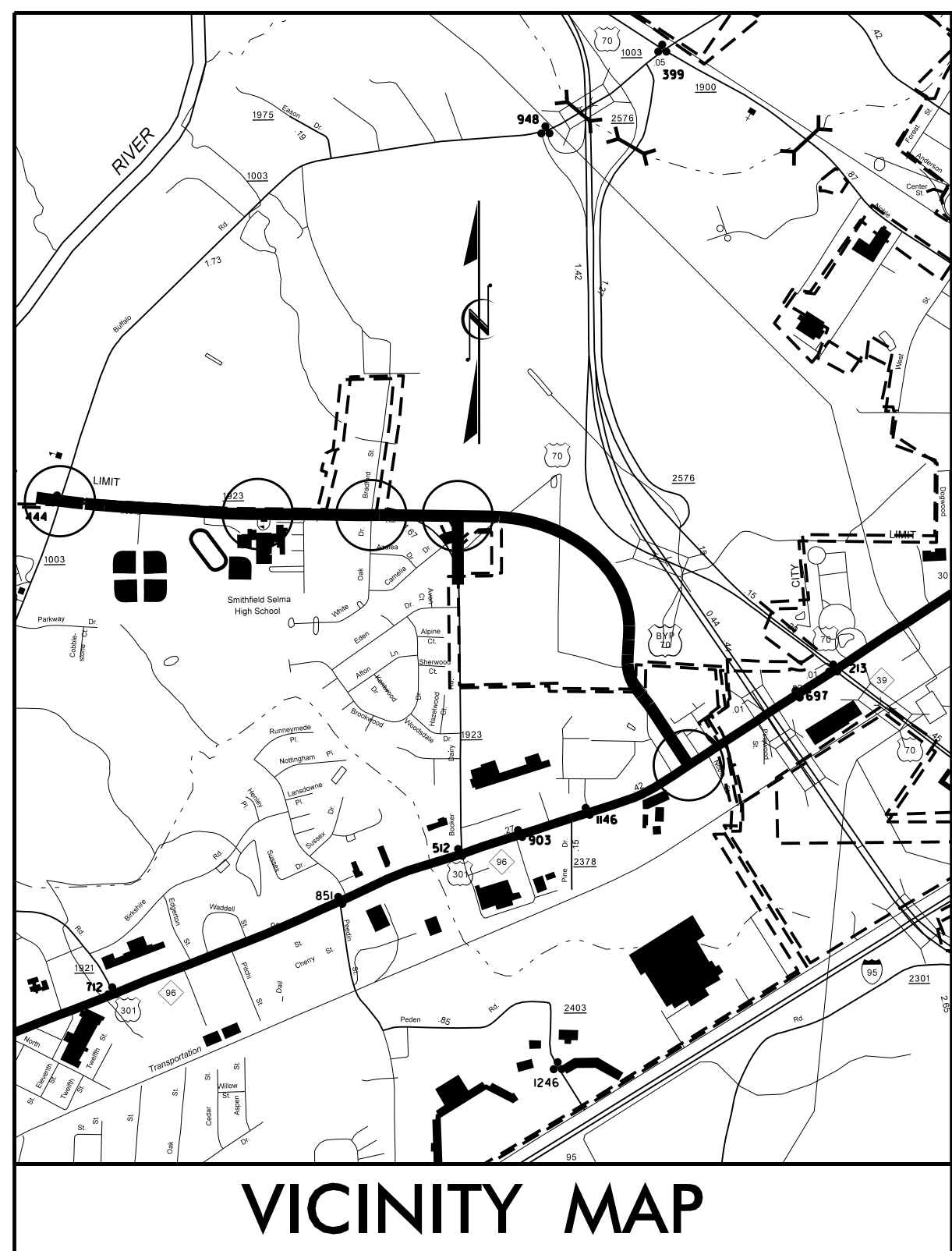
Project: U-3334B

STATE OF NORTH CAROLINA
DIVISION OF HIGHWAYS

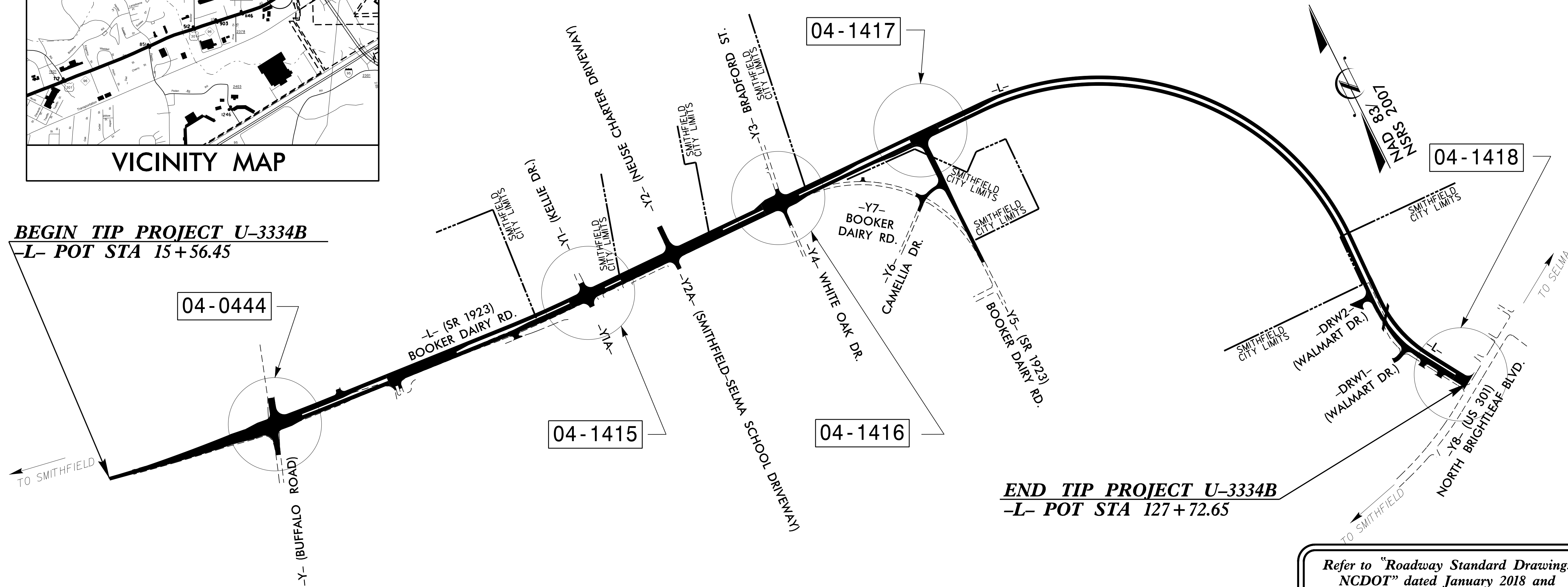
JOHNSTON COUNTY

LOCATION: SR 1923 Extension (Booker Dairy Road) from SR 1003 (Buffalo Road) to US 301 (Brightleaf Boulevard)

TYPE OF WORK: Traffic Signals



BEGIN TIP PROJECT U-3334B
L- POT STA 15+56.45



END TIP PROJECT U-3334B
L- POT STA 127+72.65

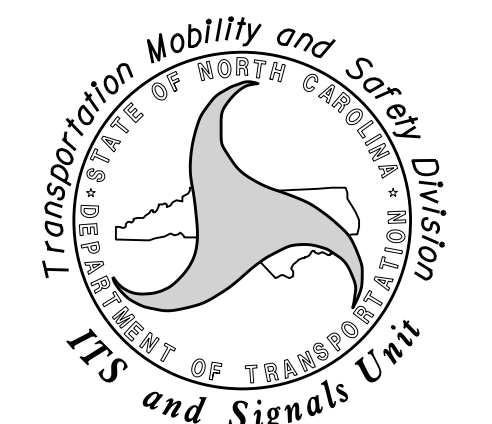
Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.

Sheet #	Reference #	Location/Description
Sig. 1.0	-----	Title Sheet
Sig. 2.0-2.2	04-0444 Temp 1	SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)
Sig. 3.0-3.2	04-0444 Temp 2	SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)
Sig. 4.0-4.2	04-0444 Final	SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)
Sig. 5.0-5.2	04-1415 Temp 1	SR 1923 (Booker Dairy Road) at Kellie Drive/High School Dwy.
Sig. 6.0-6.2	04-1415 Temp 2	SR 1923 (Booker Dairy Road) at Kellie Drive/High School Dwy.
Sig. 7.0-7.2	04-1415 Final	SR 1923 (Booker Dairy Road) at Kellie Drive/High School Dwy.
Sig. 8.0-8.1	04-1416	SR 1923 (Booker Dairy Road) at Bradford St./White Oak Dr.
Sig. 9.0-9.1	04-1417 Temp	SR 1923 (Booker Dairy Road) at SR 1923 (Existing Booker Dairy Road)
Sig. 10.0-10.1	04-1417 Final	SR 1923 (Booker Dairy Road) at SR 1923 (Existing Booker Dairy Road)
Sig. 11.0-11.2	04-1418	US 301 (Brightleaf Boulevard) at Ava Garner Street
Sig. 12.0-12.1	N/A	Revisions to Standard Drawings
Sig. M1-M8	N/A	Metal Pole Standards
SCP 1-9	N/A	Cable Routing Plans

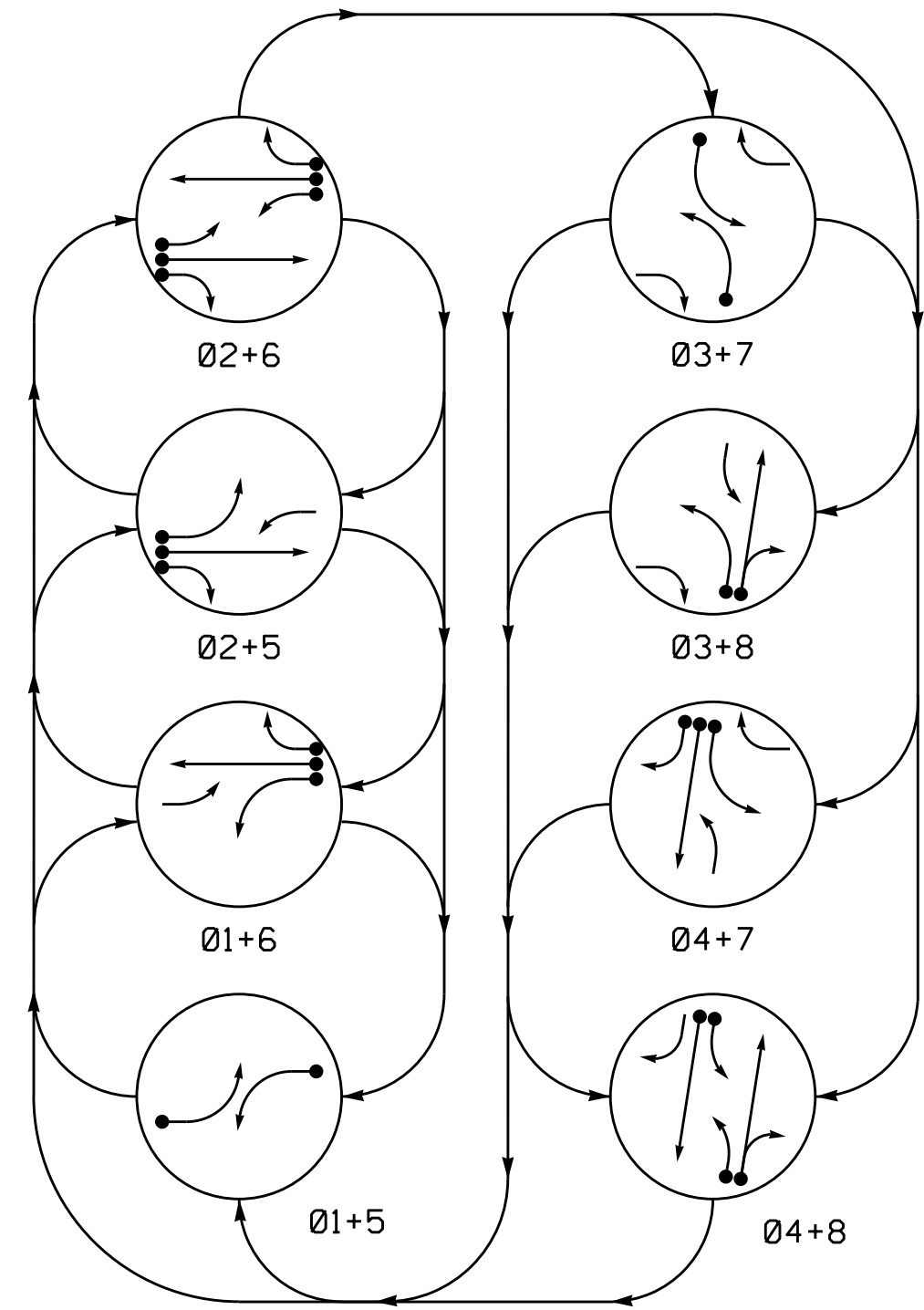
INTELLIGENT TRANSPORTATION AND SIGNALS UNIT
Contacts:

Meredith McDiarmid, PE - ITS and Signals Engineer
Jason P. Galloway, PE - Signals Engineer
Keith M. Mims, PE - Signal Equipment Design Engineer
I. Neil Avery - Signals Communications Engineer

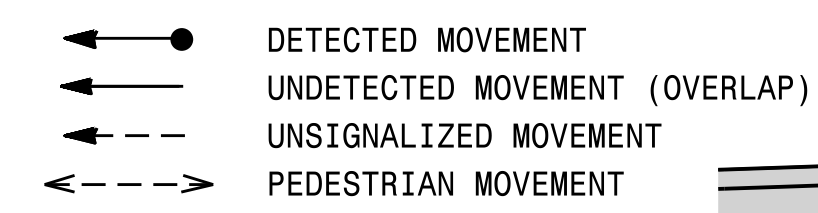
Prepared in the Office of:
DIVISION OF HIGHWAYS
TRANSPORTATION MOBILITY AND SAFETY DIVISION



PHASING DIAGRAM

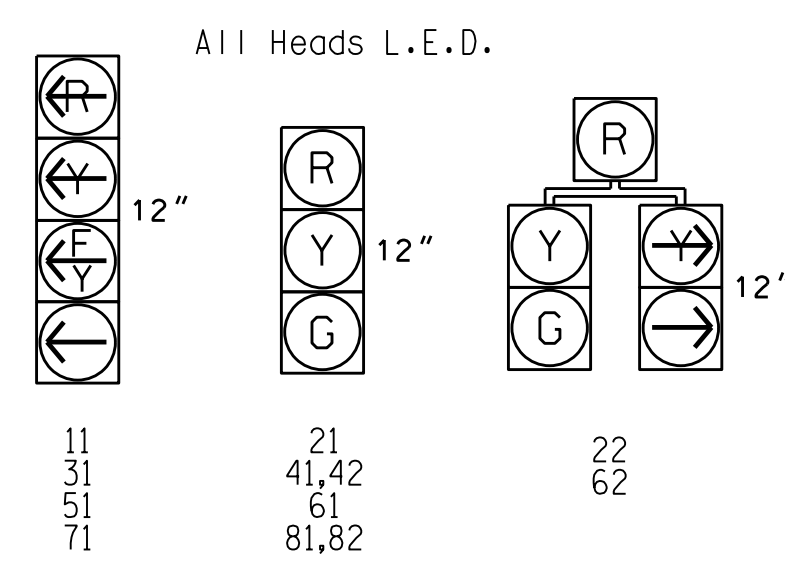


PHASING DIAGRAM DETECTION LEGEND



SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	←	←	←	←	←	←	←	←
41,42	R	R	R	R	R	R	G	G
51	←	←	←	←	←	←	←	←
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71	←	←	←	←	←	←	←	←
81,82	R	R	R	R	R	G	R	G

SIGNAL FACE I.D.



OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

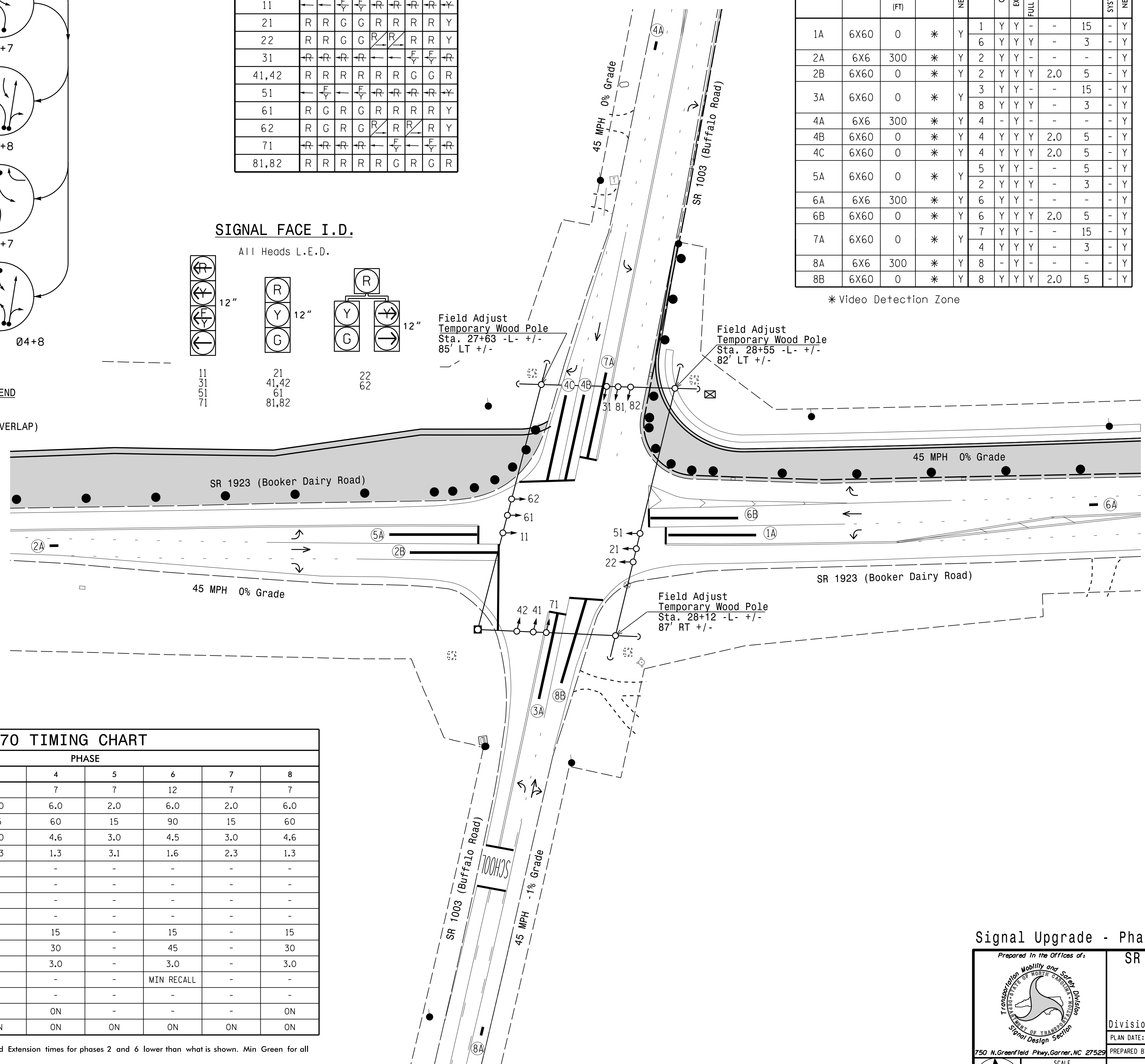
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING				SYSTEM LOOP	NEW CARD	
					PHASE	CALLING	EXTENSION	STRETCH TIME			DELAY TIME
1A	6X60	0	*	Y	1	Y	Y	-	15	-	Y
2A	6X6	300	*	Y	2	Y	Y	-	-	-	Y
2B	6X60	0	*	Y	2	Y	Y	2.0	5	-	Y
3A	6X60	0	*	Y	3	Y	Y	-	15	-	Y
4A	6X6	300	*	Y	4	-	Y	-	-	-	Y
4B	6X60	0	*	Y	4	Y	Y	2.0	5	-	Y
4C	6X60	0	*	Y	4	Y	Y	2.0	5	-	Y
5A	6X60	0	*	Y	5	Y	Y	-	5	-	Y
6A	6X6	300	*	Y	6	Y	Y	-	-	-	Y
6B	6X60	0	*	Y	6	Y	Y	2.0	5	-	Y
7A	6X60	0	*	Y	7	Y	Y	-	15	-	Y
8A	6X6	300	*	Y	8	-	Y	-	-	-	Y
8B	6X60	0	*	Y	8	Y	Y	2.0	5	-	Y

* Video Detection Zone

8 Phase Fully Actuated Isolated

NOTES

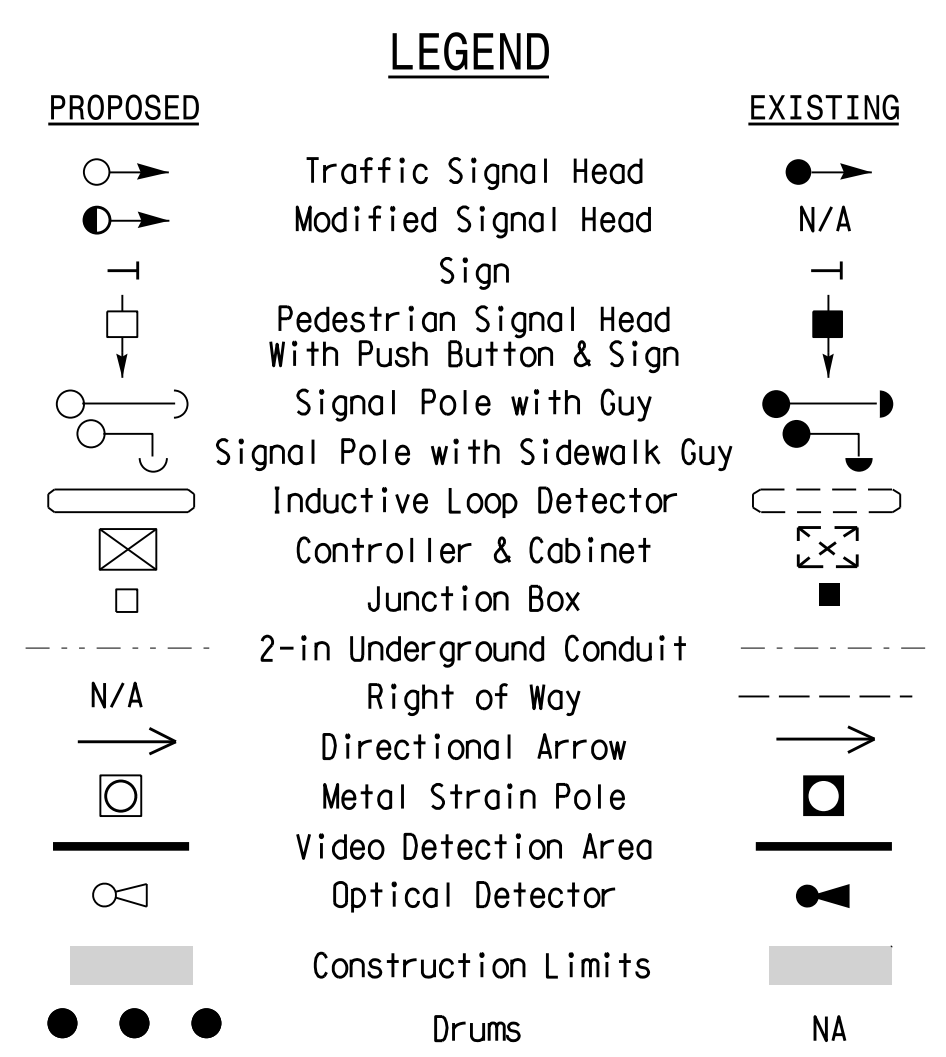
1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 1 and/or phase 5 may be lagged.
4. Phase 3 and/or phase 7 may be lagged.
5. Set all detector units to presence mode.
6. Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.



OASIS 2070 TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1 *	7	12	7	7	7	12	7	7
Extension 1 *	2.0	6.0	2.0	6.0	2.0	6.0	2.0	6.0
Max Green 1 *	15	90	15	60	15	90	15	60
Yellow Clearance	3.0	4.5	3.0	4.6	3.0	4.5	3.0	4.6
Red Clearance	2.8	1.6	2.3	1.3	3.1	1.6	2.3	1.3
Walk 1 *	-	-	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-	-	-
Seconds Per Actuation *	-	-	-	-	-	-	-	-
Max Variable Initial *	-	-	-	-	-	-	-	-
Time Before Reduction *	-	15	-	15	-	15	-	15
Time To Reduce *	-	45	-	30	-	45	-	30
Minimum Gap	-	3.0	-	3.0	-	3.0	-	3.0
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	-	-	-	-	-	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Phase 1 - Temp 1

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1923 (Booker Dairy Road)
at
SR 1003 (Buffalo Road)

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: JPG

PREPARED BY: KGP, Jr. REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

Jason P. Gallaway 9/8/2017

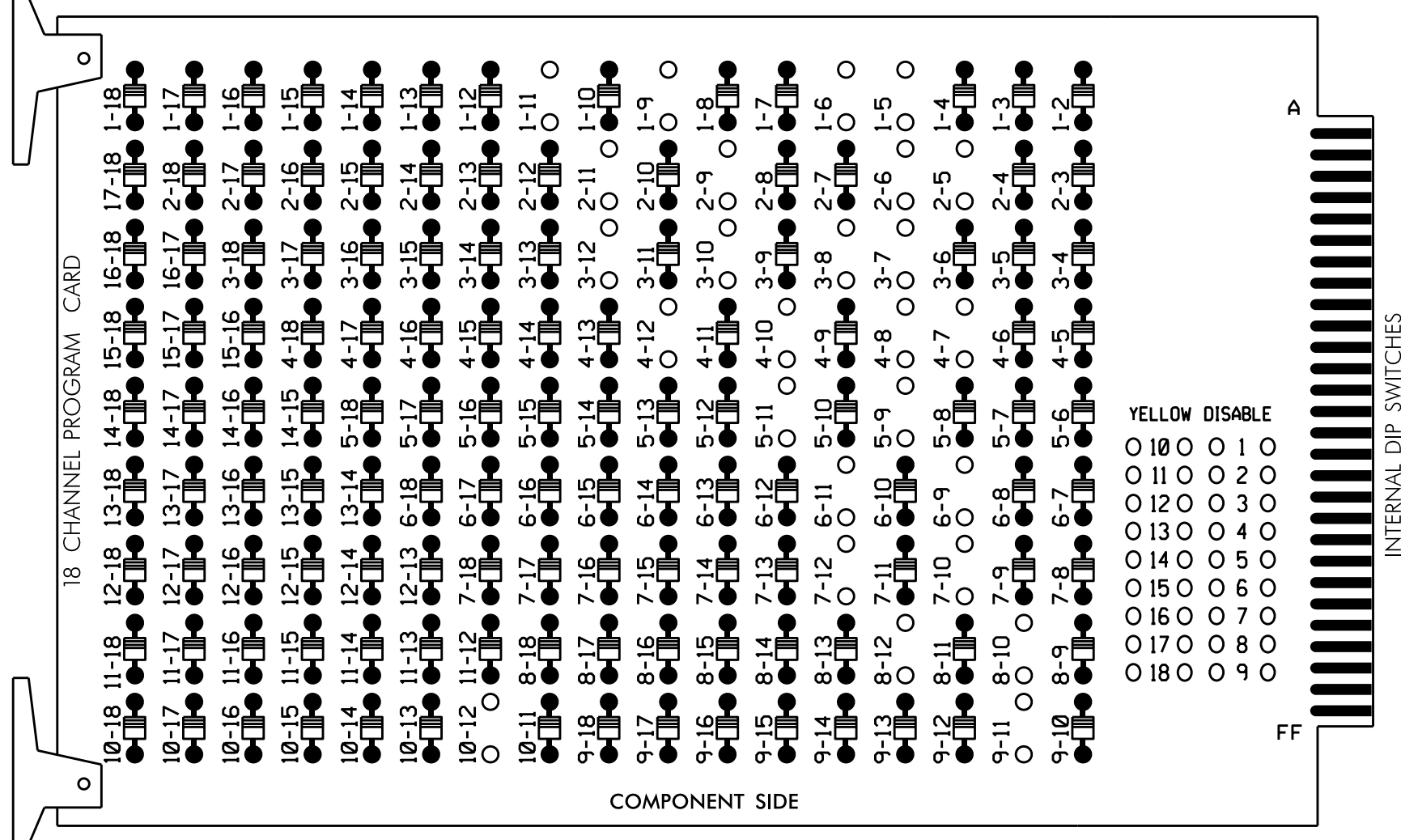
SIG. INVENTORY NO. 04-044411

REVISIONS	INIT.	DATE

EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11 and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all Phases.
4. Program phases 2, 4, 6 and 8 for Gap Reduction.
5. Program phases 2 and 6 for Startup In Green.
6. Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,
 AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

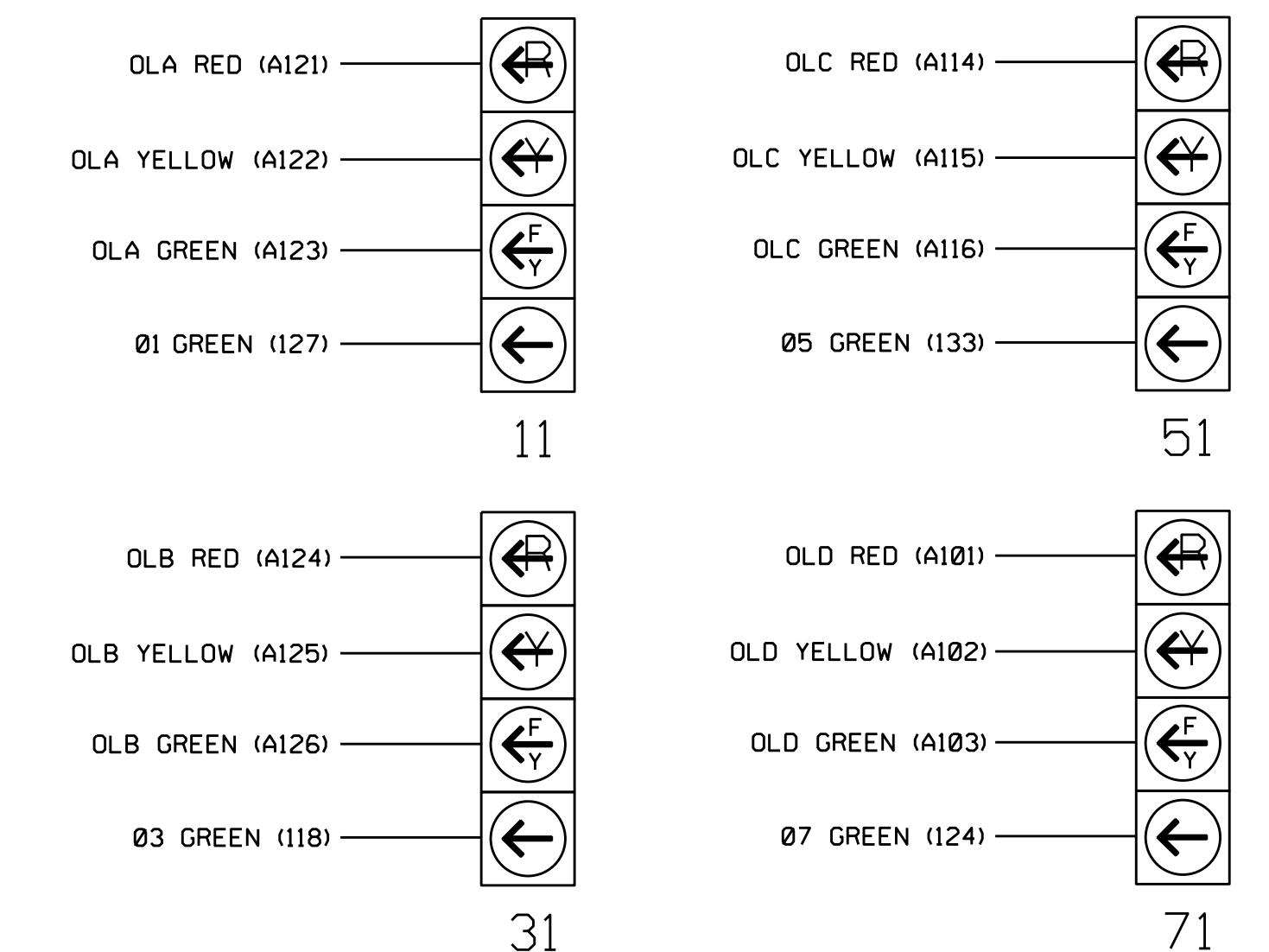
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22	NU	22	31	41,42	NU	51	61,62	NU	62	71	81,82	NU	11	31	51	71	NU
RED	128		*		101			134		*		107							
YELLOW	*	129			102		*	135				108							
GREEN		130			103			136				109							
RED ARROW													A121	A124		A114	A101		
YELLOW ARROW				117								123		A122	A125		A115	A102	
FLASHING YELLOW ARROW														A123	A126		A116	A103	
GREEN ARROW	127			118	118			133		124	124								

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

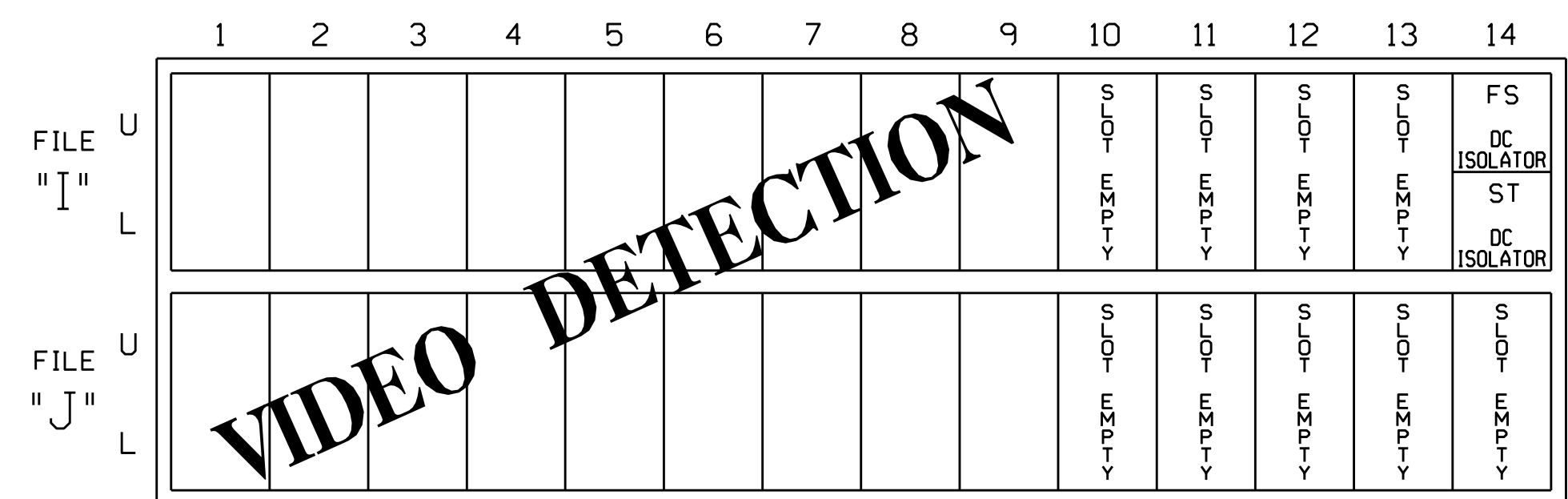


NOTE

The sequence display for signal heads 11, 31, 51, and 71 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

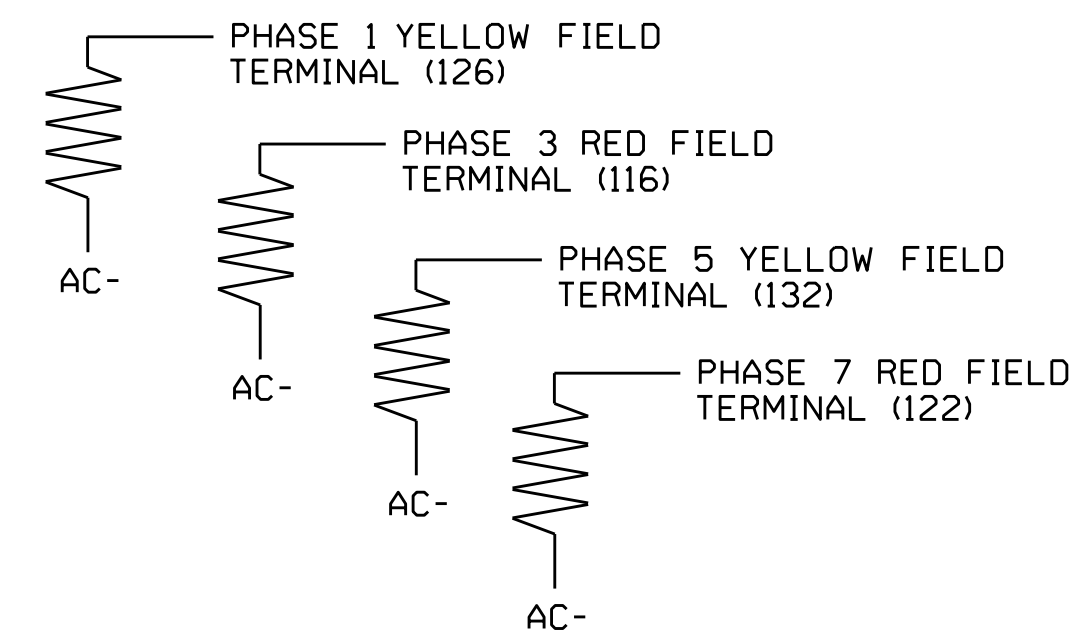
SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0444T1
 DESIGNED: August 2017
 SEALED: 9/8/2017
 REVISED:

Electrical Detail - Temp 1 - Sheet 1 of 2

Electrical and Programming Details For: SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)

Prepared In the Offices of: [Logo]

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

DocuSign by: D. Todd Joyce 9/13/2017

SIG. INVENTORY NO. 04-0444T1

13-SEP-2017 09:48 S:\IT\AS\115\SIGNAL\work\hgr\oas\sig\Map\5111\ck\lncd\040444_sml_e.xxx.dgn

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
AND RED CLEAR ON PHASE #1 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #50 ON
SET OUTPUT ASSIGNMENT #51 OFF
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

```

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #52 OFF
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #1 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #51 ON
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

```

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #44 OFF
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #6 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #43 ON
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #7 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
AND RED CLEAR ON PHASE #3 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #47 ON
SET OUTPUT ASSIGNMENT #48 OFF
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

```

LOGICAL I/O COMMAND #8 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #49 OFF
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #9 (+/-COMMAND#)
IF YELLOW ON PHASE #3 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #48 ON
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
AND RED CLEAR ON PHASE #7 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #39 ON
SET OUTPUT ASSIGNMENT #40 OFF
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

```

LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #41 OFF
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

```

LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF YELLOW ON PHASE #7 IS ON
    SCROLL DOWN
THEN:
SET OUTPUT ASSIGNMENT #40 ON
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

PRESS '+'

```

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

PRESS '+'

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

PRESS '+'

```

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

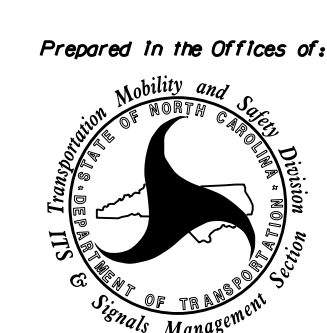
OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

- OUTPUT 39 = Overlap D Red
- OUTPUT 40 = Overlap D Yellow
- OUTPUT 41 = Overlap D Green
- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 47 = Overlap B Red
- OUTPUT 48 = Overlap B Yellow
- OUTPUT 49 = Overlap B Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

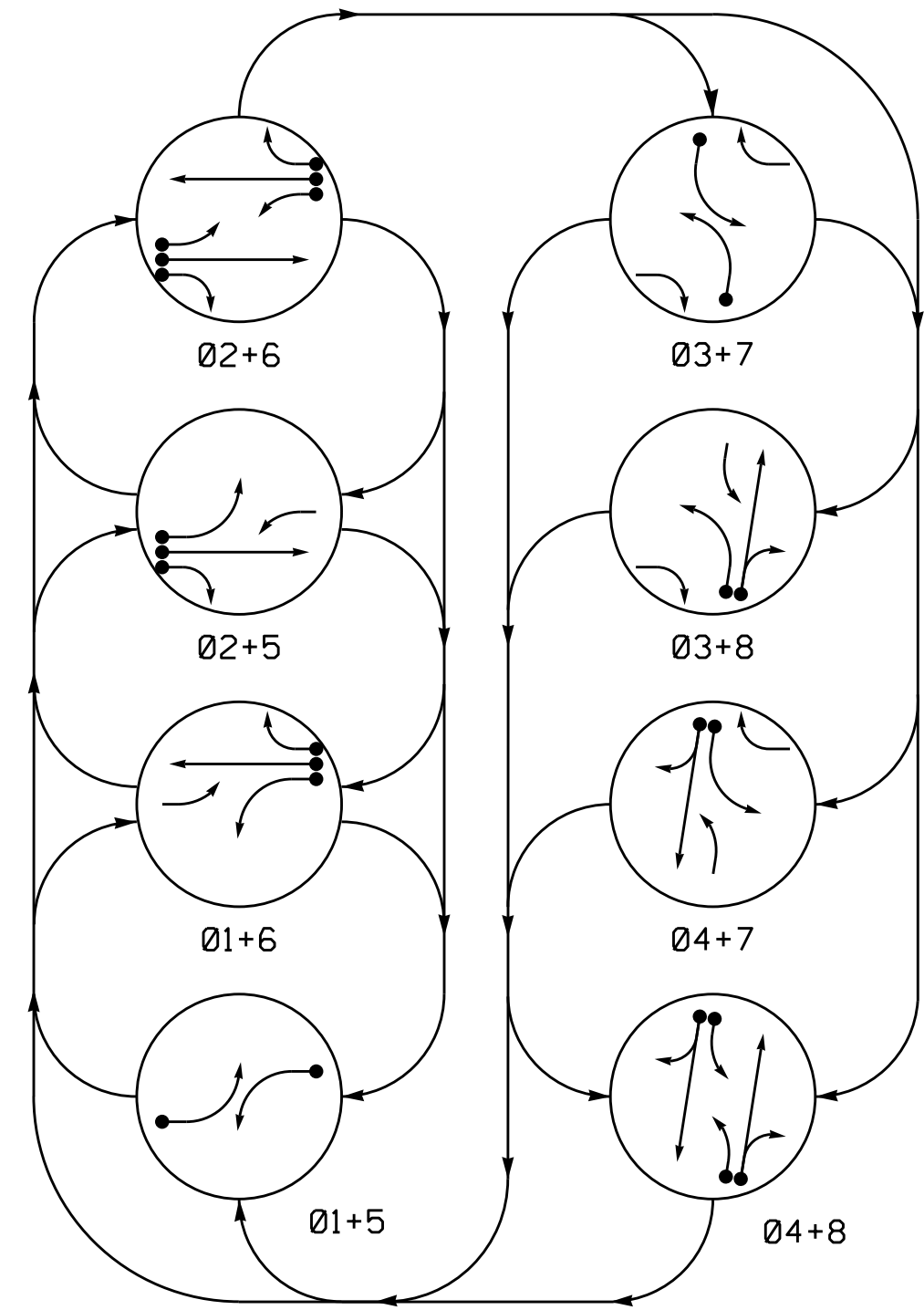
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0444T1
DESIGNED: August 2017
SEALED: 9/8/2017
REVISED:

Electrical Detail - Temp 1 - Sheet 2 of 2

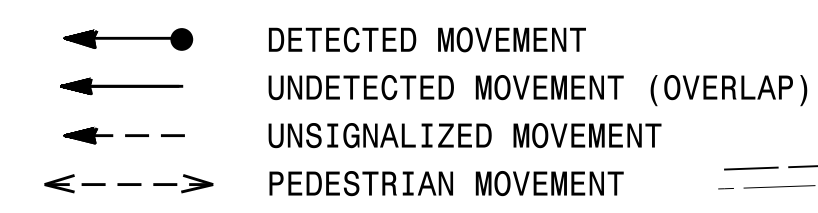
Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	DETAILS FOR: SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL PROFESSIONAL ENGINEER D. TODD JOYCE 031001
Division 4 Johnston County Smithfield	PLAN DATE: August 2017 REVIEWED BY: T. Joyce	SEAL 031001
PREPARED BY: C. Strickland	REVIEWED BY:	ENGINEER D. Todd Joyce 9/13/2017
REVISIONS	INIT. DATE	DATE
SIG. INVENTORY NO. 04-0444T1		DATE

13-SEP-2017 09:50
 S:\PROJECTS\13-SEP-2017\SIGNAL\work\hgr\oups\sig_Maps\13-11-17\ck_lan@040444_sm.ele.xxx.dgn
 C:\Users\TCK1\and

PHASING DIAGRAM

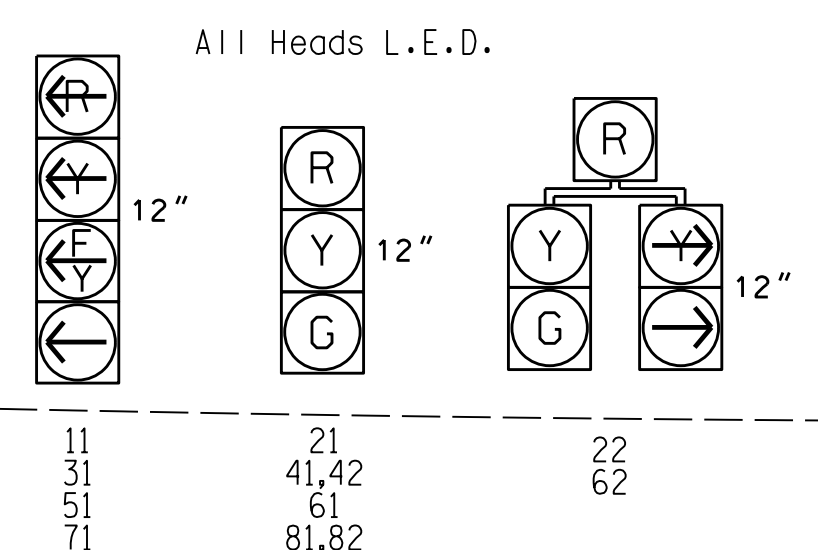


PHASING DIAGRAM DETECTION LEGEND



SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	←	←	←	←	←	←	←	←
41,42	R	R	R	R	R	R	G	G
51	←	←	←	←	←	←	←	←
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71	←	←	←	←	←	←	←	←
81,82	R	R	R	R	R	G	R	G

SIGNAL FACE I.D.



OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

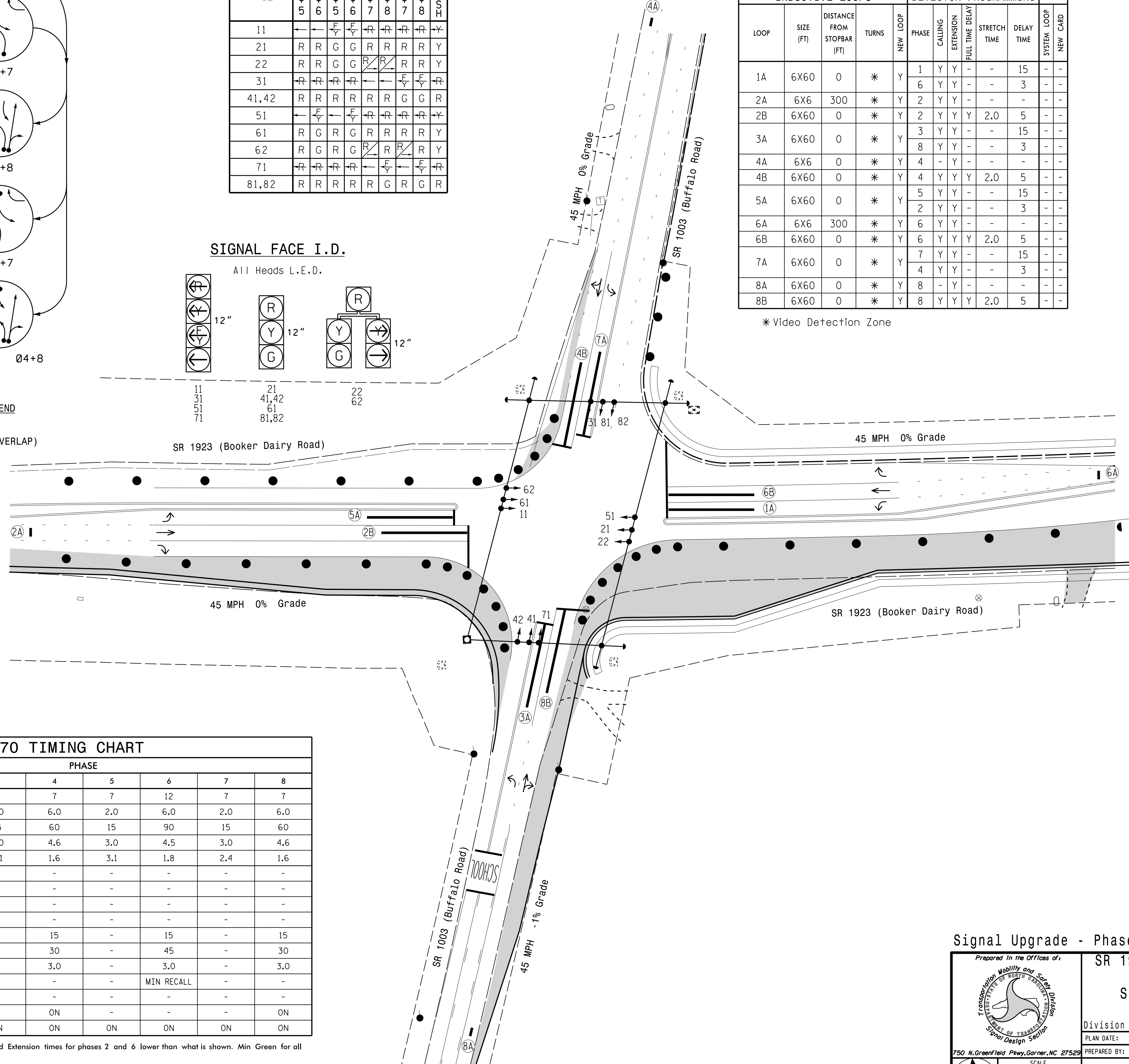
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING				SYSTEM LOOP	NEW CARD
					PHASE	CALLING	EXTENSION	STRETCH TIME		
1A	6X60	0	*	Y	1	Y	Y	-	15	-
2A	6X6	300	*	Y	2	Y	Y	-	-	-
2B	6X60	0	*	Y	2	Y	Y	2.0	5	-
3A	6X60	0	*	Y	3	Y	Y	-	15	-
4A	6X6	0	*	Y	4	Y	Y	-	-	-
4B	6X60	0	*	Y	4	Y	Y	2.0	5	-
5A	6X60	0	*	Y	5	Y	Y	-	15	-
6A	6X6	300	*	Y	6	Y	Y	-	-	-
6B	6X60	0	*	Y	6	Y	Y	2.0	5	-
7A	6X60	0	*	Y	7	Y	Y	-	15	-
8A	6X60	0	*	Y	8	Y	Y	-	3	-
8B	6X60	0	*	Y	8	Y	Y	2.0	5	-

* Video Detection Zone

8 Phase Fully Actuated Isolated

NOTES

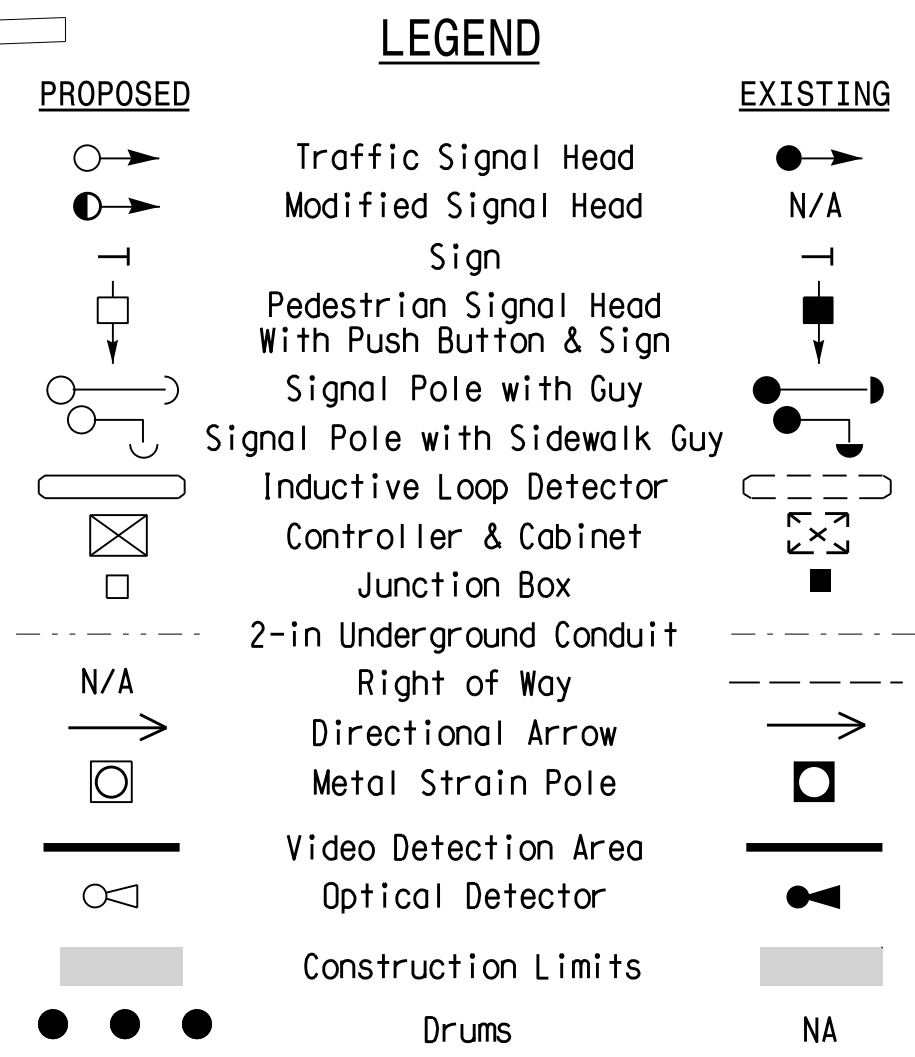
1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 1 and/or phase 5 may be lagged.
4. Phase 3 and/or phase 7 may be lagged.
5. Reposition existing signal heads numbered 11, 21, 22, 51, 61, 62.
6. Set all detector units to presence mode.



OASIS 2070 TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1 *	7	12	7	7	7	12	7	7
Extension 1 *	2.0	6.0	2.0	6.0	2.0	6.0	2.0	6.0
Max Green 1 *	15	90	15	60	15	90	15	60
Yellow Clearance	3.0	4.5	3.0	4.6	3.0	4.5	3.0	4.6
Red Clearance	3.1	1.8	3.1	1.6	3.1	1.8	2.4	1.6
Walk 1 *	-	-	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-	-	-
Seconds Per Actuation *	-	-	-	-	-	-	-	-
Max Variable Initial *	-	-	-	-	-	-	-	-
Time Before Reduction *	-	15	-	15	-	15	-	15
Time To Reduce *	-	45	-	30	-	45	-	30
Minimum Gap	-	3.0	-	3.0	-	3.0	-	3.0
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	-	-	-	-	-	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Phase 2 - Temp 2

Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: JPG

PREPARED BY: KGP, Jr. REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

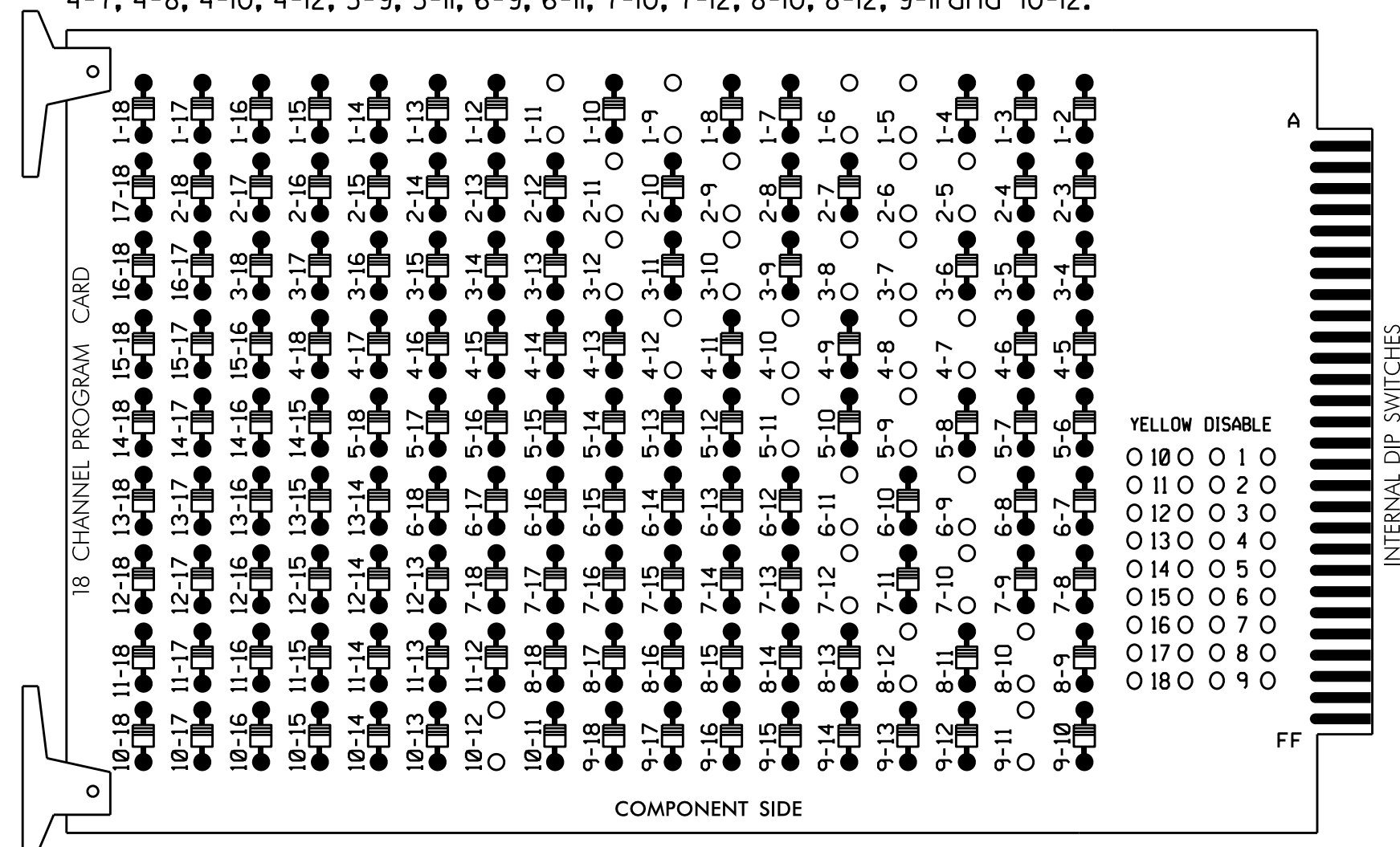
9/8/2017

SIG. INVENTORY NO. 04-044412

EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11 and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2, 4, 6 and 8 for Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,
 AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

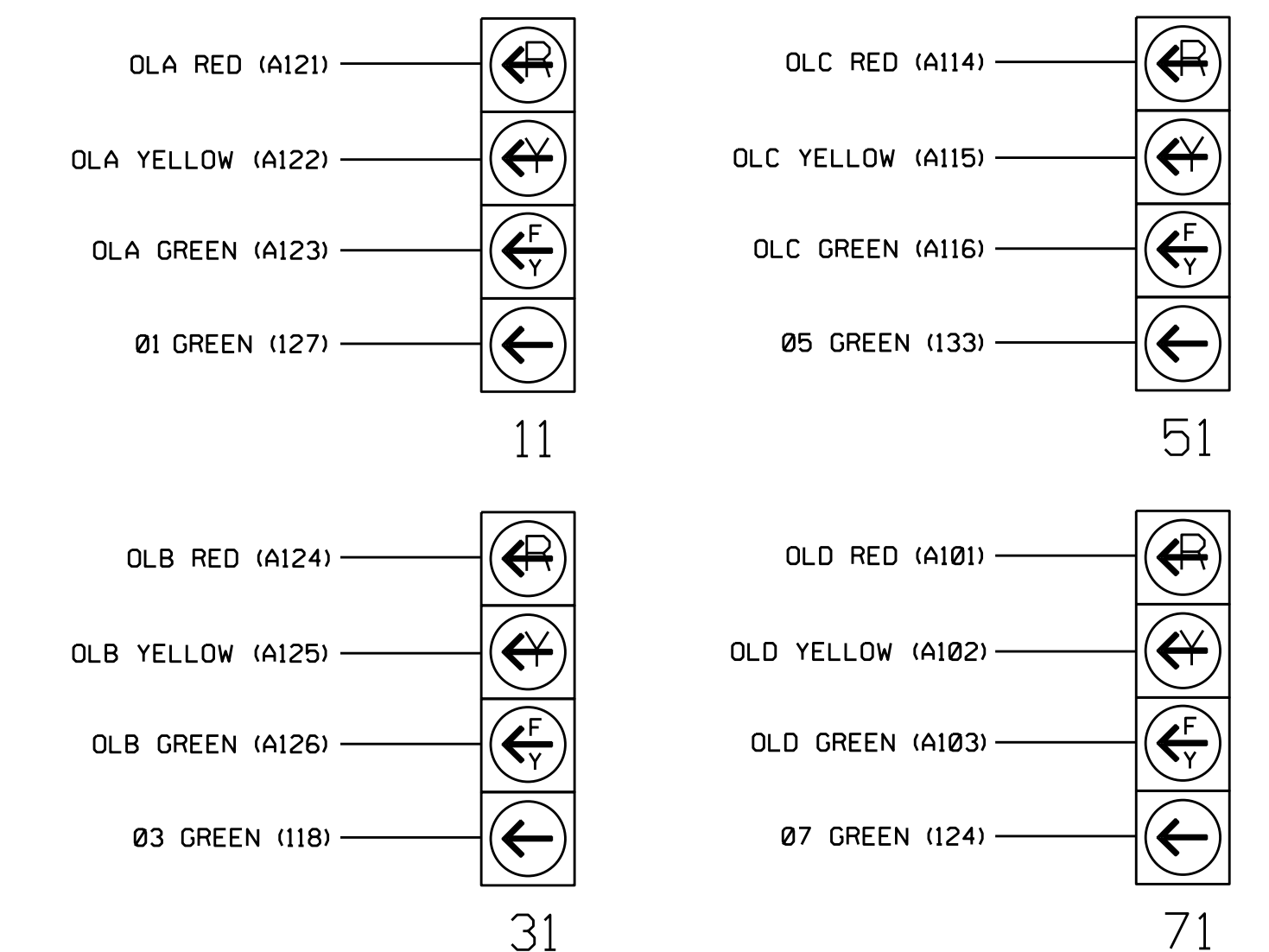
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22	NU	22	31	41,42	NU	51	61,62	NU	62	71	81,82	NU	11	31	51	71	NU
RED	128		*		101			134		*		107							
YELLOW	*	129			102		*	135				108							
GREEN		130			103			136				109							
RED ARROW													A121	A124		A114	A101		
YELLOW ARROW				117							123		A122	A125		A115	A102		
FLASHING YELLOW ARROW													A123	A126		A116	A103		
GREEN ARROW	127			118	118			133		124	124								

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

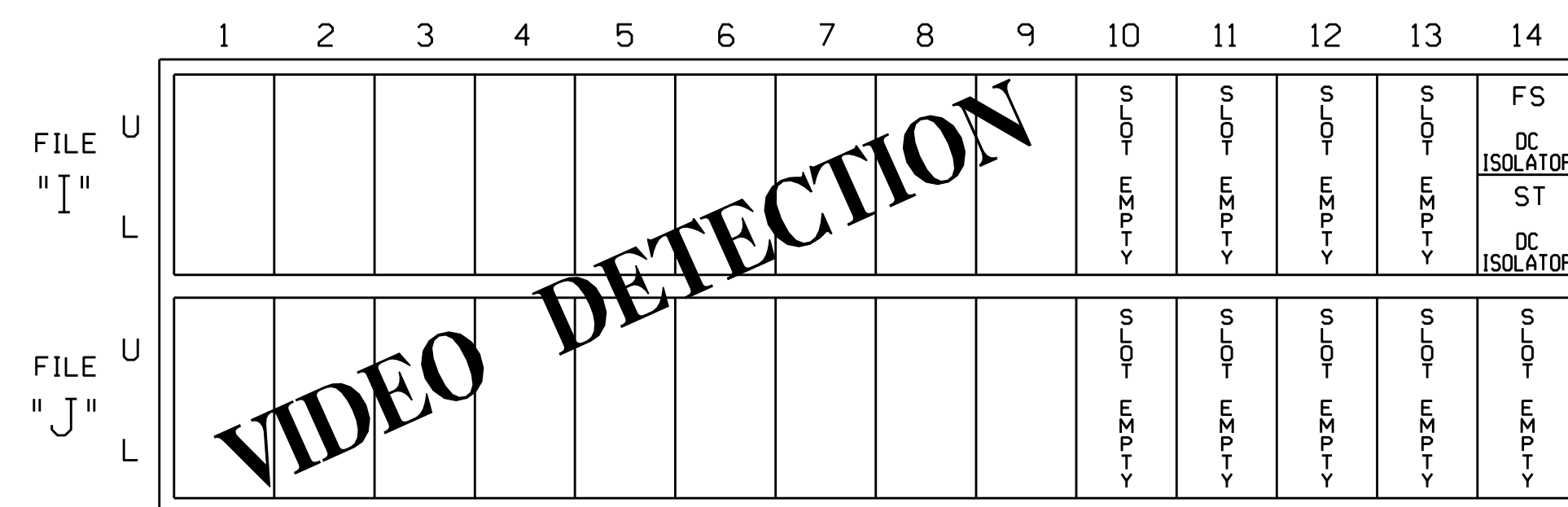


NOTE

The sequence display for signal heads 11, 31, 51, and 71 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

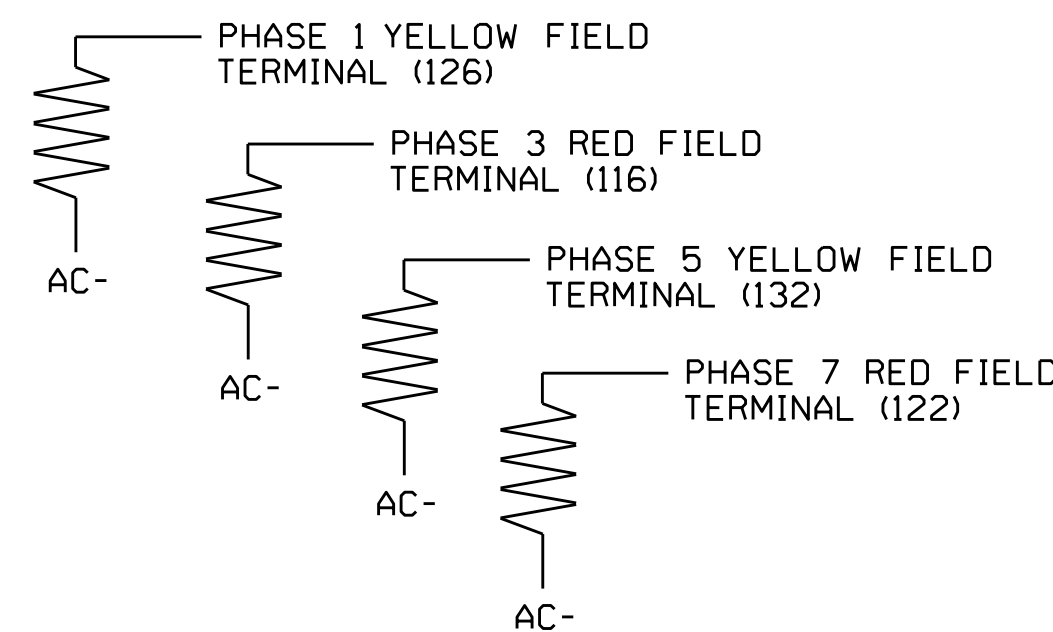
SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



Electrical Detail - Temp 2 - Sheet 1 of 2

Electrical and Programming Details for: SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)

Prepared In the Offices of: [Logo]

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

Seal: SEAL 031001 ENGINEER TODD JOYCE

DocuSigned by: D. Todd Joyce 9/13/2017

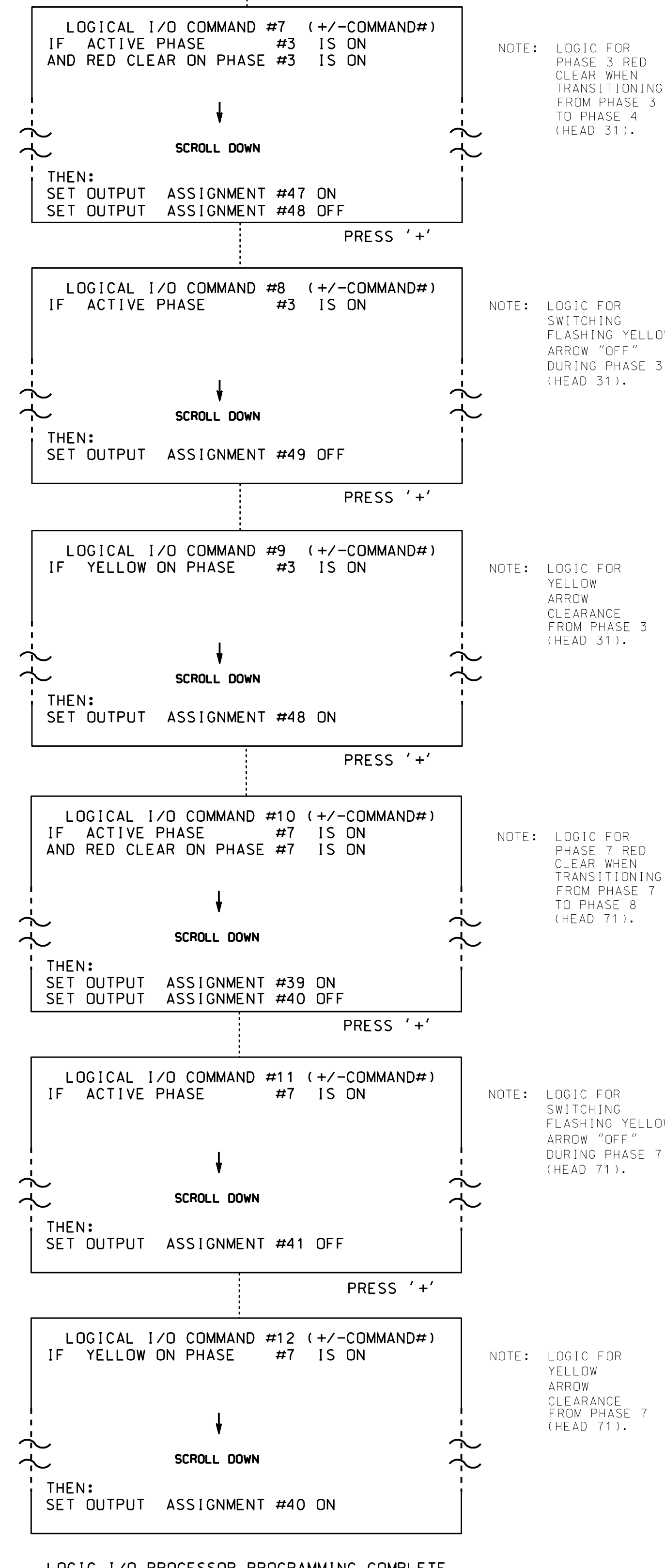
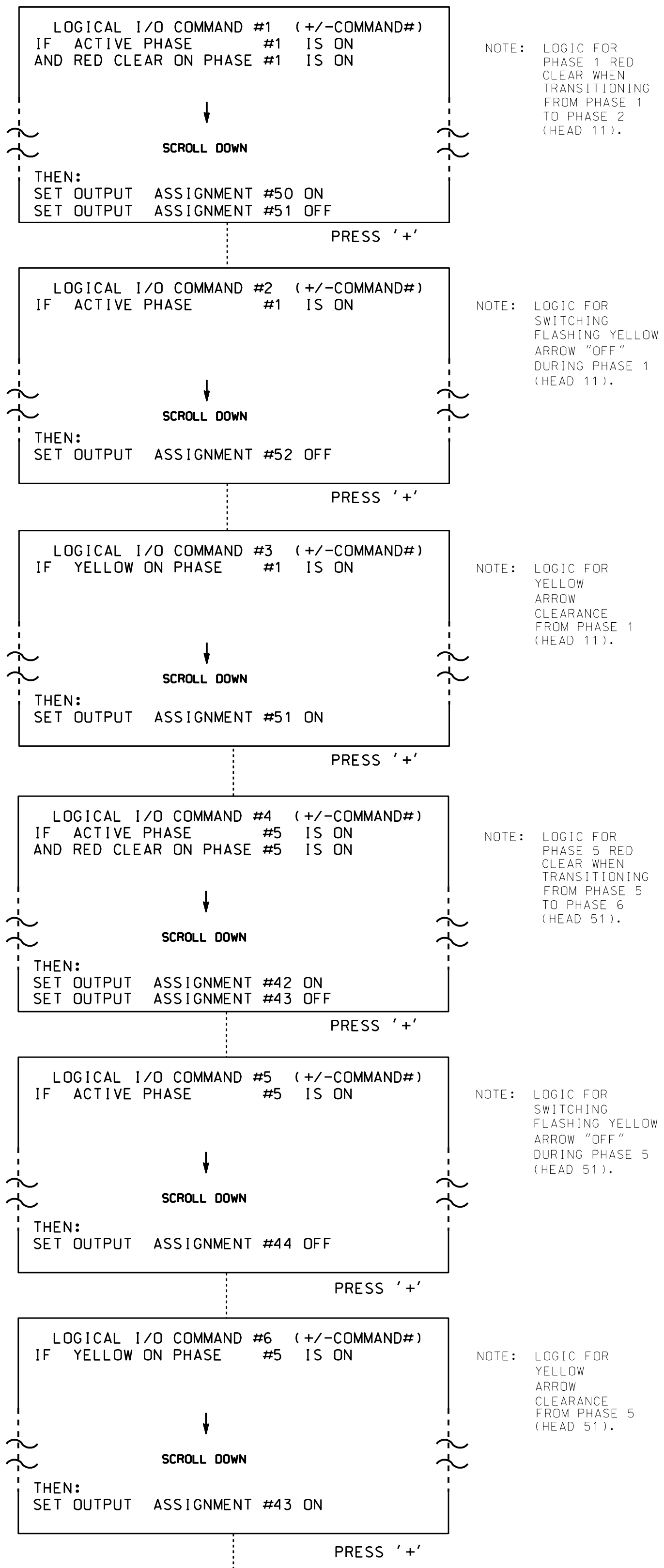
SIG. INVENTORY NO. 04-0444T2

13-SEP-2017 10:26 S:\IT\ASIS\115\SigHead\work\hgr\oas\sig_head\040444_sml_e.xxx.dgn

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

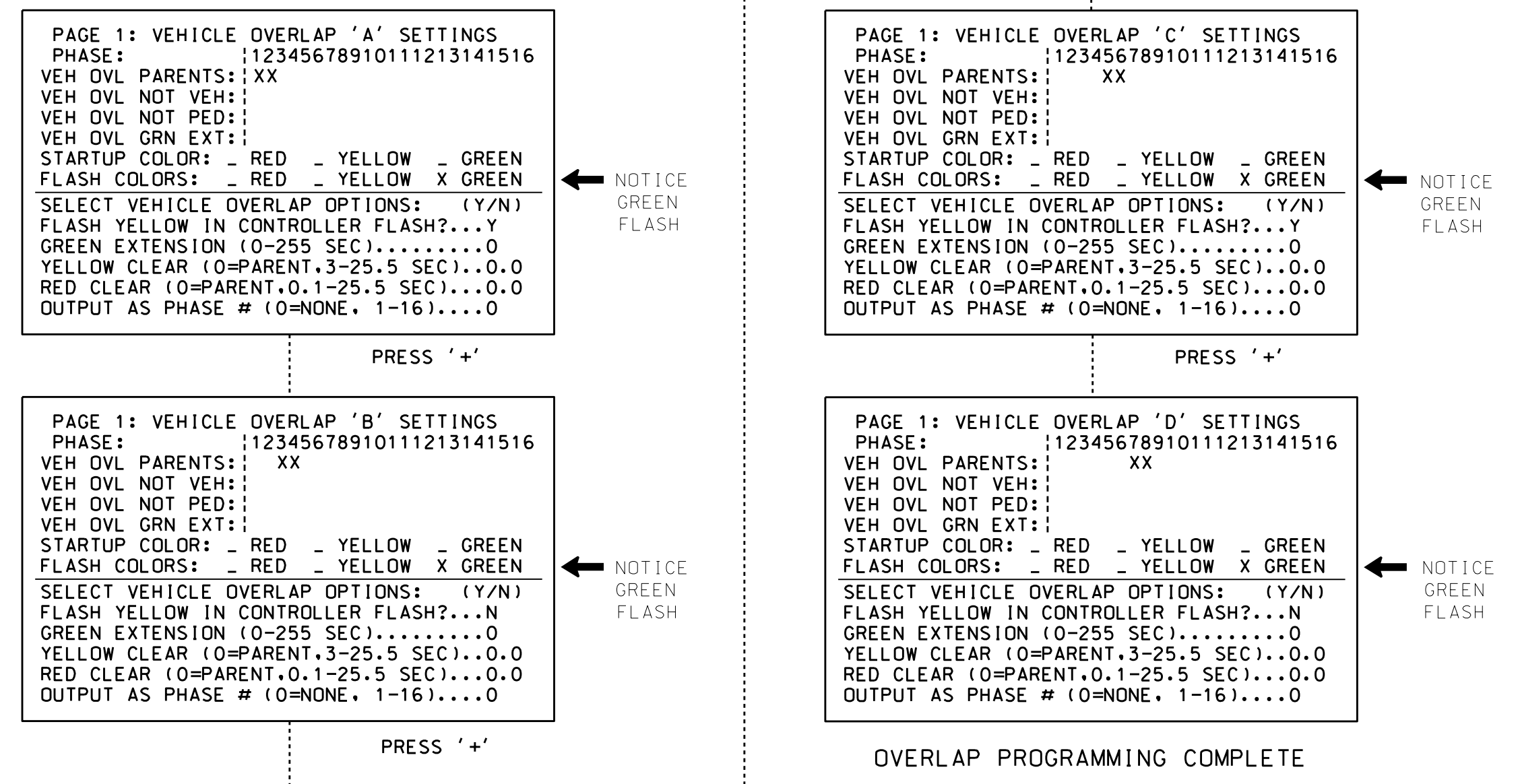


LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

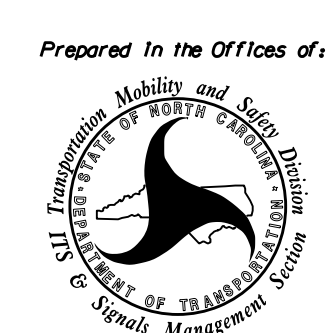
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT REFERENCE SCHEDULE
USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 =	Overlap D Red
OUTPUT 40 =	Overlap D Yellow
OUTPUT 41 =	Overlap D Green
OUTPUT 42 =	Overlap C Red
OUTPUT 43 =	Overlap C Yellow
OUTPUT 44 =	Overlap C Green
OUTPUT 47 =	Overlap B Red
OUTPUT 48 =	Overlap B Yellow
OUTPUT 49 =	Overlap B Green
OUTPUT 50 =	Overlap A Red
OUTPUT 51 =	Overlap A Yellow
OUTPUT 52 =	Overlap A Green

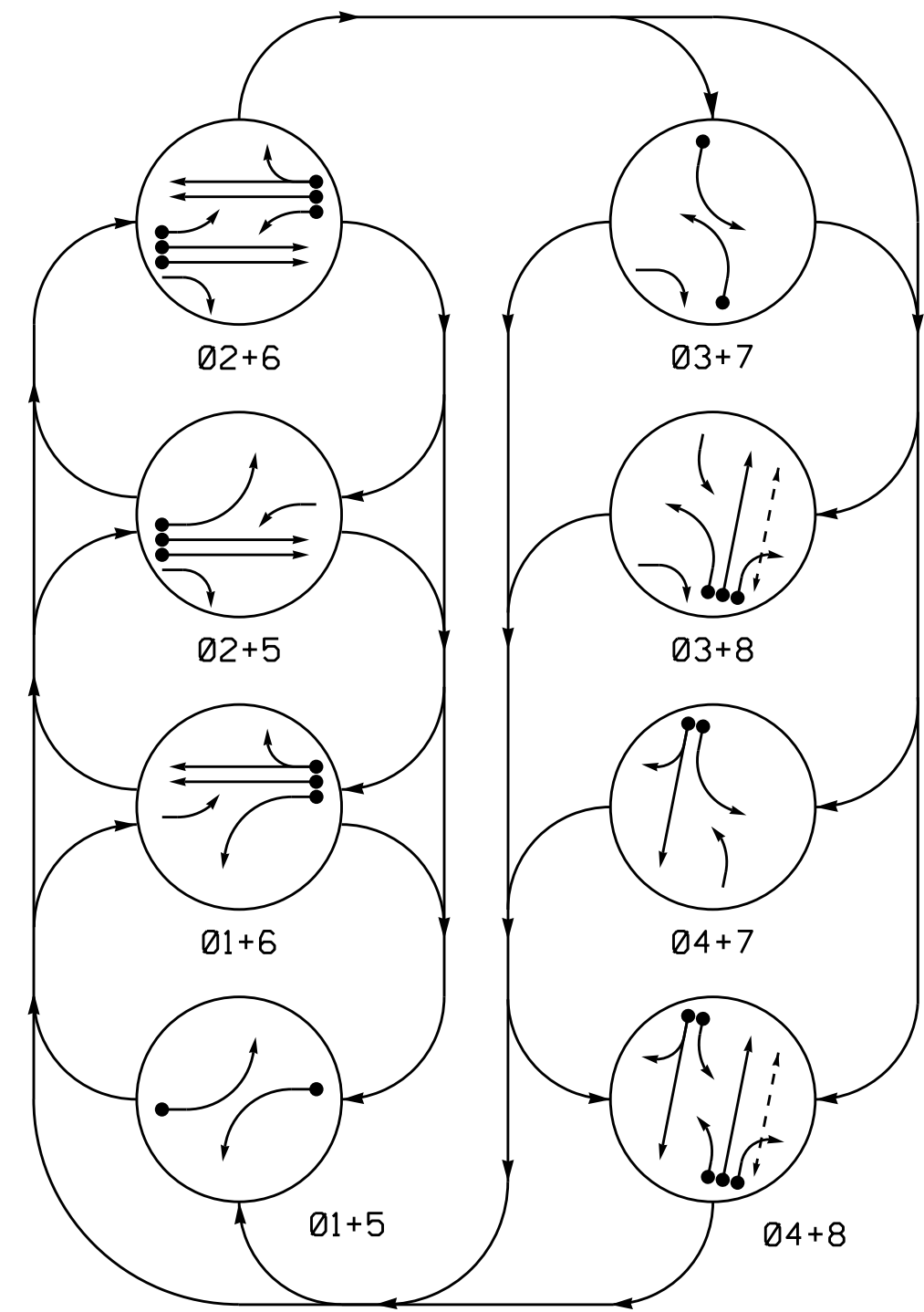
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-0444T2
DESIGNED: August 2017
SEALED: 9/8/2017
REVISED:

Electrical Detail - Temp 2 - Sheet 2 of 2

 <p style="font-size: 8px;">750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)</p> <p>Division 4 Johnston County Smithfield</p> <p>PLAN DATE: August 2017 REVIEWED BY: T. Joyce</p> <p>PREPARED BY: C. Strickland REVIEWED BY:</p> <table border="1" style="width: 100%; font-size: 8px;"> <tr><th>REVISIONS</th><th>INIT.</th><th>DATE</th></tr> <tr><td> </td><td> </td><td> </td></tr> <tr><td> </td><td> </td><td> </td></tr> </table>	REVISIONS	INIT.	DATE							<p>DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED</p> <p style="font-size: 8px;">SEAL STATE OF NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 031001 D. Todd Joyce 9/13/2017 SIG. INVENTORY NO. 04-0444T2</p>
REVISIONS	INIT.	DATE									

I:\3-SEP-2017_10:28
S:\IT\ASIS\IT\IS\Sig\Signal\work\hgr\oups\sig_Maps\Strickland\040444_sml_e_xxr.dgn
C:\Users\strickland

PHASING DIAGRAM



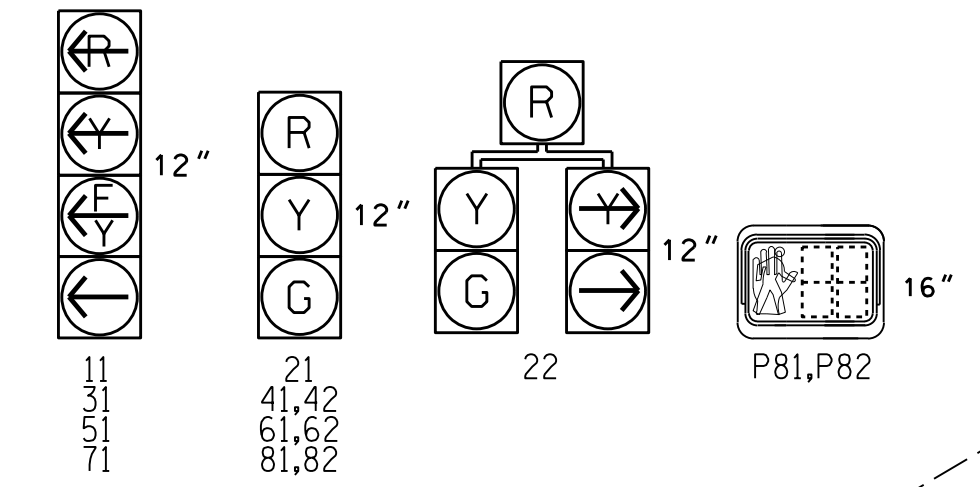
PHASING DIAGRAM DETECTION LEGEND

- ← ● → DETECTED MOVEMENT
- ← ○ → UNDETECTED MOVEMENT (OVERLAP)
- ← - - - → UNSIGNALIZED MOVEMENT
- ← - - - P → PEDESTRIAN MOVEMENT

SIGNAL FACE	PHASE							
	01+5	02+5	03+5	04+5	01+6	02+6	03+6	04+6
11	←	←	←	←	←	←	←	←
21	R	R	G	G	R	R	R	Y
22	R	R	G	G	R	R	R	Y
31	←	←	←	←	←	←	←	←
41,42	R	R	R	R	R	R	G	G
51	←	←	←	←	←	←	←	←
61,62	R	G	R	G	R	R	R	Y
71	←	←	←	←	←	←	←	←
81,82	R	R	R	R	R	G	R	G
P81,P82	DW	DW	DW	DW	DW	W	DW	W

SIGNAL FACE I.D.

All Heads L.E.D.



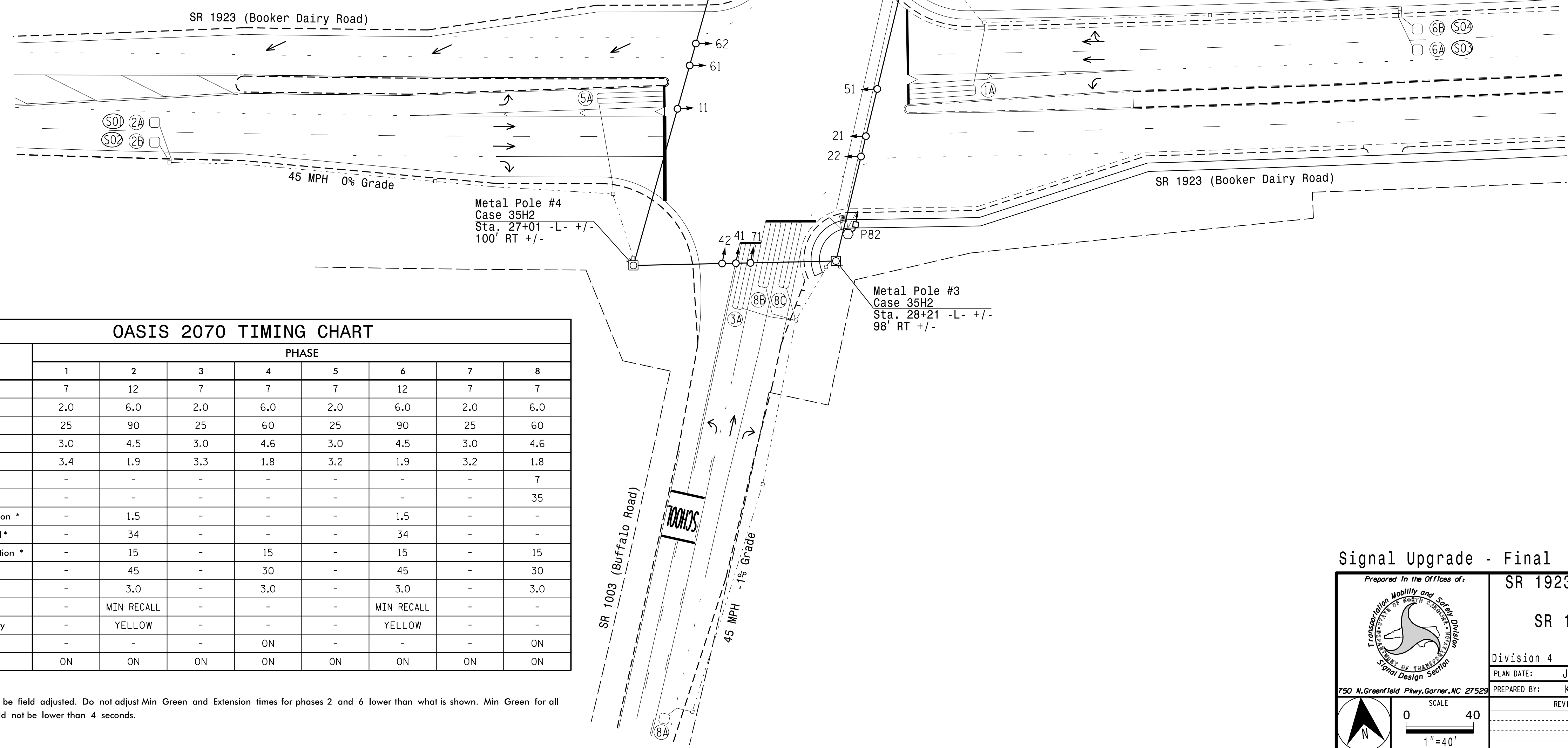
OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING				SYSTEM LOOP	NEW CARD	
					PHASE	CALLING	EXTENSION	FULL TIME DELAY			
1A	6X40	0	2-4-2	Y	1	Y	Y	-	15	-	Y
2A/S01	6X6	300	6	Y	2	Y	Y	-	-	-	Y
2B/S02	6X6	300	6	Y	2	Y	Y	-	-	-	Y
3A	6X40	0	2-4-2	Y	3	Y	Y	-	15	-	Y
4A	6X6	300	5	Y	4	-	Y	-	-	-	Y
4B	6X40	0	2-4-2	Y	4	Y	Y	2.0	5	-	Y
5A	6X40	0	2-4-2	Y	5	Y	Y	-	15	-	Y
6A/S03	6X6	300	4	Y	6	Y	Y	-	-	-	Y
6B/S04	6X6	300	4	Y	6	Y	Y	-	-	-	Y
7A	6X40	0	2-4-2	Y	7	Y	Y	-	15	-	Y
8A	6X6	300	6	Y	8	-	Y	-	-	-	Y
8B	6X40	0	2-4-2	Y	8	Y	Y	2.0	5	-	Y
8C	6X40	0	2-4-2	Y	8	Y	Y	-	15	-	Y

8 Phase Fully Actuated Booker Dairy Road CLS

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Phase 3 and/or phase 7 may be lagged.
- Set all detector units to presence mode.
- Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
- Program pedestrian heads to countdown the flashing "Don't Walk" time only.
- Pedestrian pedestals are conceptual and shown for reference only. See sheets P1-P3 for pushbutton location details.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- Closed loop system data: Controller Asset #: 0444.



OASIS 2070 TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1 *	7	12	7	7	7	12	7	7
Extension 1 *	2.0	6.0	2.0	6.0	2.0	6.0	2.0	6.0
Max Green 1 *	25	90	25	60	25	90	25	60
Yellow Clearance	3.0	4.5	3.0	4.6	3.0	4.5	3.0	4.6
Red Clearance	3.4	1.9	3.3	1.8	3.2	1.9	3.2	1.8
Walk 1 *	-	-	-	-	-	-	-	7
Don't Walk 1	-	-	-	-	-	-	-	35
Seconds Per Actuation *	-	1.5	-	-	-	1.5	-	-
Max Variable Initial *	-	34	-	-	-	34	-	-
Time Before Reduction *	-	15	-	15	-	15	-	15
Time To Reduce *	-	45	-	30	-	45	-	30
Minimum Gap	-	3.0	-	3.0	-	3.0	-	3.0
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	YELLOW	-	-	-	YELLOW	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND

- | PROPOSED | EXISTING |
|--|--|
| ○ → Traffic Signal Head | ● → N/A |
| ○ → Modified Signal Head | ○ → N/A |
| ⊥ Sign | ⊥ Sign |
| ⊥ Pedestrian Signal Head With Push Button & Sign | ⊥ Pedestrian Signal Head With Push Button & Sign |
| ○ Signal Pole with Guy | ○ Signal Pole with Guy |
| ○ Signal Pole with Sidewalk Guy | ○ Signal Pole with Sidewalk Guy |
| ⊗ Inductive Loop Detector | ⊗ Inductive Loop Detector |
| □ Controller & Cabinet | □ Controller & Cabinet |
| □ Junction Box | □ Junction Box |
| - - - 2-in Underground Conduit | - - - 2-in Underground Conduit |
| N/A Right of Way | N/A Right of Way |
| → Directional Arrow | → Directional Arrow |
| ○ Metal Strain Pole | ○ Metal Strain Pole |
| ○ Type II Signal Pedestal | ○ Type II Signal Pedestal |
| ○ Curb Ramp | ○ Curb Ramp |
| ⊗ Master Controller & Cabinet | ⊗ Master Controller & Cabinet |
| ⊗ Right Arrow "ONLY" Sign (R3-5R) | ⊗ Right Arrow "ONLY" Sign (R3-5R) |

Signal Upgrade - Final

Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)

Division 4 Johnston County Smithfield

PLAN DATE: June 2017 REVIEWED BY: JPG

PREPARED BY: KGP, Jr. REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

DATE: 9/8/2017

SIG. INVENTORY NO. 04-0444

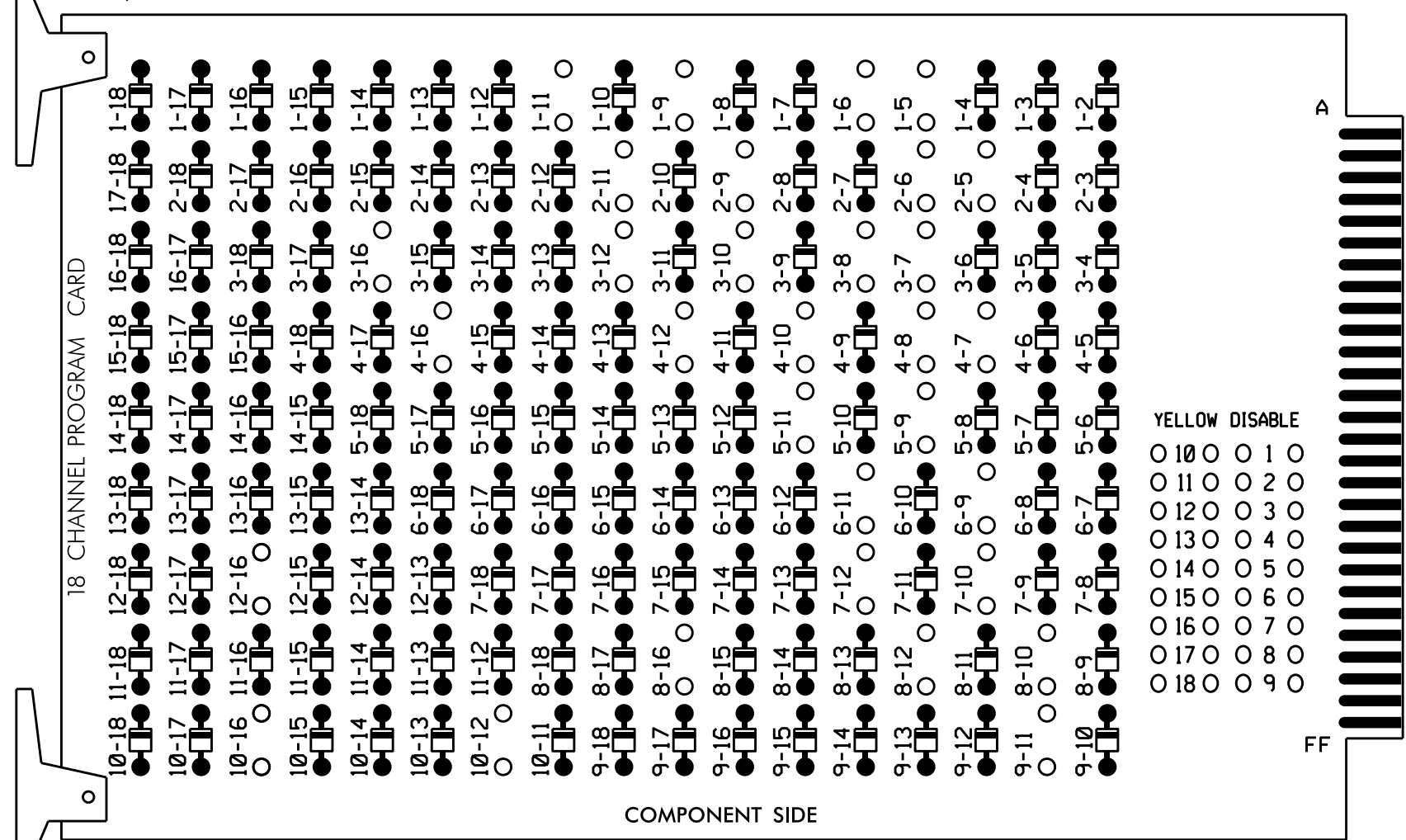
04-0444-2017_11:40
 R:\Projects\0444\Signal\0444-Final\040444-4-1.dgn
 10/11/2017 11:40 AM

EDI MODEL 2018ECL-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

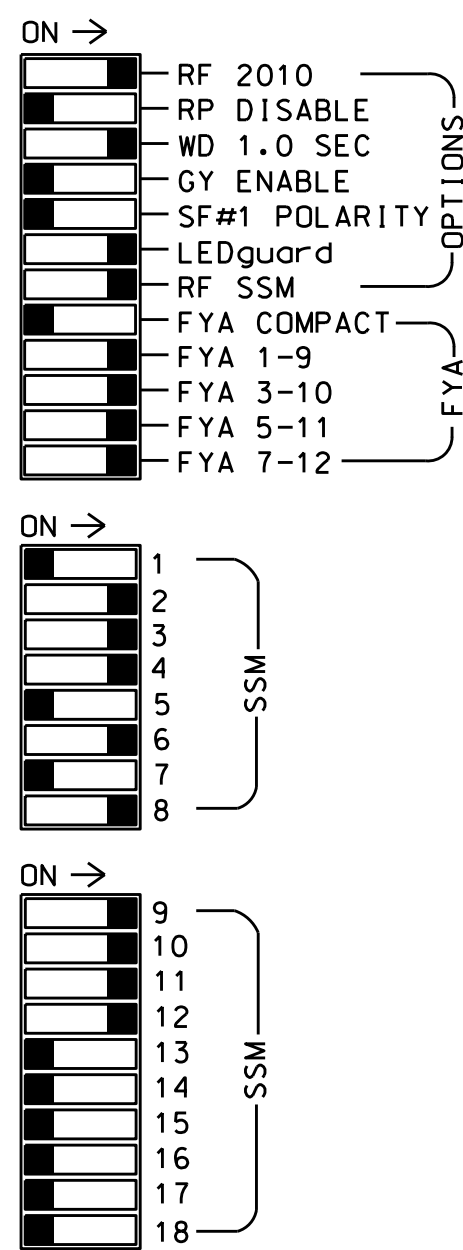
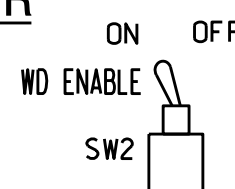
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 3-16, 4-7, 4-8, 4-10, 4-12, 4-16, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 8-16, 9-11, 10-12, 10-16 and 12-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



■ = DENOTES POSITION OF SWITCH

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and phases 2, 4, 6 and 8 for Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phase 8 for Startup Ped Call.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the Booker Dairy Road Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,S12,
 AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8,8 PED
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	11	21,22	NU	22	31	41,42	NU	51	61,62	NU	71	81,82	P81, P82	11	31	NU	51	71	NU
RED		128		*	101			134			107								
YELLOW	*	129			102		*	135		*	108								
GREEN		130			103			136			109								
RED ARROW														A121	A124		A114	A101	
YELLOW ARROW				117										A122	A125		A115	A102	
FLASHING YELLOW ARROW														A123	A126		A116	A103	
GREEN ARROW	127			118	118			133			124								
Hand													110						
Person																			112

NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 2/SYS	∅ 3	∅ 4	∅ 5	∅ 6/SYS	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14
L	1A	2A/S01	3A	4A	5A	6A/S03	7A	8A	9A	10A	11A	12A	13A	14A
U	NOT USED	∅ 2/SYS	NOT USED	∅ 4	∅ 5	∅ 6/SYS	∅ 7	∅ 8	∅ 9	∅ 10	∅ 11	∅ 12	∅ 13	∅ 14
L	2B/S02	2B/S02	NOT USED	4B	5A	6B/S04	7A	8B	9A	10A	11A	12A	13A	14A

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

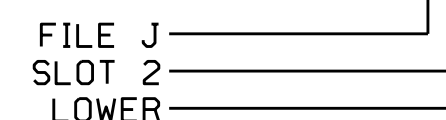
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A/S01	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B/S02	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
3A ²	TB4-5,6	I5U	58	20	3	3	Y	Y			15
	-	J8U	50	12	28	8	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4		Y			
4B	TB4-11,12	I6L	45	7	14	4	Y	Y	Y	2.0	5
5A ³	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
6A/S03	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S04	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
7A ⁴	TB5-5,6	J5U	57	19	7	7	Y	Y			15
	-	I8U	49	11	24	4	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8		Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y	Y	2.0	5
PED PUSH BUTTONS											
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOT 113.

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from I5-W to J8-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.
- Add jumper from J5-W to I8-W, on rear of input file.

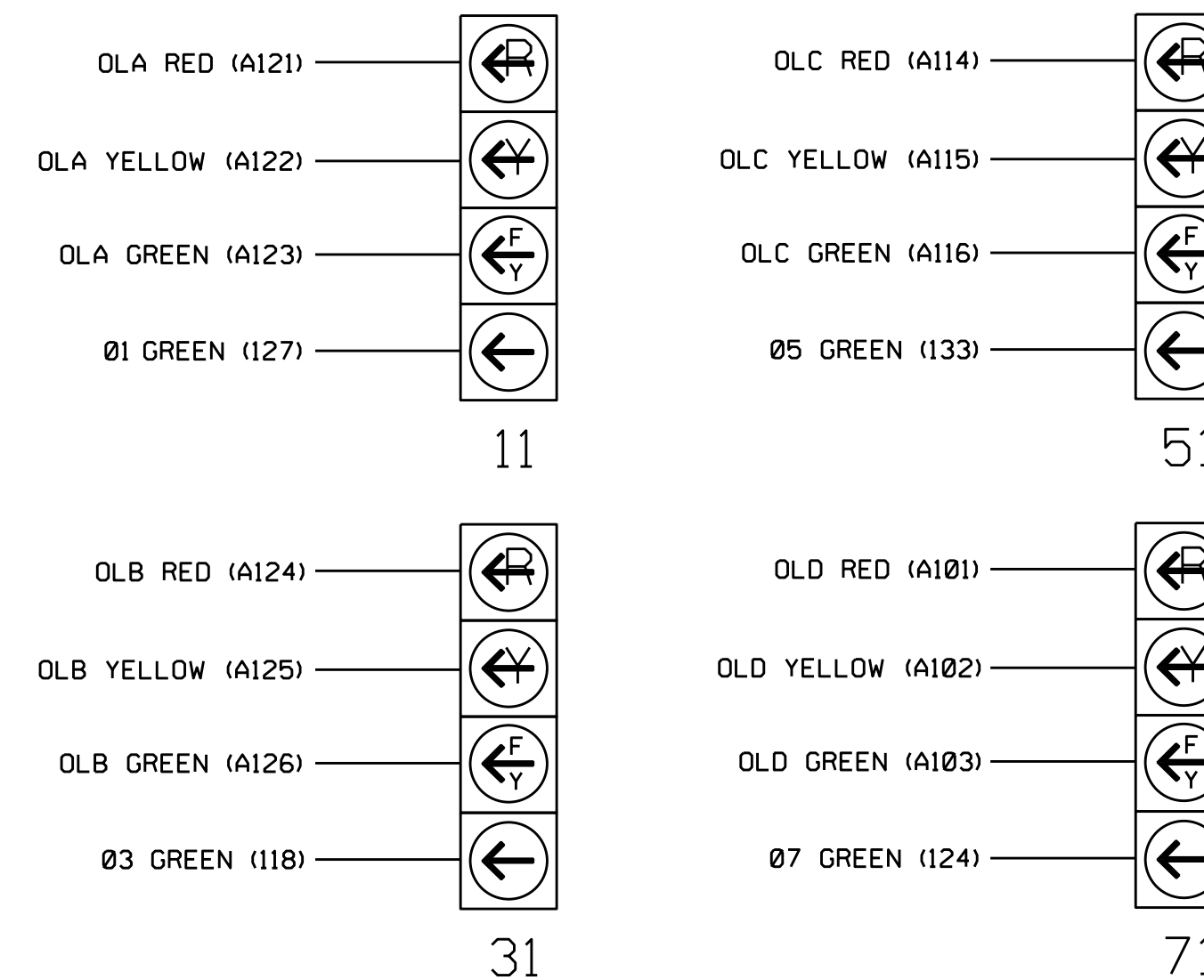
INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0444
 DESIGNED: June 2017
 SEALED: 9/8/2017
 REVISED:

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



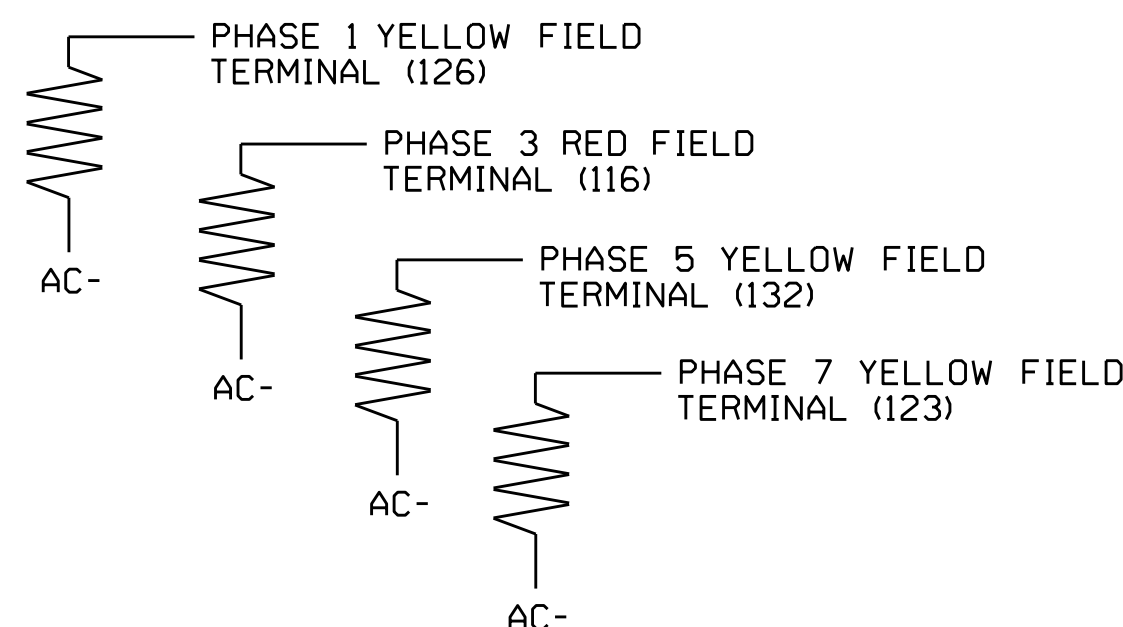
NOTE

The sequence display for signal heads 11, 31, 51, and 71 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Electrical Detail - Final - Sheet 1 of 2

SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

SEAL 031001

ENGINEER D. TODD JOYCE

DocuSigned by: D. Todd Joyce 9/13/2017

SIG. INVENTORY NO. 04-0444

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.
- FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
AND RED CLEAR ON PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #50 ON
SET OUTPUT ASSIGNMENT #51 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

```

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #52 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #51 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

```

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #44 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #6 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #43 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #7 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
AND RED CLEAR ON PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #47 ON
SET OUTPUT ASSIGNMENT #48 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

```

LOGICAL I/O COMMAND #8 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #49 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #9 (+/-COMMAND#)
IF YELLOW ON PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #48 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
AND RED CLEAR ON PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #39 ON
SET OUTPUT ASSIGNMENT #40 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

```

LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #41 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

```

LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF YELLOW ON PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #40 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

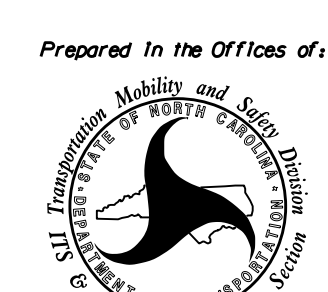
THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT REFERENCE SCHEDULE
USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39	=	Overlap D Red
OUTPUT 40	=	Overlap D Yellow
OUTPUT 41	=	Overlap D Green
OUTPUT 42	=	Overlap C Red
OUTPUT 43	=	Overlap C Yellow
OUTPUT 44	=	Overlap C Green
OUTPUT 47	=	Overlap B Red
OUTPUT 48	=	Overlap B Yellow
OUTPUT 49	=	Overlap B Green
OUTPUT 50	=	Overlap A Red
OUTPUT 51	=	Overlap A Yellow
OUTPUT 52	=	Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0444
DESIGNED: June 2017
SEALED: 9/8/2017
REVISED:

Electrical Detail - Final - Sheet 2 of 2

Prepared In the Offices of:

 750 N. Greenfield Pkwy, Garner, NC 27529

DETAILS FOR: SR 1923 (Booker Dairy Road) at SR 1003 (Buffalo Road)

Division 4 Johnston County Smithfield

PLAN DATE: August 2017	REVIEWED BY: T. Joyce
PREPARED BY: C. Strickland	REVIEWED BY:

REVISIONS: INIT. DATE

DocuSigned by: *D. Todd Joyce* 9/13/2017

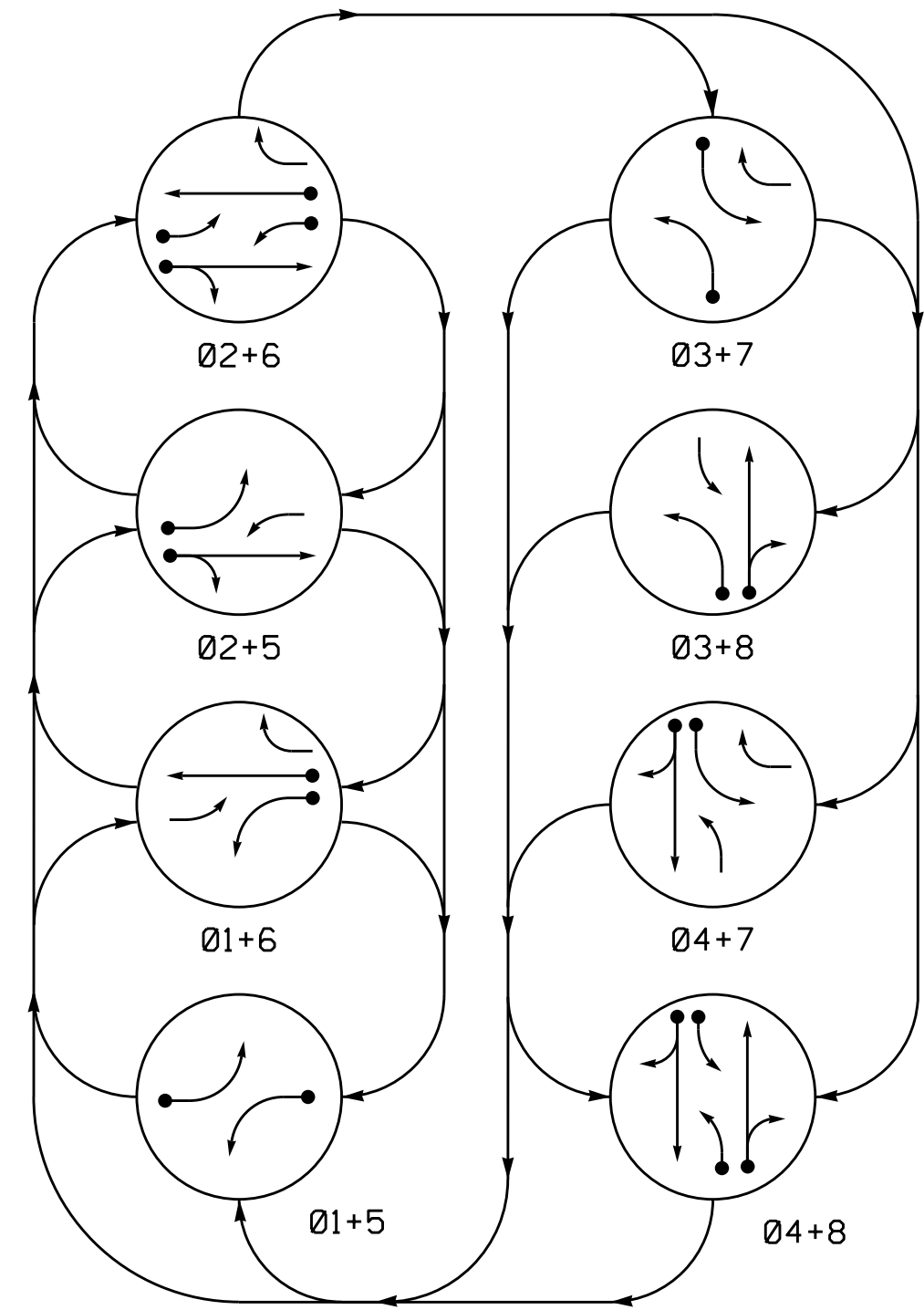
SEAL: SEAL 031001

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 04-0444

13-SEP-2017 10:36
 S:\IT\SS\JMS\Signal\work\hgr\oups\sig_Maps\Strickland\040444_sml_e_xxr.dgn
 C:\Users\jms\Documents\Signal Management\Signal

PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND

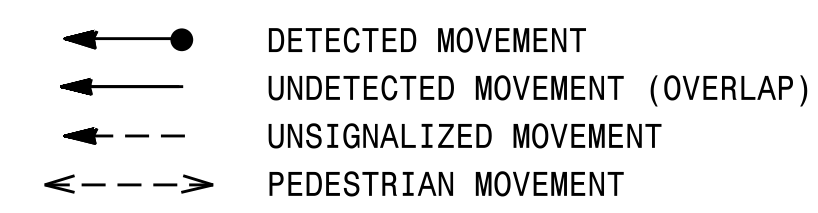
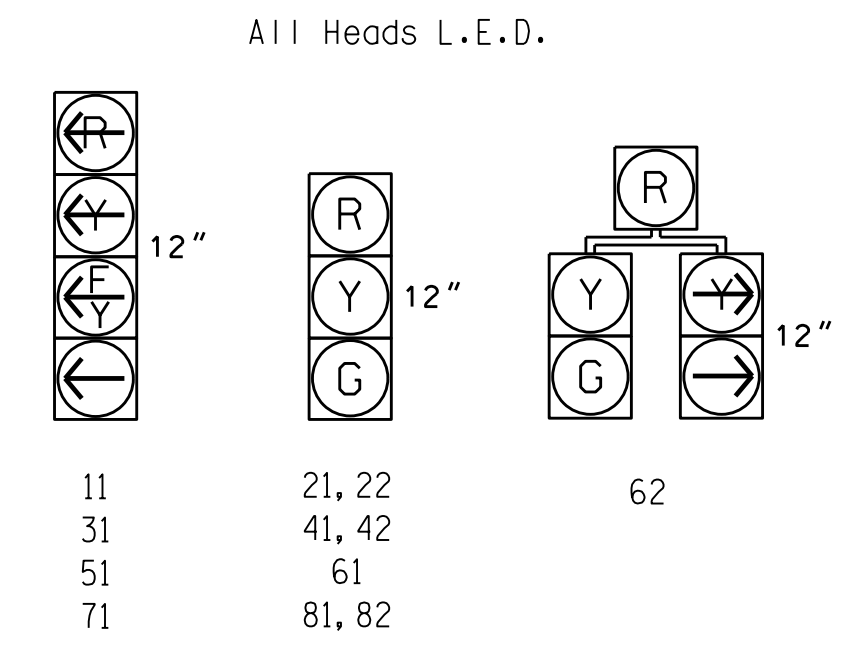


TABLE OF OPERATION

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	—	—	—	—	—	—	—	—
21,22	R	R	G	G	R	R	R	Y
31	R	R	R	R	—	—	—	—
41,42	R	R	R	R	R	R	G	G
51	—	—	—	—	—	—	—	—
61	R	G	R	G	R	R	R	Y
62	R	G	R	G	R	R	R	Y
71	R	R	R	R	—	—	—	—
81,82	R	R	R	R	R	G	R	G

SIGNAL FACE I.D.



OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

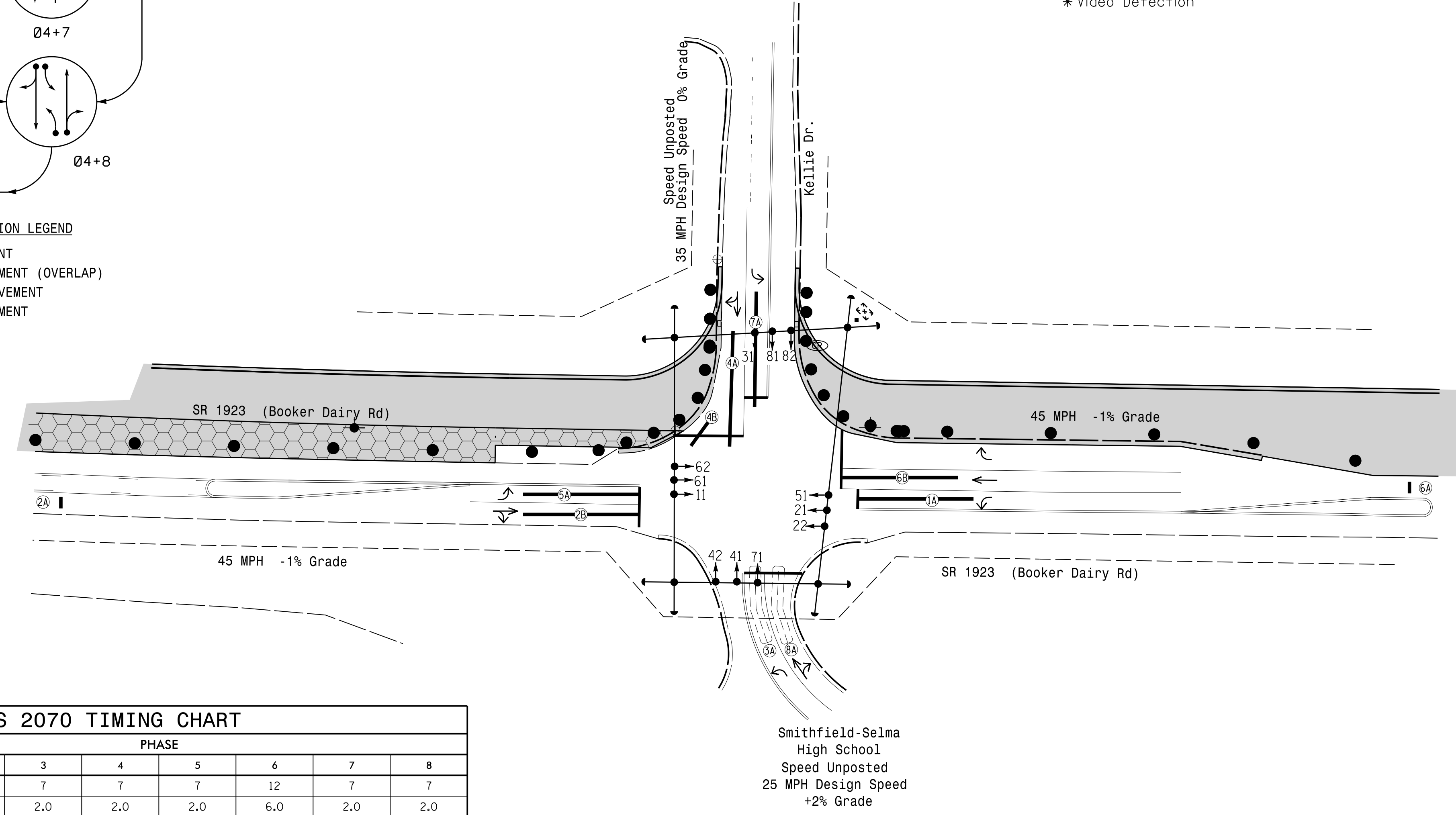
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	DETECTOR PROGRAMMING							
				NEW LOOP	PHASE	CALLING	EXTENSION	STRETCH TIME	DELAY TIME		
1A	6X40	+5	*	-	1	Y	Y	-	15	-	Y
2A	6X6	300	*	-	2	Y	Y	-	3	-	Y
2B	6X60	0	*	-	2	Y	Y	2.0	5	-	Y
3A	6X40	+5	2-4-2	-	3	Y	Y	-	3	-	-
4A	6X40	+5	*	-	4	Y	Y	-	10	-	Y
4B	6X15	+5	*	-	4	Y	Y	-	10	-	Y
5A	6X40	+5	*	-	5	Y	Y	-	15	-	Y
6A	6X6	300	*	-	6	Y	Y	-	3	-	Y
6B	6X60	0	*	-	6	Y	Y	2.0	5	-	Y
7A	6X40	+5	*	-	7	Y	Y	-	3	-	Y
8A	6X40	+5	2-4-2	-	8	Y	Y	-	10	-	-

* Video Detection

8 Phase Fully Actuated Isolated

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 1 and/or phase 5 may be lagged.
4. Phase 3 and/or phase 7 may be lagged.
5. Set all detector units to presence mode.

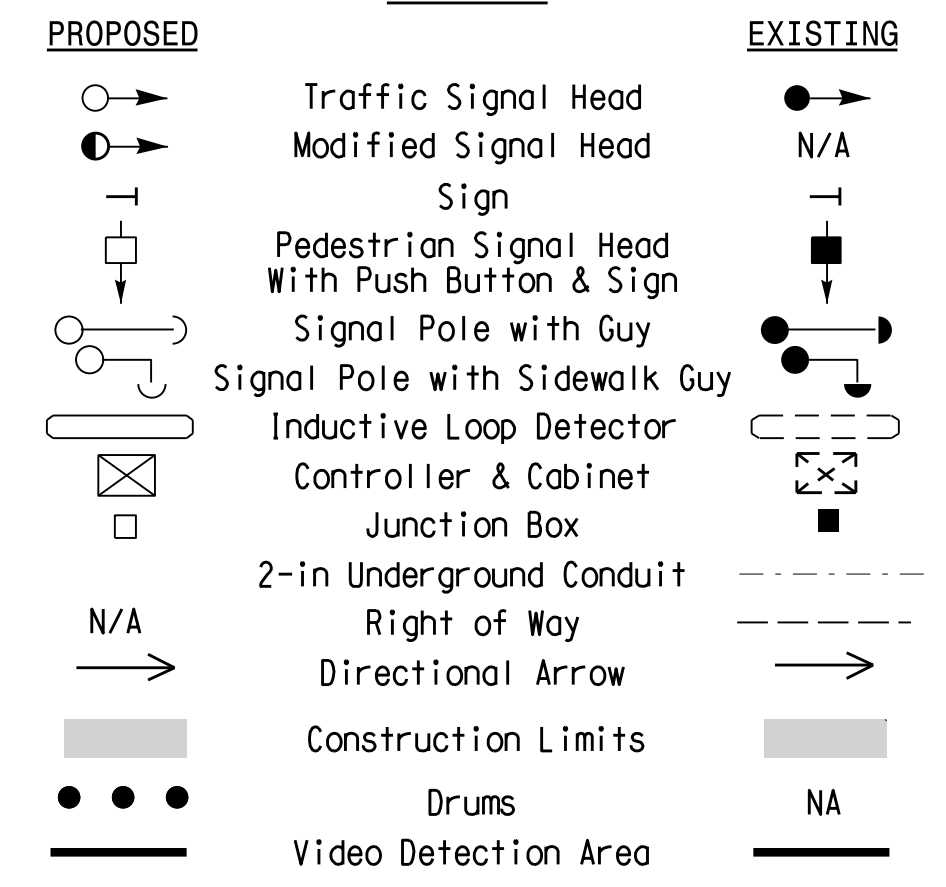


OASIS 2070 TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1 *	7	12	7	7	7	12	7	7
Extension 1 *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0
Max Green 1 *	15	90	15	20	15	90	15	20
Yellow Clearance	3.0	4.6	3.0	3.8	3.0	4.6	3.0	3.8
Red Clearance	2.6	1.3	2.3	2.2	2.6	1.3	2.3	2.2
Walk 1 *	-	-	-	-	-	-	-	-
Don't Walk 1	-	-	-	-	-	-	-	-
Seconds Per Actuation *	-	-	-	-	-	-	-	-
Max Variable Initial *	-	-	-	-	-	-	-	-
Time Before Reduction *	-	15	-	-	-	15	-	-
Time To Reduce *	-	45	-	-	-	45	-	-
Minimum Gap	-	3.0	-	-	-	3.0	-	-
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	-	-	-	-	-	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

LEGEND



Signal Upgrade - Phase 1 Temp 1

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1923 (Booker Dairy Road)
at
Kellie Dr/Smithfield-Selma
High School

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: JPG

PREPARED BY: KGP, Jr. REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

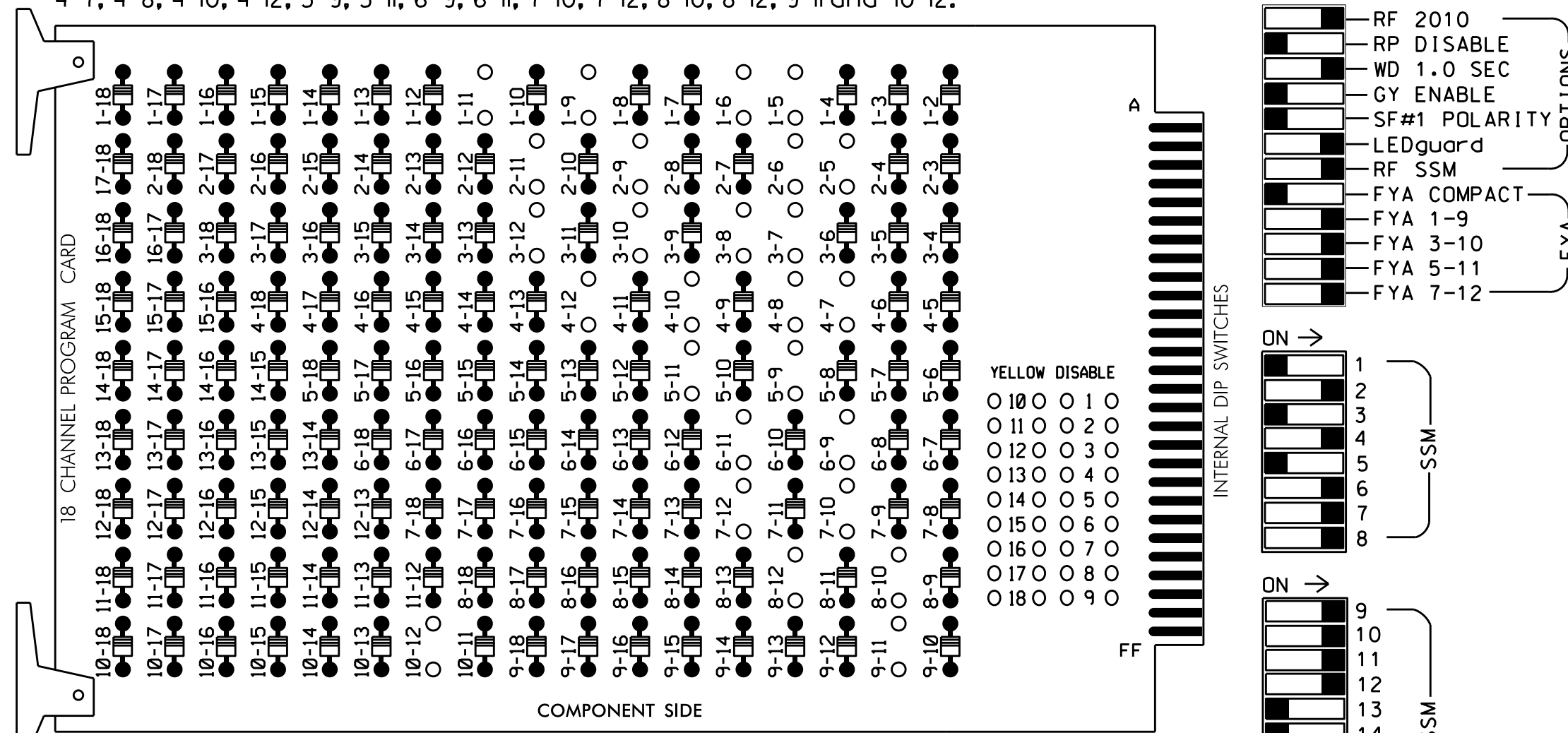
9/5/2017

SIG. INVENTORY NO. 04-1415T1

EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11 and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,
 AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

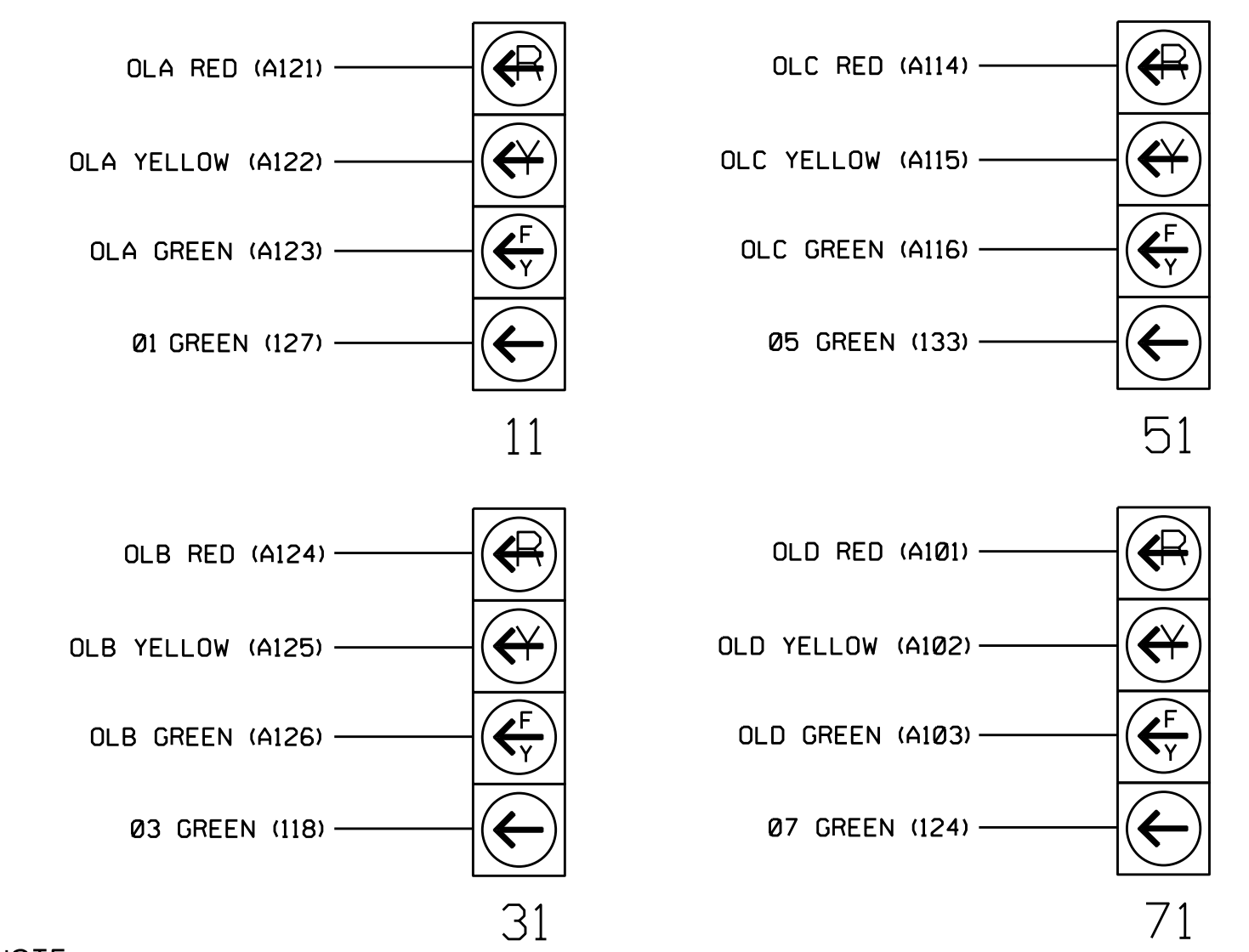
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	31	41,42	NU	51	61,62	NU	62	71	81,82	NU	11	31	NU	51	71
RED		128			101			134		*		107						
YELLOW	*	129		*	102		*	135				108						
GREEN		130			103			136				109						
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW										123			A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127				118			133		124	124							

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

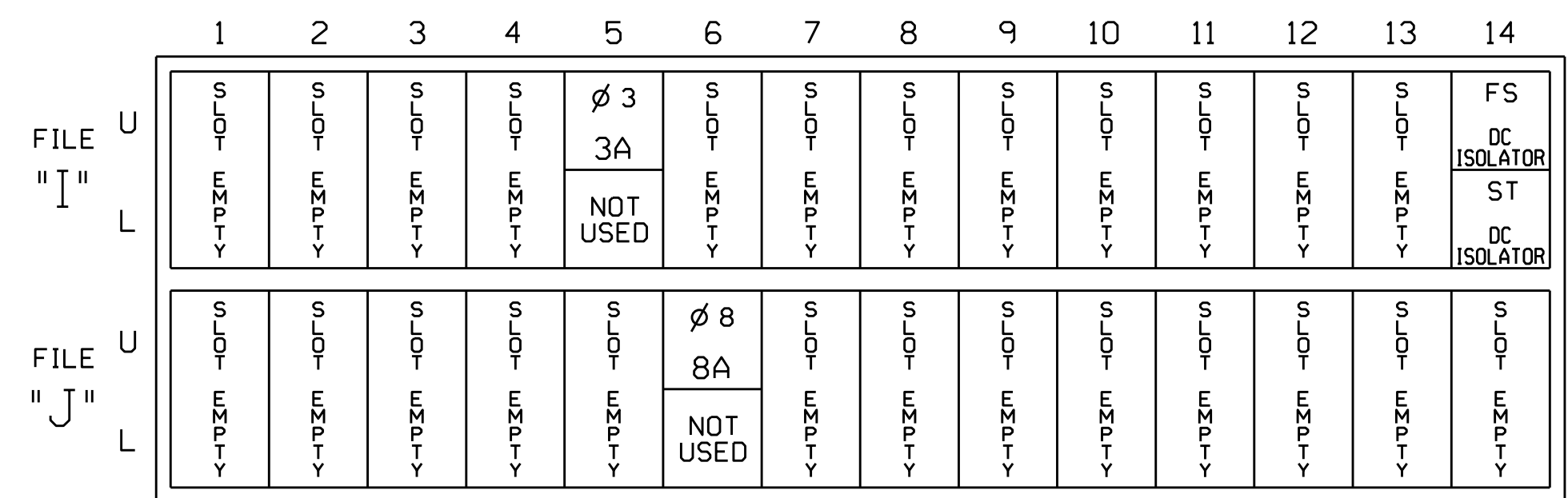


NOTE

The sequence display for signal heads 11, 31, 51, and 71 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			10

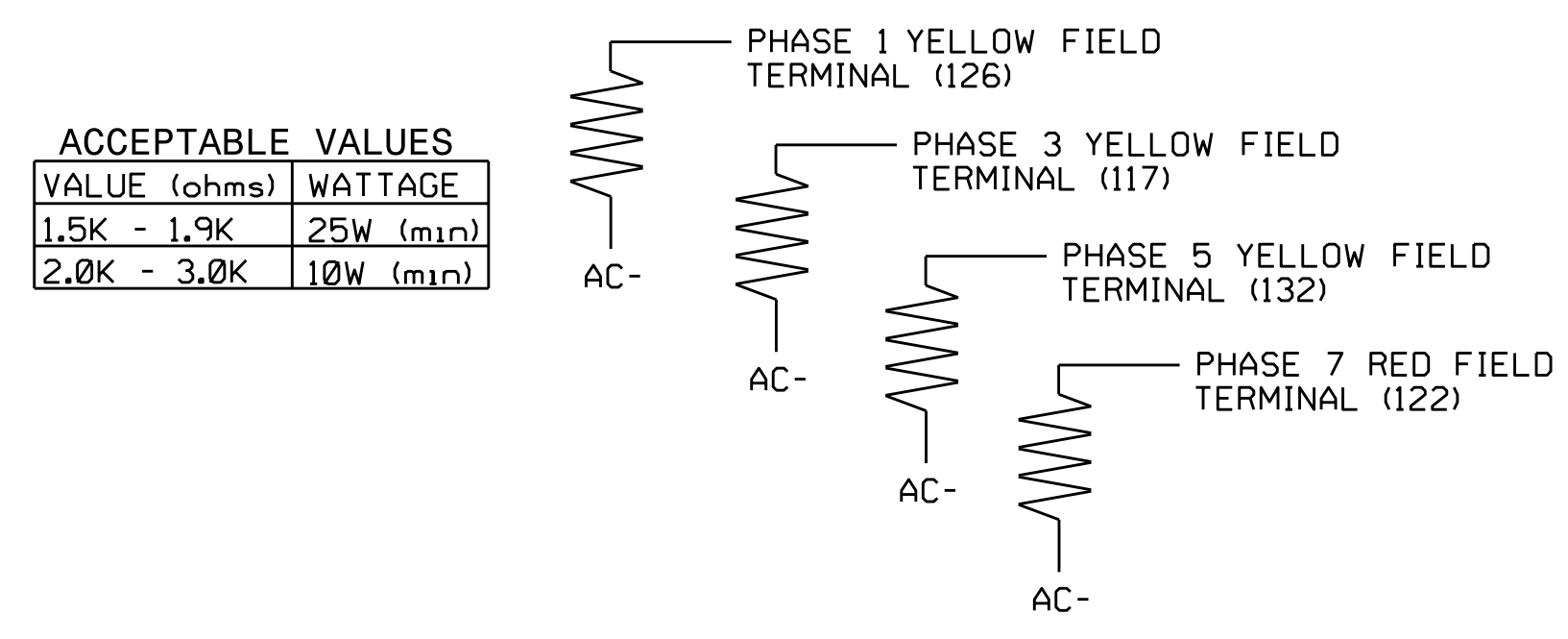
INPUT FILE POSITION LEGEND: J2L
 FILE J
 SLOT 2
 LOWER

SPECIAL DETECTOR NOTE

Where required install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1415T1
 DESIGNED: August 2017
 SEALED: 9/5/2017
 REVISED:

Electrical Detail - Temp 1 - Sheet 1 of 2

Prepared In the Offices of:
 G.L. Transportation, Mobility and Safety Division
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
 Signal Management Section

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1923 (Booker Dairy Road) at Kellie Dr/Smithfield-Selma High School

Division 4 Johnston County Smithfield
 PLAN DATE: August 2017 REVIEWED BY: T. Joyce
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

Seal of D. Todd Joyce, Professional Engineer, License No. 031001, dated 9/7/2017.

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 04-1415T1

07-SEP-2017 09:32 C:\IT\SIG\15\SIG\15\Signal\work\hgr\041415_sml_e.xxx.dgn

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
AND RED CLEAR ON PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #50 ON
SET OUTPUT ASSIGNMENT #51 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

```

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #52 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #51 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

```

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #44 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #6 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #43 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #7 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
AND RED CLEAR ON PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #47 ON
SET OUTPUT ASSIGNMENT #48 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

```

LOGICAL I/O COMMAND #8 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #49 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #9 (+/-COMMAND#)
IF YELLOW ON PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #48 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
AND RED CLEAR ON PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #39 ON
SET OUTPUT ASSIGNMENT #40 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

```

LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #41 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

```

LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF YELLOW ON PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #40 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

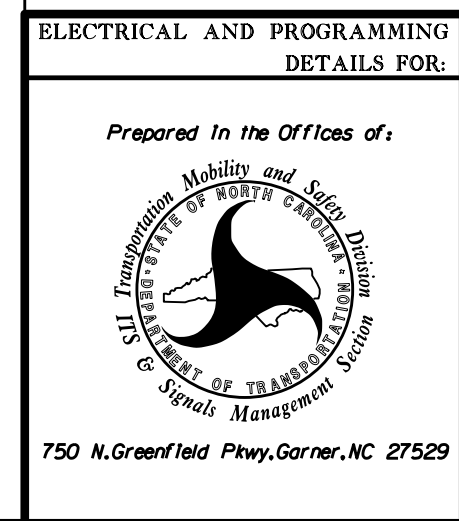
OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

- OUTPUT 39 = Overlap D Red
- OUTPUT 40 = Overlap D Yellow
- OUTPUT 41 = Overlap D Green
- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 47 = Overlap B Red
- OUTPUT 48 = Overlap B Yellow
- OUTPUT 49 = Overlap B Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

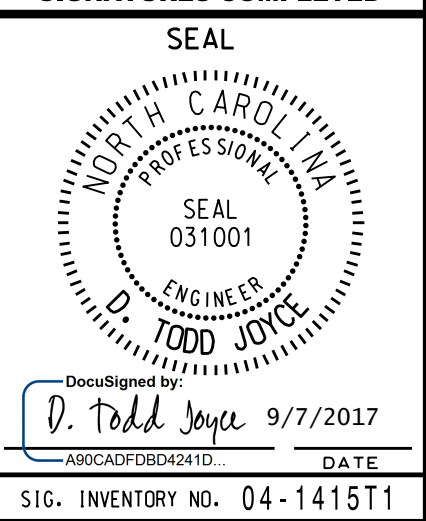
THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1415T1
DESIGNED: August 2017
SEALED: 9/5/2017
REVISED:

Electrical Detail - Temp 1 - Sheet 2 of 2



SR 1923 (Booker Dairy Road) at Kellie Dr/Smithfield-Selma High School	
Division 4	Johnston County
Smithfield	
PLAN DATE: August 2017	REVIEWED BY: T. Joyce
PREPARED BY: C. Strickland	REVIEWED BY:
REVISIONS	INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



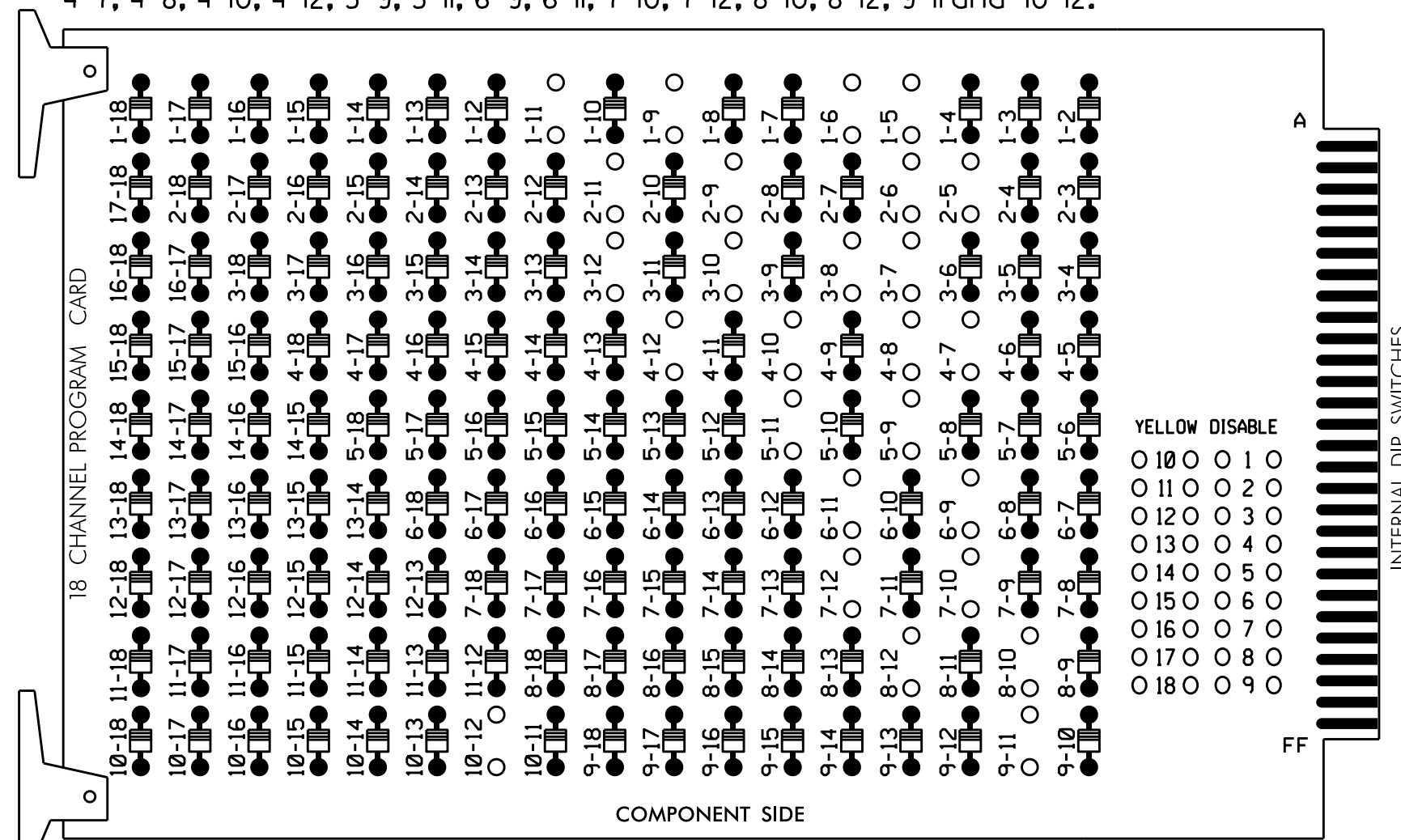
SIG. INVENTORY NO. 04-1415T1

07-SEP-2017 09:34 C:\WITS\AS\WITS\Sig\msh\tr\ck\lnd\041415_sml_e.xxx.dgn

**EDI MODEL 2018ECL-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)

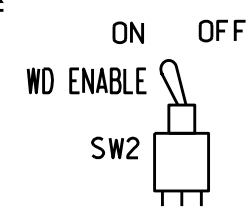
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 2-5, 2-6, 2-9, 2-11, 3-7, 3-8, 3-10, 3-12, 4-7, 4-8, 4-10, 4-12, 5-9, 5-11, 6-9, 6-11, 7-10, 7-12, 8-10, 8-12, 9-11 and 10-12.



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all Phases.
4. Program phases 2 and 6 for Gap Reduction.
5. Program phases 2 and 6 for Startup In Green.
6. Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S4,S5,S7,S8,S10,S11,
 AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,3,4,5,6,7,8
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

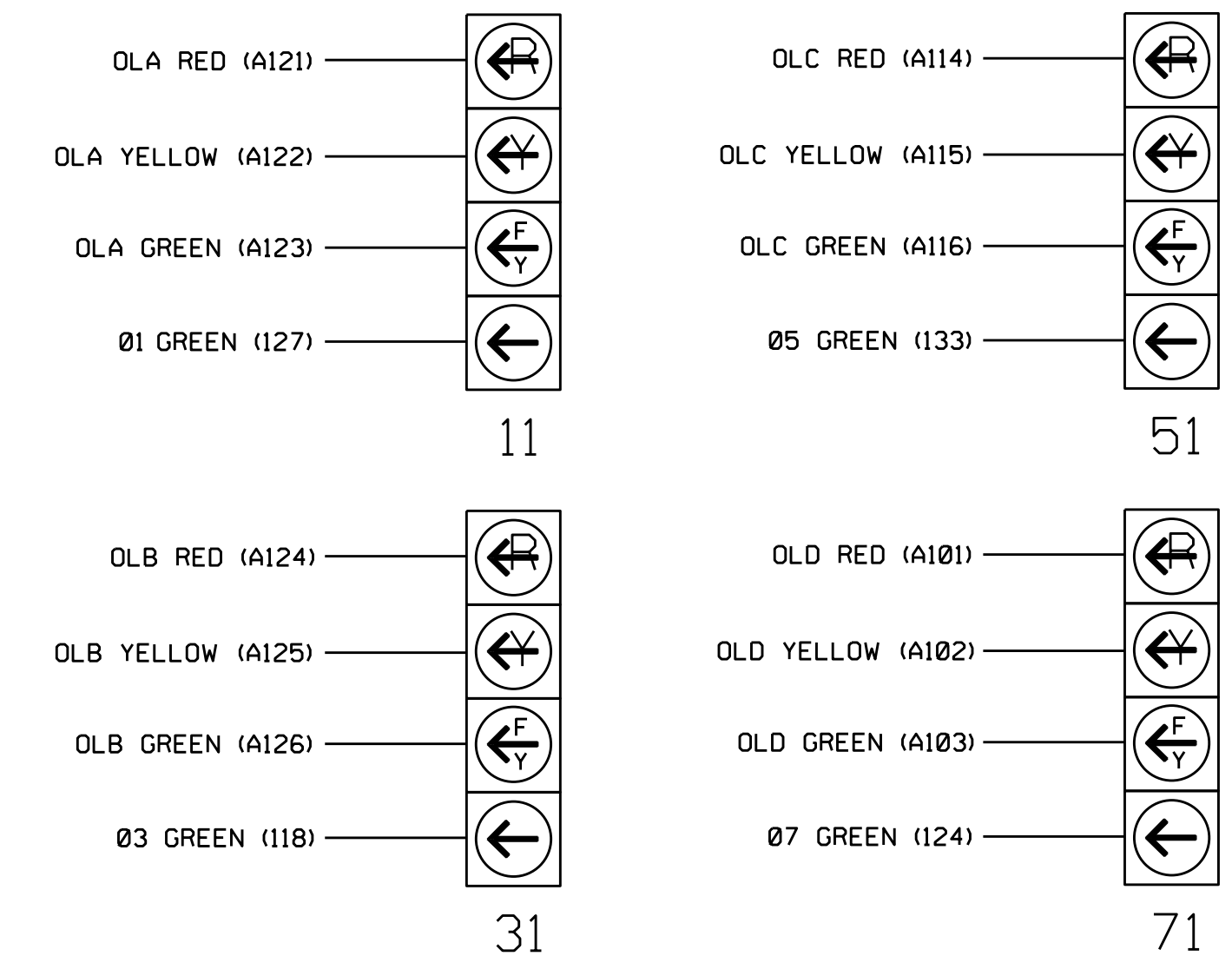
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	31	41,42	NU	51	61,62	NU	71	81,82	NU	11	31	NU	51	71	NU
RED		128			101			134			107							
YELLOW	*	129		*	102		*	135		*	108							
GREEN		130			103			136			109							
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127				118			133			124							

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

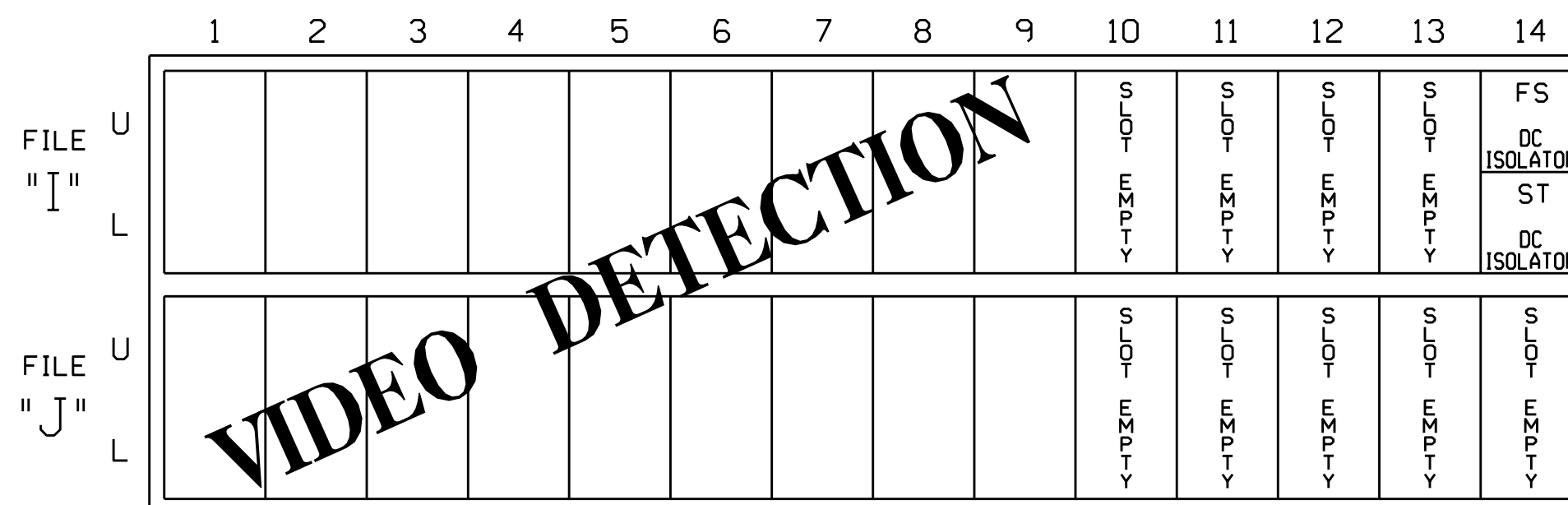


NOTE

The sequence display for signal heads 11, 31, 51, and 71 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

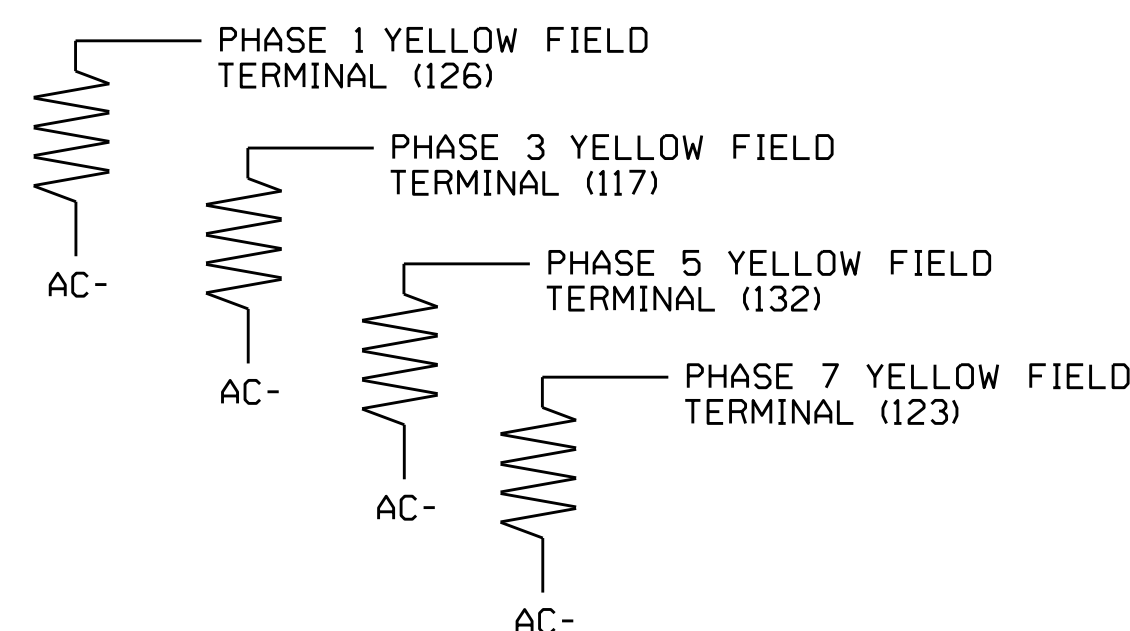
SPECIAL DETECTOR NOTE

Install a video detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 04-1415T2
 DESIGNED: August 2017
 SEALED: 9/5/2017
 REVISED:

Electrical Detail - Temp 2 - Sheet 1 of 2

Electrical and Programming Details For:

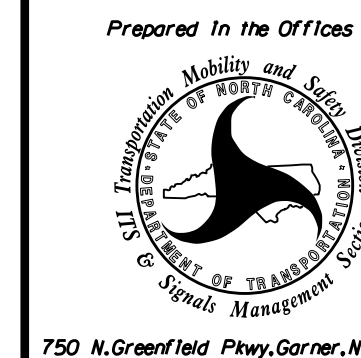
SR 1923 (Booker Dairy Road)
 at
 Kellie Dr/Smithfield-Selma
 High School

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE



DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Seal area with signature and date: 9/7/2017

SIG. INVENTORY NO. 04-1415T2

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
AND RED CLEAR ON PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #50 ON
SET OUTPUT ASSIGNMENT #51 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

```

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #52 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #51 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

```

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #44 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #6 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #43 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #7 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
AND RED CLEAR ON PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #47 ON
SET OUTPUT ASSIGNMENT #48 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

```

LOGICAL I/O COMMAND #8 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #49 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #9 (+/-COMMAND#)
IF YELLOW ON PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #48 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
AND RED CLEAR ON PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #39 ON
SET OUTPUT ASSIGNMENT #40 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

```

LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #41 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

```

LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF YELLOW ON PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #40 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0.0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

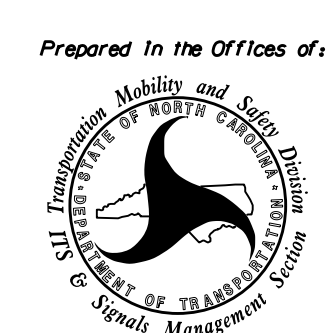
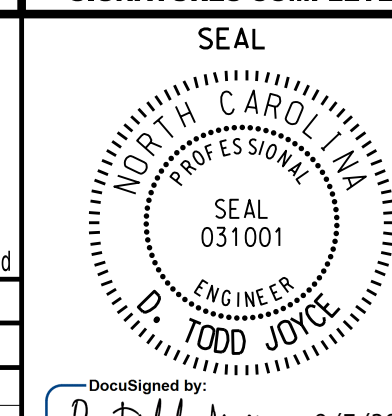
OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

- OUTPUT 39 = Overlap D Red
- OUTPUT 40 = Overlap D Yellow
- OUTPUT 41 = Overlap D Green
- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 47 = Overlap B Red
- OUTPUT 48 = Overlap B Yellow
- OUTPUT 49 = Overlap B Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1415T2
DESIGNED: August 2017
SEALED: 9/5/2017
REVISED:

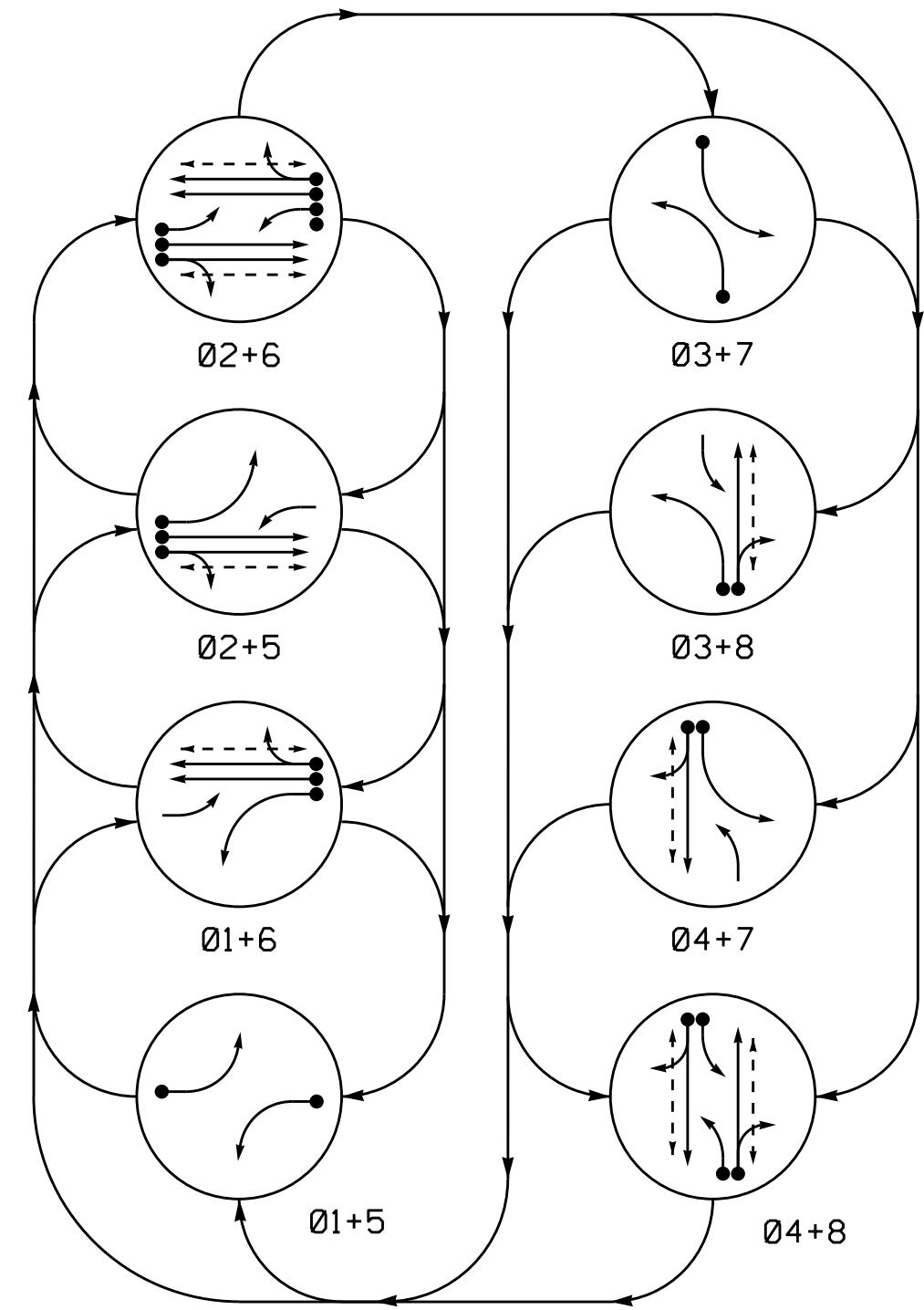
Electrical Detail - Temp 2 - Sheet 2 of 2

Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	DETAILS FOR: SR 1923 (Booker Dairy Road) at Kellie Dr/Smithfield-Selma High School	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL  SEAL 031001 ENGINEER D. TODD JOYCE
Electrical and Programming Division 4 PLAN DATE: August 2017 PREPARED BY: C. Strickland	JOHNSTON COUNTY Smithfield REVIEWED BY: T. Joyce REVIEWED BY:	REVISIONS INIT. DATE
DocuSigned by: D. Todd Joyce 9/7/2017 AR0CAFD9ED24241D		DATE

07-SEP-2017 09:50 C:\PLOTS\041415T2\SIG\041415T2_Sig.dgn

8 Phase Fully Actuated Booker Dairy Road CLS

PHASING DIAGRAM



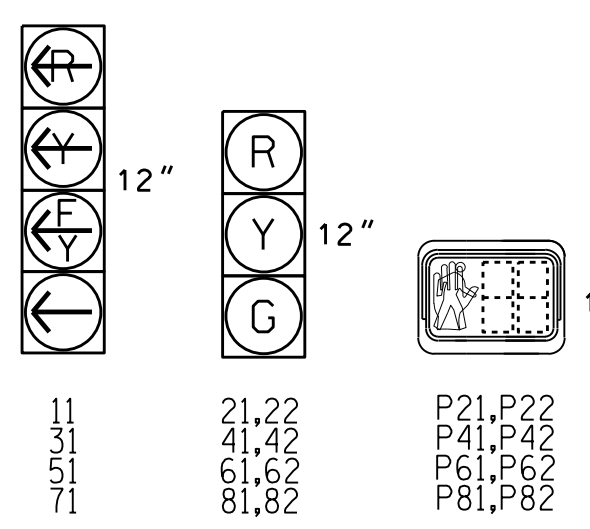
PHASING DIAGRAM DETECTION LEGEND

- ← ● DETECTED MOVEMENT
- ← ○ UNDETECTED MOVEMENT (OVERLAP)
- ← ○ UNSIGNALIZED MOVEMENT
- ← ○ PEDESTRIAN MOVEMENT

SIGNAL FACE	PHASE							
	01+5	01+6	02+5	02+6	03+7	03+8	04+7	04+8
11	←	←	←	←	←	←	←	←
21,22	R	R	G	G	R	R	R	Y
31	←	←	←	←	←	←	←	←
41,42	R	R	R	R	R	R	G	R
51	←	←	←	←	←	←	←	←
61,62	R	G	R	G	R	R	R	Y
71	←	←	←	←	←	←	←	←
81,82	R	R	R	R	R	G	R	R
P21,P22	DW	DW	W	W	DW	DW	DW	DRK
P41,P42	DW	DW	DW	DW	DW	DW	W	DRK
P61,P62	DW	W	DW	DW	DW	DW	DW	DRK
P81,P82	DW	DW	DW	DW	DW	W	DW	DRK

SIGNAL FACE I.D.

All Heads L.E.D.

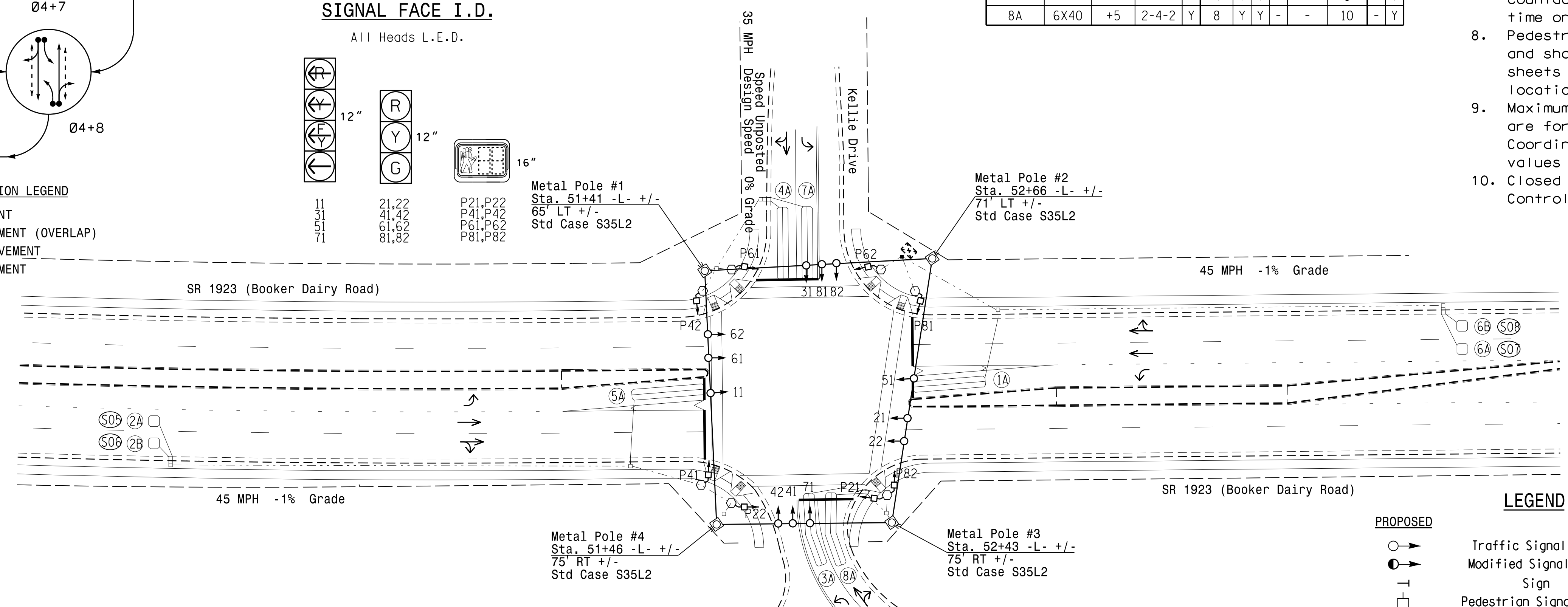


OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING							
					PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
1A	6X40	0	2-4-2	Y	1	Y	Y	-	-	15	-	Y
2A/S05	6X6	300	6	Y	2	Y	Y	-	-	-	-	Y
2B/S06	6X6	300	6	Y	2	Y	Y	-	-	-	-	Y
3A	6X40	+5	2-4-2	Y	3	Y	Y	-	-	15	-	Y
4A	6X40	0	2-4-2	Y	4	Y	Y	-	-	10	-	Y
5A	6X40	0	2-4-2	Y	5	Y	Y	-	-	15	-	Y
6A/S07	6X6	300	4	Y	6	Y	Y	-	-	-	-	Y
6B/S08	6X6	300	4	Y	6	Y	Y	-	-	-	-	Y
7A	6X40	0	2-4-2	Y	7	Y	Y	-	-	15	-	Y
8A	6X40	+5	2-4-2	Y	8	Y	Y	-	-	10	-	Y

NOTES

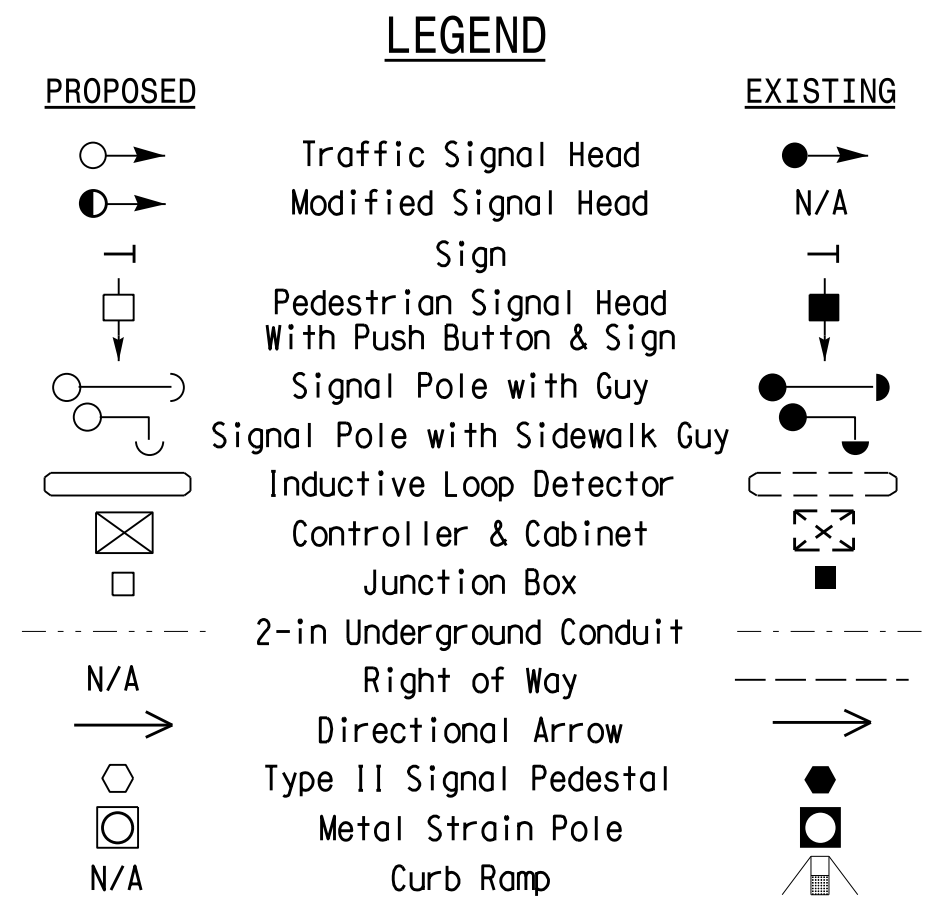
- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Phase 1 and/or phase 5 may be lagged.
- Phase 3 and/or phase 7 may be lagged.
- Set all detector units to presence mode.
- Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
- Program pedestrian heads to countdown the flashing "Don't Walk" time only.
- Pedestrian pedestals are conceptual and shown for reference only. See sheets P1-P3 for pushbutton location details.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- Closed loop system data: Controller Asset #: 1415.



OASIS 2070 TIMING CHART

FEATURE	PHASE							
	1	2	3	4	5	6	7	8
Min Green 1 *	7	12	7	7	7	12	7	7
Extension 1 *	2.0	6.0	2.0	2.0	2.0	6.0	2.0	2.0
Max Green 1 *	25	90	25	20	25	90	25	20
Yellow Clearance	3.0	4.6	3.0	3.8	3.0	4.6	3.0	3.8
Red Clearance	3.1	1.6	3.1	3.1	2.9	1.6	2.9	3.1
Walk 1 *	-	7	-	7	-	7	-	7
Don't Walk 1	-	16	-	21	-	16	-	25
Seconds Per Actuation *	-	1.5	-	-	-	1.5	-	-
Max Variable Initial *	-	34	-	-	-	34	-	-
Time Before Reduction *	-	15	-	-	-	15	-	-
Time To Reduce *	-	30	-	-	-	30	-	-
Minimum Gap	-	3.0	-	-	-	3.0	-	-
Recall Mode	-	MIN RECALL	-	-	-	MIN RECALL	-	-
Vehicle Call Memory	-	YELLOW	-	-	-	YELLOW	-	-
Dual Entry	-	-	-	ON	-	-	-	ON
Simultaneous Gap	ON	ON	ON	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Final

Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

SR 1923 (Booker Dairy Road) at Kellie Dr./Smithfield-Selma High School

Division 4 Johnston County Smithfield

PLAN DATE: June 2017 REVIEWED BY: JPG

PREPARED BY: KGP, Jr. REVIEWED BY:

SEAL

DATE: 9/5/2017

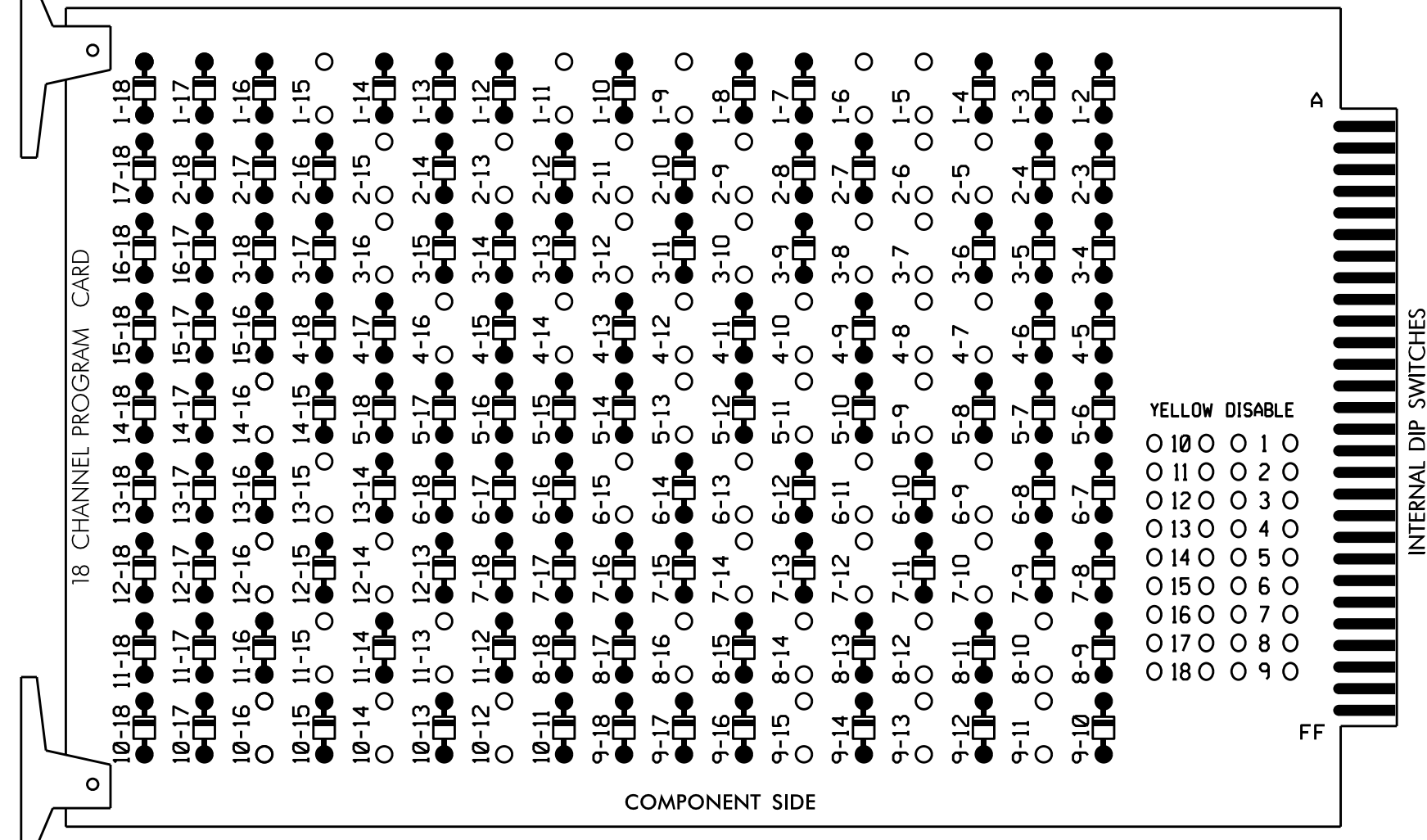
09-SEP-2017 11:02 R:\Projects\041415_Sig.dgn 2017modl.fncal.dgn kgsaedrn

EDI MODEL 2018ECL-NC CONFLICT MONITOR

PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

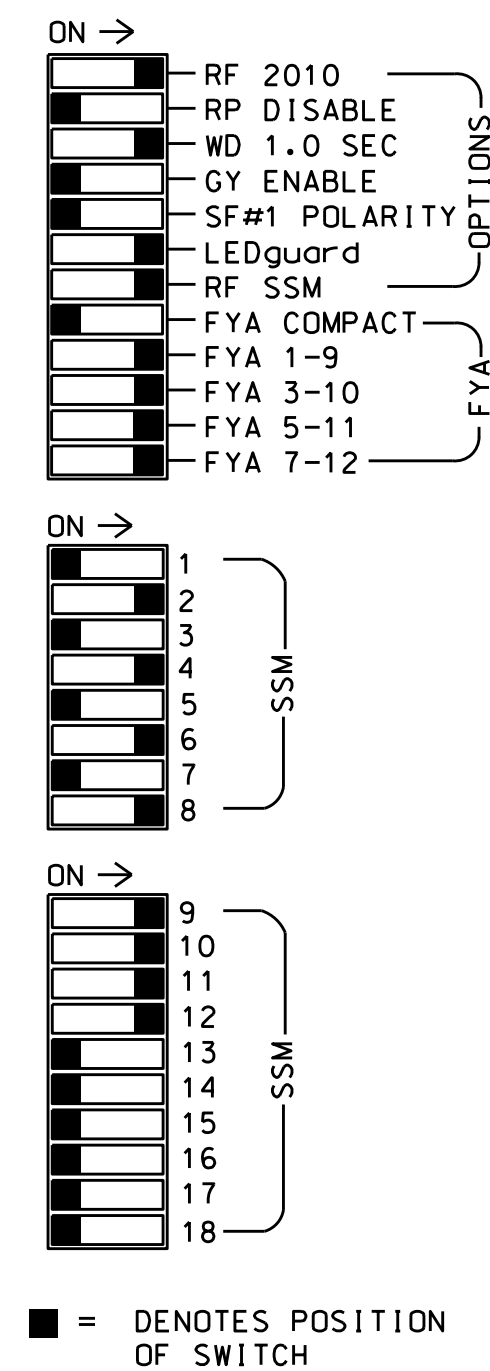
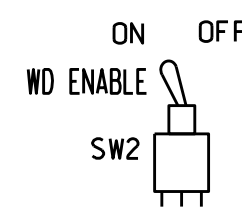
REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-15, 2-5, 2-6, 2-9, 2-11, 2-13, 2-15, 3-7, 3-8, 3-10, 3-12, 3-16, 4-7, 4-8, 4-10, 4-12, 4-14, 4-16, 5-9, 5-11, 5-13, 6-9, 6-11, 6-13, 6-15, 7-10, 7-12, 7-14, 8-10, 8-12, 8-14, 8-16, 9-11, 9-13, 9-15, 10-12, 10-14, 10-16, 11-13, 11-15, 12-14, 12-16, 13-15 and 14-16.



REMOVE JUMPERS AS SHOWN

NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2, 4, 6, and 8 for Startup Ped Call.
- Program phases 2 and 6 for Yellow Flash, and overlaps 1 and 2 as Wag Overlaps.
- The cabinet and controller are part of the Booker Dairy Road Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,S3,S4,S5,S6,S7,S8,S9,S10,
 S11,S12,AUX S1,AUX S2,AUX S4,AUX S5
 PHASES USED.....1,2,2 PED,3,4,4 PED,5,6,6 PED,7,
 8,8 PED
 OVERLAP "A".....1+2
 OVERLAP "B".....3+4
 OVERLAP "C".....5+6
 OVERLAP "D".....7+8

PROJECT REFERENCE NO.	SHEET NO.
U-3334B	Sig. 7.1

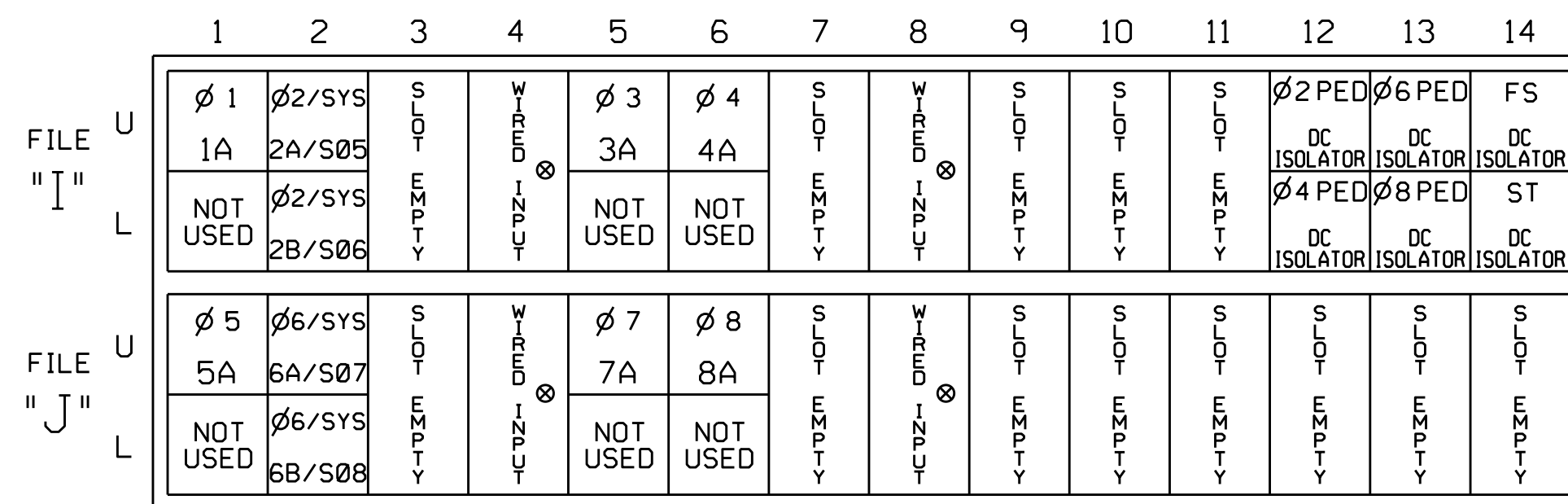
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	P21, P22	31	41,42	P41, P42	51	61,62	P61, P62	71	81,82	P81, P82	11	31	NU	51	71	NU
RED		128			101			134			107							
YELLOW	*	129		*	102		*	135		*	108							
GREEN		130			103			136			109							
RED ARROW													A121	A124		A114	A101	
YELLOW ARROW													A122	A125		A115	A102	
FLASHING YELLOW ARROW													A123	A126		A116	A103	
GREEN ARROW	127						118		133		124							
Hand				113				104		119			110					
Person								106					112					

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

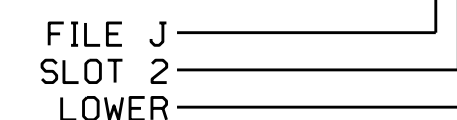
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A ¹	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10	26	6	Y	Y	Y		3
2A/S05	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
	2B/S06	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y		
3A ²	TB4-5,6	I5U	58	20	3	3	Y	Y			15
	-	J8U	50	12	28	8	Y	Y			3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
	5A ³	TB3-1,2	J1U	55	17	5	5	Y	Y		15
6A/S07	-	I4U	47	9	22	2	Y	Y	Y		3
	6B/S08	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y		
7A ⁴	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
	-	TB5-5,6	J5U	57	19	7	7	Y	Y		15
8A	-	I8U	49	11	24	4	Y	Y			3
	TB5-9,10	J6U	42	4	8	8	Y	Y			10
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29					2 PED		
P41,P42	TB8-5,6	I12L	69	31					4 PED		
P61,P62	TB8-7,9	I13U	68	30					6 PED		
P81,P82	TB8-8,9	I13L	70	32					8 PED		

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

- Add jumper from I1-W to J4-W, on rear of input file.
- Add jumper from I5-W to J8-W, on rear of input file.
- Add jumper from J1-W to I4-W, on rear of input file.
- Add jumper from J5-W to I8-W, on rear of input file.

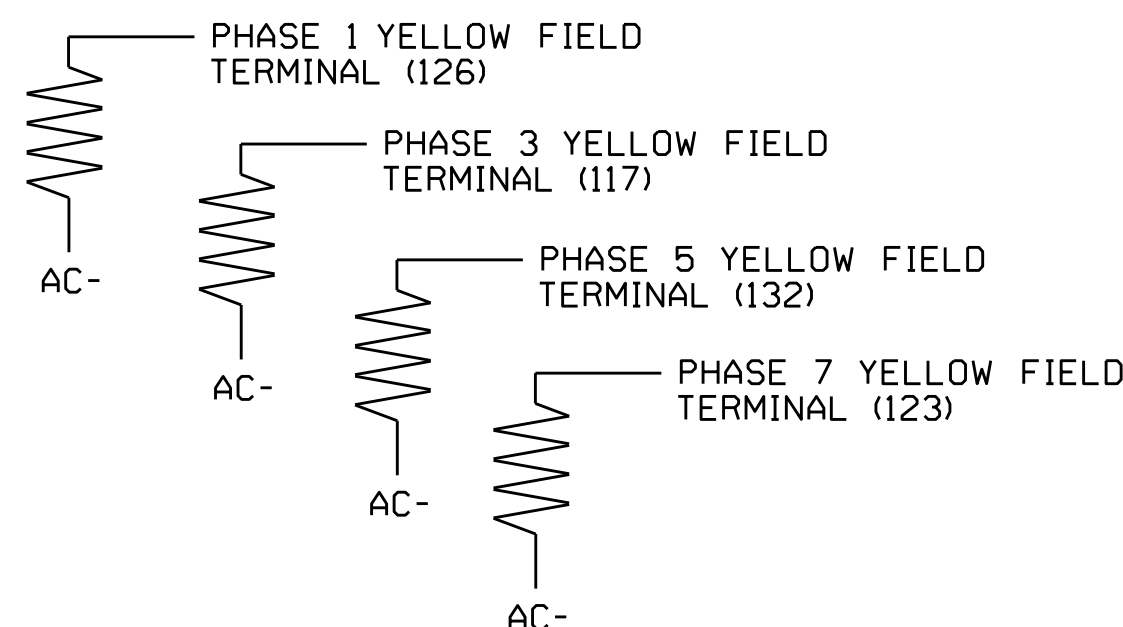
INPUT FILE POSITION LEGEND: J2L



LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1415
 DESIGNED: June 2017
 SEALED: 9/5/2017
 REVISED:

COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

Electrical Detail - Final - Sheet 1 of 2

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared In the Offices of: 750 N. Greenfield Pkwy, Garner, NC 27529	SR 1923 (Booker Dairy Road) at Kellie Dr/Smithfield-Selma High School	DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL D. Todd Joyce 9/7/2017
	Division 4 Johnston County Smithfield PLAN DATE: August 2017 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY: REVISIONS INIT. DATE	SIG. INVENTORY NO. 04-1415

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS), SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```

LOGICAL I/O COMMAND #1 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
AND RED CLEAR ON PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #50 ON
SET OUTPUT ASSIGNMENT #51 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

```

LOGICAL I/O COMMAND #2 (+/-COMMAND#)
IF ACTIVE PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #52 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #3 (+/-COMMAND#)
IF YELLOW ON PHASE #1 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #51 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

```

LOGICAL I/O COMMAND #4 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
AND RED CLEAR ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #42 ON
SET OUTPUT ASSIGNMENT #43 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

```

LOGICAL I/O COMMAND #5 (+/-COMMAND#)
IF ACTIVE PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #44 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #6 (+/-COMMAND#)
IF YELLOW ON PHASE #5 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #43 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

```

LOGICAL I/O COMMAND #7 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
AND RED CLEAR ON PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #47 ON
SET OUTPUT ASSIGNMENT #48 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

```

LOGICAL I/O COMMAND #8 (+/-COMMAND#)
IF ACTIVE PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #49 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #9 (+/-COMMAND#)
IF YELLOW ON PHASE #3 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #48 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

```

LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
AND RED CLEAR ON PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #39 ON
SET OUTPUT ASSIGNMENT #40 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

```

LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF ACTIVE PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #41 OFF
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 7 (HEAD 71).

```

LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF YELLOW ON PHASE #7 IS ON
    ↓
    SCROLL DOWN
    ↓
THEN:
SET OUTPUT ASSIGNMENT #40 ON
    ↓
    PRESS '+'
    
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

```

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0=255 SEC)...0.0
YELLOW CLEAR (0=PARENT.3-25.5 SEC)...0.0
RED CLEAR (0=PARENT.0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    ↓
    PRESS '+'
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

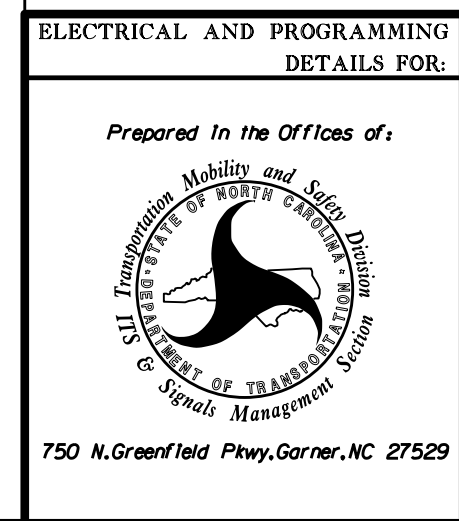
OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

- OUTPUT 39 = Overlap D Red
- OUTPUT 40 = Overlap D Yellow
- OUTPUT 41 = Overlap D Green
- OUTPUT 42 = Overlap C Red
- OUTPUT 43 = Overlap C Yellow
- OUTPUT 44 = Overlap C Green
- OUTPUT 47 = Overlap B Red
- OUTPUT 48 = Overlap B Yellow
- OUTPUT 49 = Overlap B Green
- OUTPUT 50 = Overlap A Red
- OUTPUT 51 = Overlap A Yellow
- OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1415
DESIGNED: June 2017
SEALED: 9/5/2017
REVISED:

Electrical Detail - Final - Sheet 2 of 2



Prepared In the Offices of:	
750 N. Greenfield Pkwy, Garner, NC 27529	
ELECTRICAL AND PROGRAMMING DETAILS FOR: SR 1923 (Booker Dairy Road) at Kellie Dr/Smithfield-Selma High School	Division 4 Johnston County Smithfield
PLAN DATE: August 2017	REVIEWED BY: T. Joyce
PREPARED BY: C. Strickland	REVIEWED BY:
REVISIONS	INIT. DATE

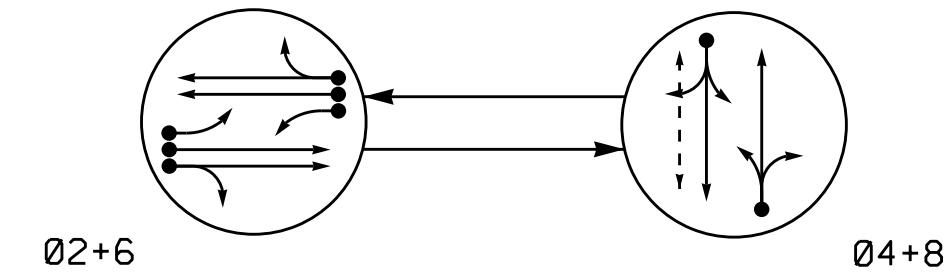
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL STATE OF NORTH CAROLINA PROFESSIONAL ENGINEER D. Todd Joyce 031001
DocuSign D. Todd Joyce 9/7/2017 DATE
SIG. INVENTORY NO. 04-1415

07-SEP-2017 10:25 C:\PLOTS\041415\Sig\041415_sme.ele.xxx.dgn

2 Phase Fully Actuated Booker Dairy Road CLS

PHASING DIAGRAM

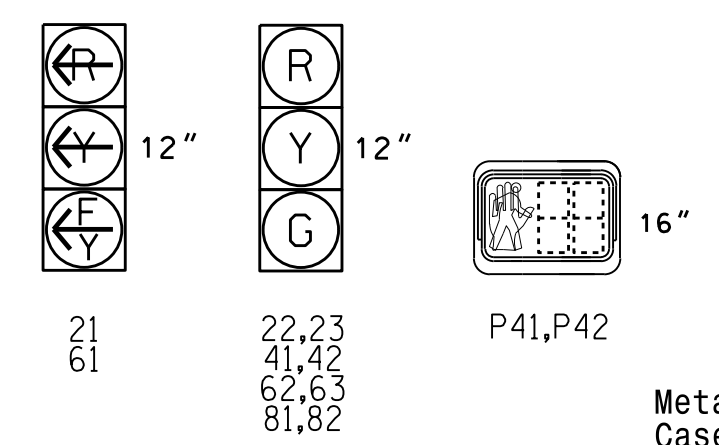


PHASING DIAGRAM DETECTION LEGEND
 ● DETECTED MOVEMENT
 ○ UNDETECTED MOVEMENT (OVERLAP)
 - - - UNSIGNALIZED MOVEMENT
 - - - PEDESTRIAN MOVEMENT

SIGNAL FACE	PHASE		
	02+6	04+8	FLASH
21	Y	R	Y
22,23	G	R	Y
41,42	R	G	R
61	Y	R	Y
62,63	G	R	Y
81,82	R	G	R
P41,P42	DW	W	DRK

SIGNAL FACE I.D.

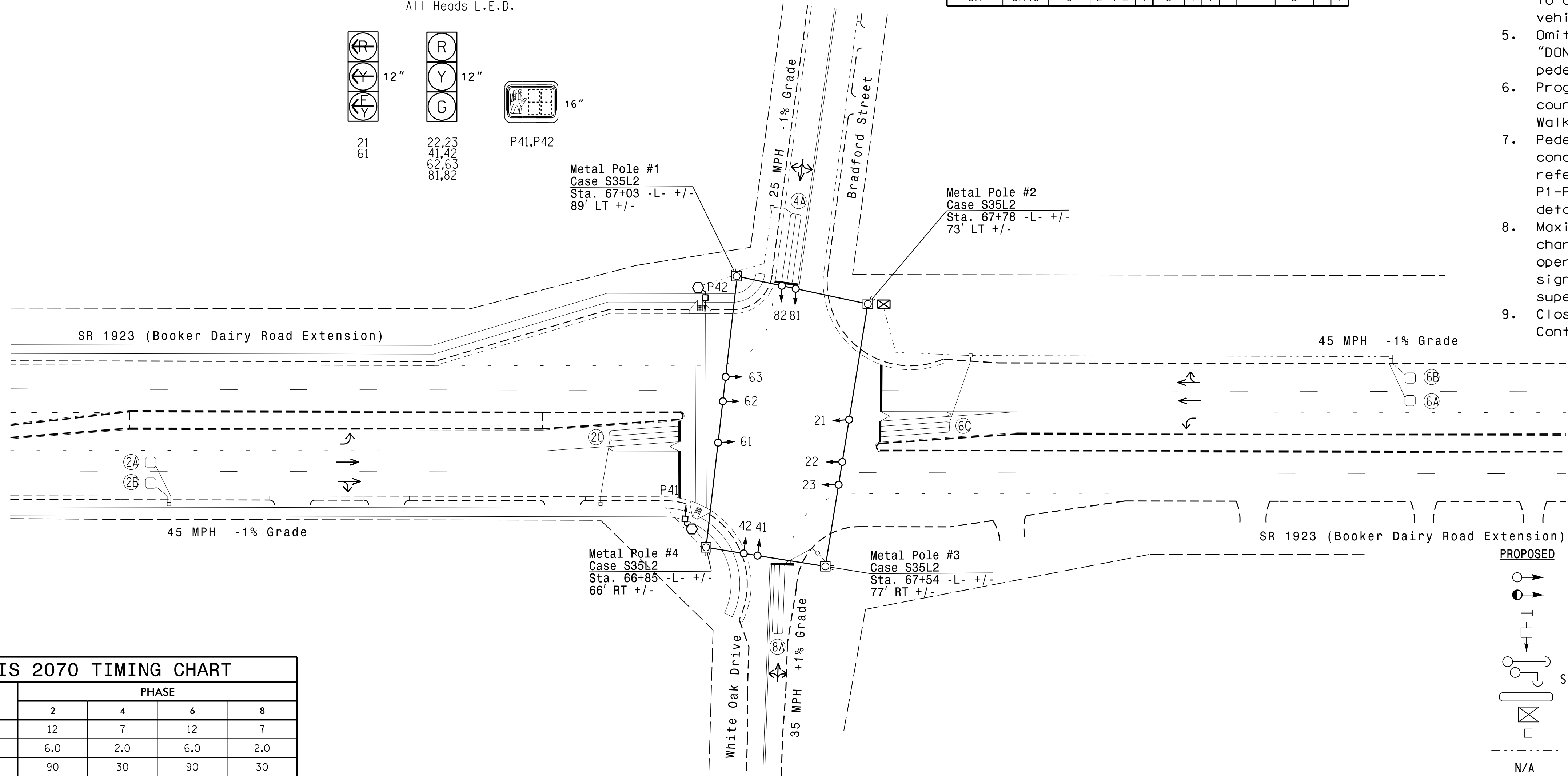
All Heads L.E.D.



OASIS 2070 LOOP & DETECTOR INSTALLATION CHART												
INDUCTIVE LOOPS					DETECTOR PROGRAMMING							
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
2A	6X6	300	6	Y	2	Y	Y	-	-	-	-	Y
2B	6X6	300	6	Y	2	Y	Y	-	-	-	-	Y
2C	6X40	0	2-4-2	Y	2	Y	Y	Y	-	3	-	Y
4A	6X40	0	2-4-2	Y	4	Y	Y	-	-	5	-	Y
6A	6X6	300	4	Y	6	Y	Y	-	-	-	-	Y
6B	6X6	300	4	Y	6	Y	Y	-	-	-	-	Y
6C	6X40	0	2-4-2	Y	6	Y	Y	Y	-	3	-	Y
8A	6X40	0	2-4-2	Y	8	Y	Y	-	-	5	-	Y

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Set all detector units to presence mode.
- Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
- Omit "WALK" and flashing "DON'T WALK" with no pedestrian calls.
- Program pedestrian heads to countdown the flashing "Don't Walk" time only.
- Pedestrian pedestals are conceptual and shown for reference only. See sheets P1-P3 for pushbutton location details.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- Closed loop system data: Controller Asset #:1416.



OASIS 2070 TIMING CHART				
FEATURE	PHASE			
	2	4	6	8
Min Green 1 *	12	7	12	7
Extension 1 *	6.0	2.0	6.0	2.0
Max Green 1 *	90	30	90	30
Yellow Clearance	4.6	3.8	4.6	3.8
Red Clearance	1.6	3.2	1.6	3.2
Walk 1 *	-	7	-	-
Don't Walk 1	-	30	-	-
Seconds Per Actuation *	1.5	-	1.5	-
Max Variable Initial *	34	-	34	-
Time Before Reduction *	15	-	15	-
Time To Reduce *	30	-	30	-
Minimum Gap	3.0	-	3.0	-
Recall Mode	MIN RECALL	-	MIN RECALL	-
Vehicle Call Memory	YELLOW	-	YELLOW	-
Dual Entry	-	ON	-	ON
Simultaneous Gap	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

PROPOSED		EXISTING	
○	Traffic Signal Head	●	N/A
○	Modified Signal Head		
○	Sign		
○	Pedestrian Signal Head With Push Button & Sign		
○	Signal Pole with Guy	○	
○	Signal Pole with Sidewalk Guy	○	
○	Inductive Loop Detector	○	
○	Controller & Cabinet	○	
○	Junction Box	○	
○	2-in Underground Conduit	○	
N/A	Right of Way	---	
→	Directional Arrow	→	
○	Metal Strain Pole	○	
N/A	Curb Ramp	○	
○	Type II Signal Pedestal	○	

New Installation

SR 1923 (Booker Dairy Rd. Ext.) at Bradford Street / White Oak Drive

Division 4 Johnston County Smithfield

PLAN DATE: June 2017 REVIEWED BY: JPG

PREPARED BY: KGP, Jr. REVIEWED BY:

REVISIONS: INIT. DATE

750 N. Greenfield Hwy, Garner, NC 27529

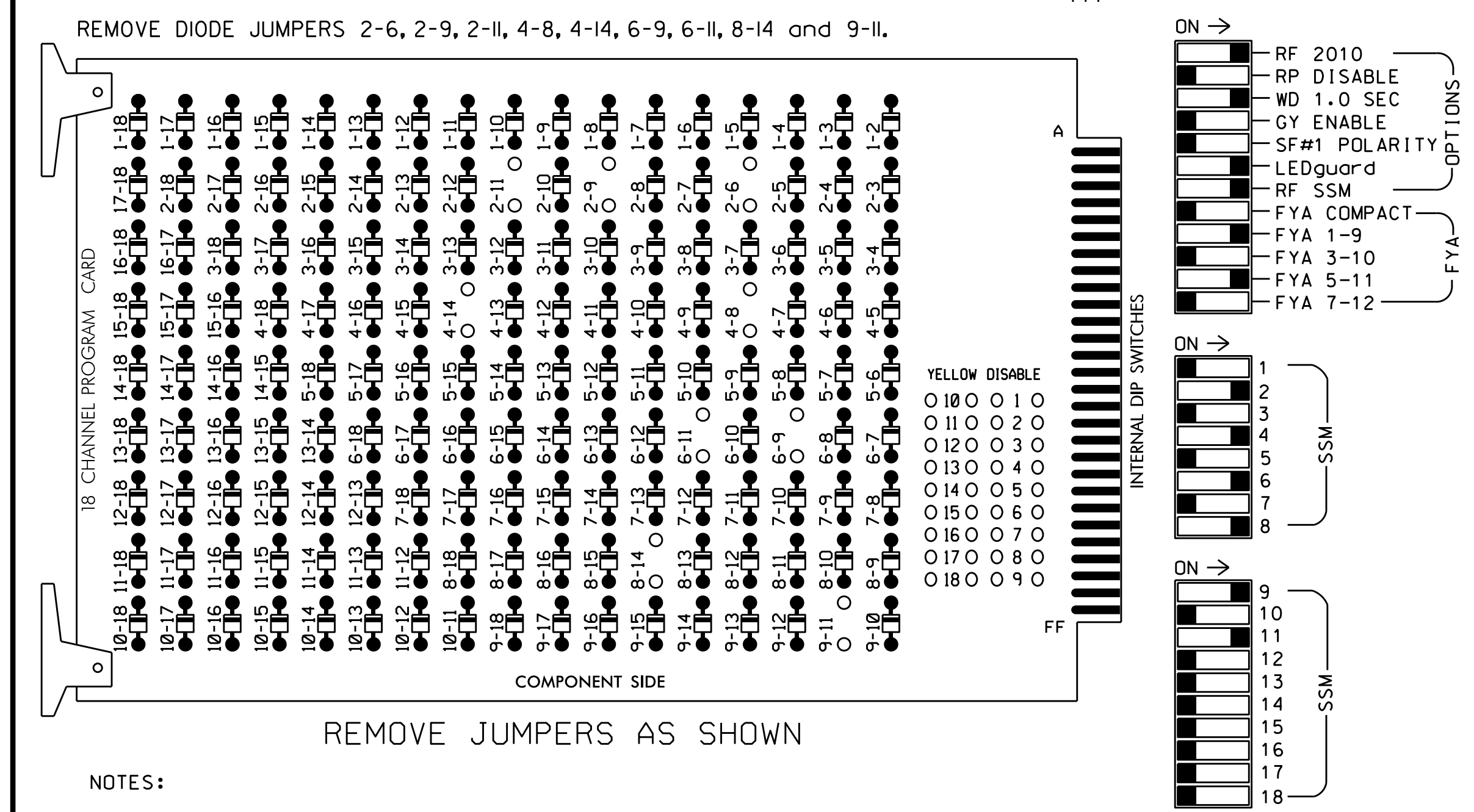
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 029904 JASON P. GALLAGHER ENGINEER JASON P. GALLAGHER 8/29/2017 DATE 8/29/2017 DATE SIG. INVENTORY NO. 04-1416

20-AUG-2017 1:45:04 R:\Projects\Signal\Signal\041416.sig.dwg, 2017madd.dgn kgspeardrn

**EDI MODEL 2018ECL-NC CONFLICT MONITOR
PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phase 4 for Startup Ped Call.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Booker Dairy Road Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S5,S6,S8,S11,AUX S1,AUX S4
 PHASES USED.....2,4,4 PED,6,8
 OVERLAP "A".....2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....6
 OVERLAP "D".....NOT USED

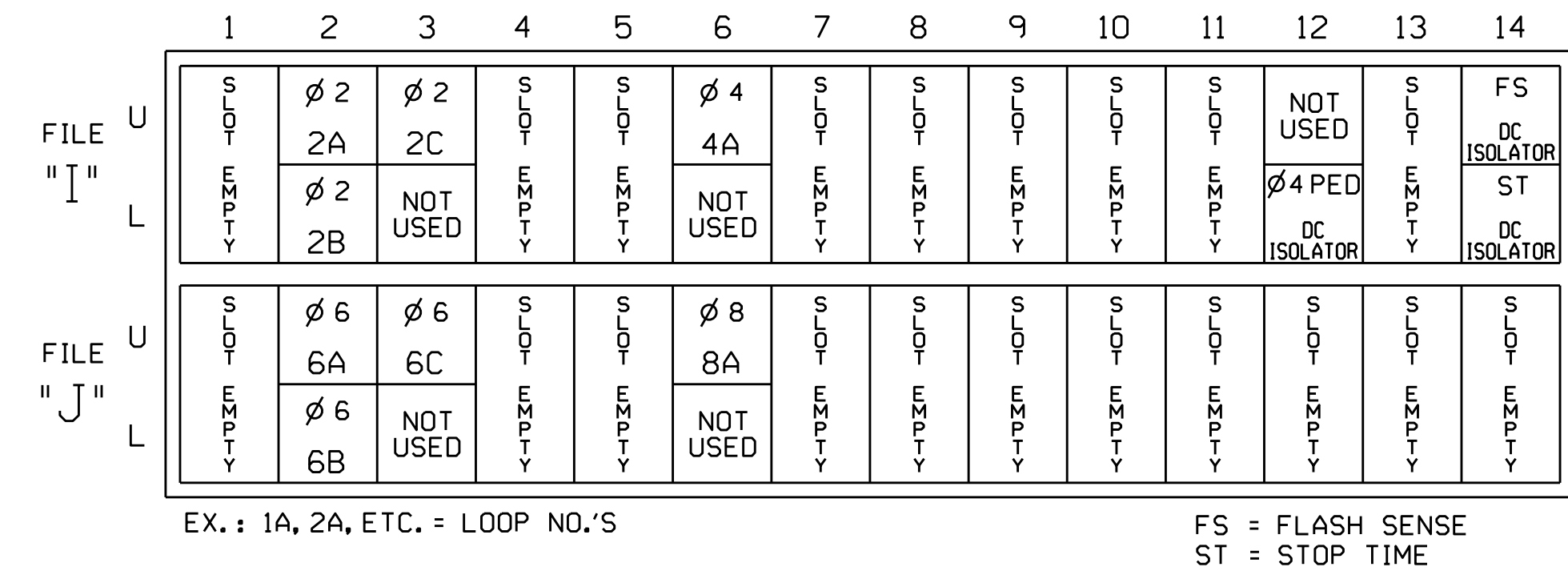
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	22,23	NU	NU	41,42	P41, P42	NU	62,63	NU	NU	81,82	NU	61	NU	NU	21	NU	NU	
RED		128			101			134			107								
YELLOW		129			102			135			108								
GREEN		130			103			136			109								
RED ARROW													A121				A114		
YELLOW ARROW													A122				A115		
FLASHING YELLOW ARROW													A123				A116		
GREEN ARROW																			
Hand																		104	
Walking																			106

NU = Not Used
 ★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S FS = FLASH SENSE ST = STOP TIME

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

From Main Menu press '8' (OVERLAPS), then '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

```

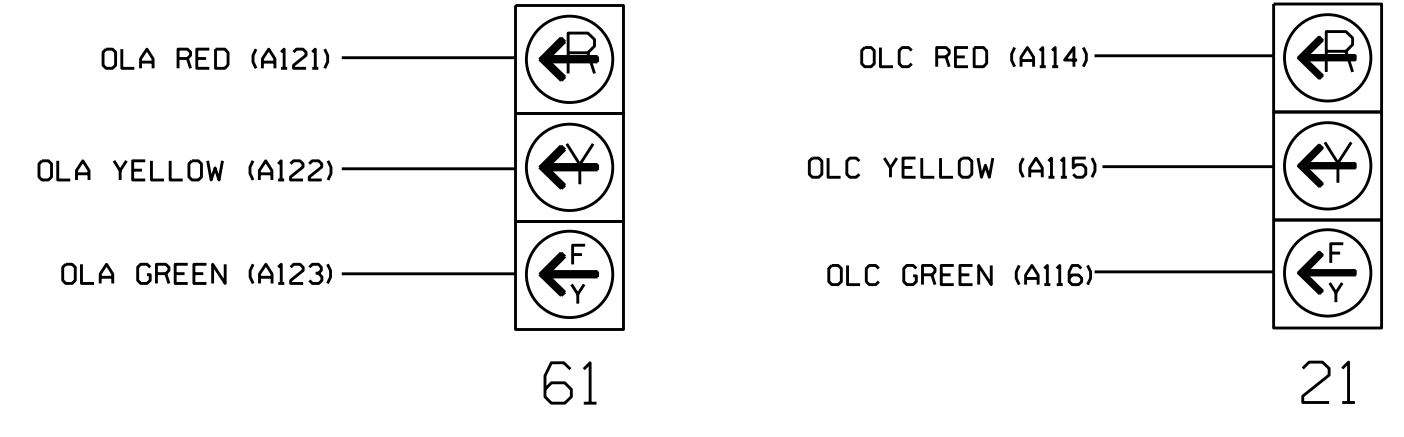
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS: - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



COUNTDOWN PEDESTRIAN SIGNAL OPERATION

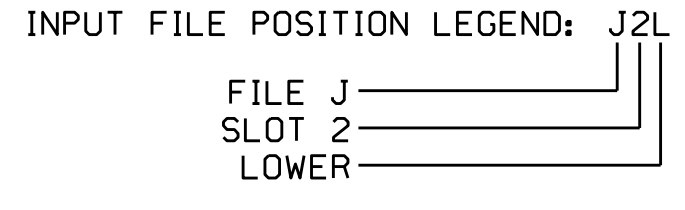
Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1416
 DESIGNED: June 2017
 SEALED: 8/29/2017
 REVISED:

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
2C	TB2-9,10	I3U	63	25	32	2	Y	Y	Y		3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			5
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
6C	TB3-9,10	J3U	64	26	36	6	Y	Y	Y		3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			5
PED PUSH BUTTONS											
P41,P42	TB8-5,6	I12L	69	31	PED 4	4	PED				

NOTE: INSTALL DC ISOLATORS IN INPUT FILE SLOT I12.



Electrical Detail

Electrical and Programming Details For: SR 1923 (Booker Dairy Rd. Ext.) at Bradford Street/White Oak Drive

Prepared In the Offices of: [Logo]

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

Seal: D. Todd Joyce, Engineer, License No. 031001, Date: 9/12/2017

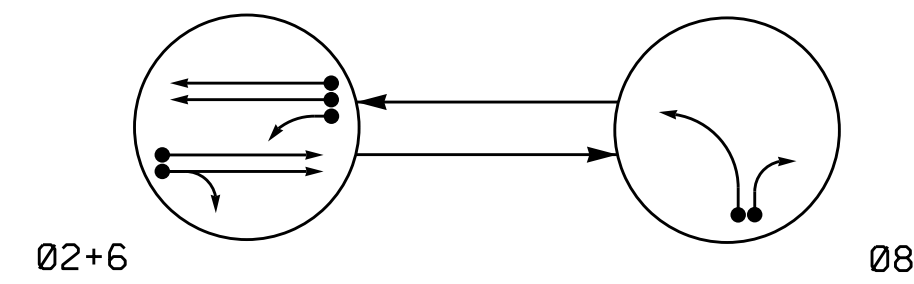
750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SIG. INVENTORY NO. 04-1416

10-SEP-2017 13:26
 S:\PROJECTS\1157\SIGNAL\work\hgr\oups\g_Maps\Trk\ck\oups\041416_sml_e_xxx.dgn
 C:\Users\jckland

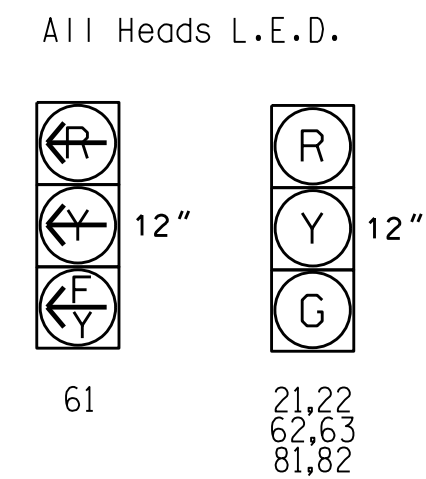
PHASING DIAGRAM



PHASING DIAGRAM DETECTION LEGEND
 ←●→ DETECTED MOVEMENT
 ←○→ UNDETECTED MOVEMENT (OVERLAP)
 ←- - - UNSIGNALIZED MOVEMENT
 ←- - - PEDESTRIAN MOVEMENT

SIGNAL FACE	PHASE		
	02+6	08	FLASH
21,22	G	R	Y
61	F	R	Y
62,63	G	R	Y
81,82	R	G	R

SIGNAL FACE I.D.

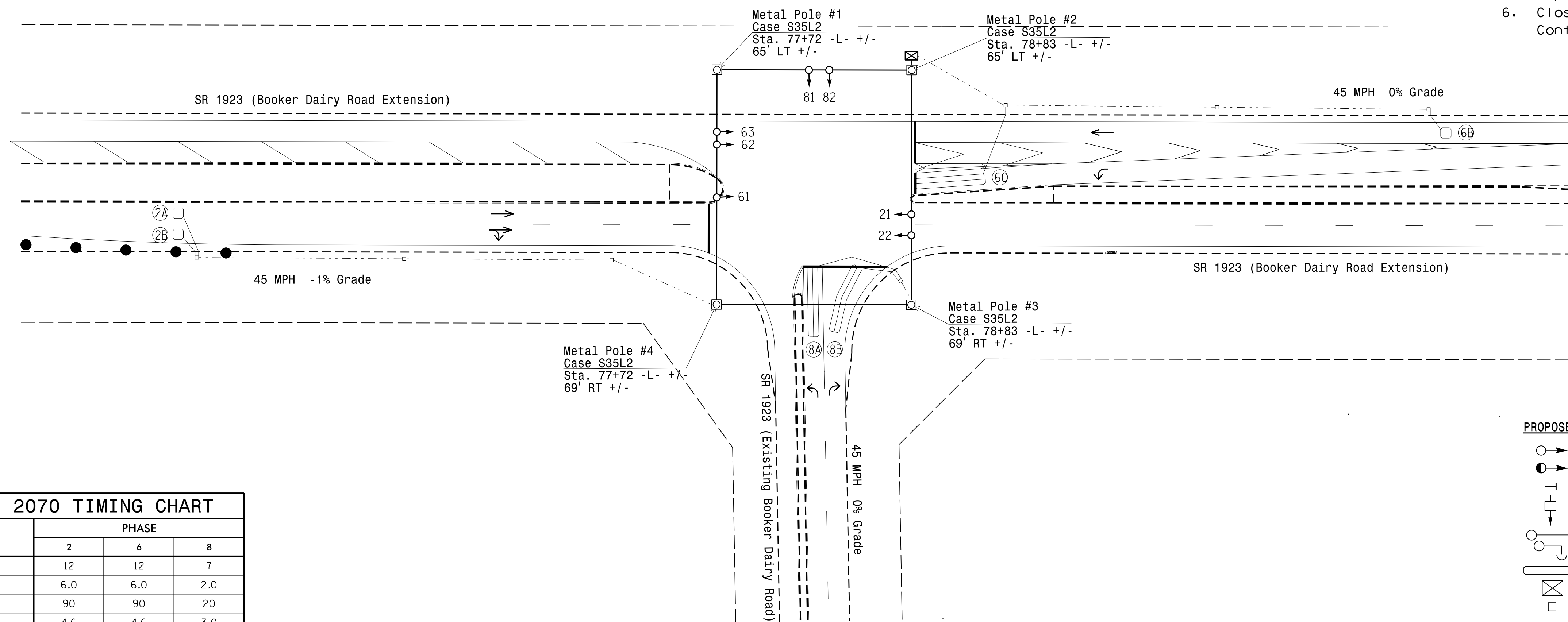


OASIS 2070 LOOP & DETECTOR INSTALLATION CHART												
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	DETECTOR PROGRAMMING							
					PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	LOOP SYSTEM	NEW CARD
2A	6X6	300	6	Y	2	Y	Y	-	-	-	-	Y
2B	6X6	300	6	Y	2	Y	Y	-	-	-	-	Y
6B	6X6	300	4	Y	6	Y	Y	-	-	-	-	Y
6C	6X40	0	2-4-2	Y	6	Y	Y	Y	-	3	-	Y
8A	6X40	0	2-4-2	Y	8	Y	Y	-	-	-	-	Y
8B	6X40	0	2-4-2	Y	8	Y	Y	-	-	15	-	Y

2 Phase Fully Actuated Booker Dairy Road CLS

NOTES

- Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
- Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
- Set all detector units to presence mode.
- Locate new cabinet so as not to obstruct sight distance of vehicles turning right on red.
- Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
- Closed loop system data: Controller Asset #: 1417.



FEATURE	PHASE		
	2	6	8
Min Green 1 *	12	12	7
Extension 1 *	6.0	6.0	2.0
Max Green 1 *	90	90	20
Yellow Clearance	4.6	4.6	3.0
Red Clearance	1.4	1.4	2.3
Walk 1 *	-	-	-
Don't Walk 1	-	-	-
Seconds Per Actuation *	1.5	1.5	-
Max Variable Initial *	34	34	-
Time Before Reduction *	15	15	-
Time To Reduce *	30	30	-
Minimum Gap	3.0	3.0	-
Recall Mode	MIN RECALL	MIN RECALL	-
Vehicle Call Memory	YELLOW	YELLOW	-
Dual Entry	-	-	-
Simultaneous Gap	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

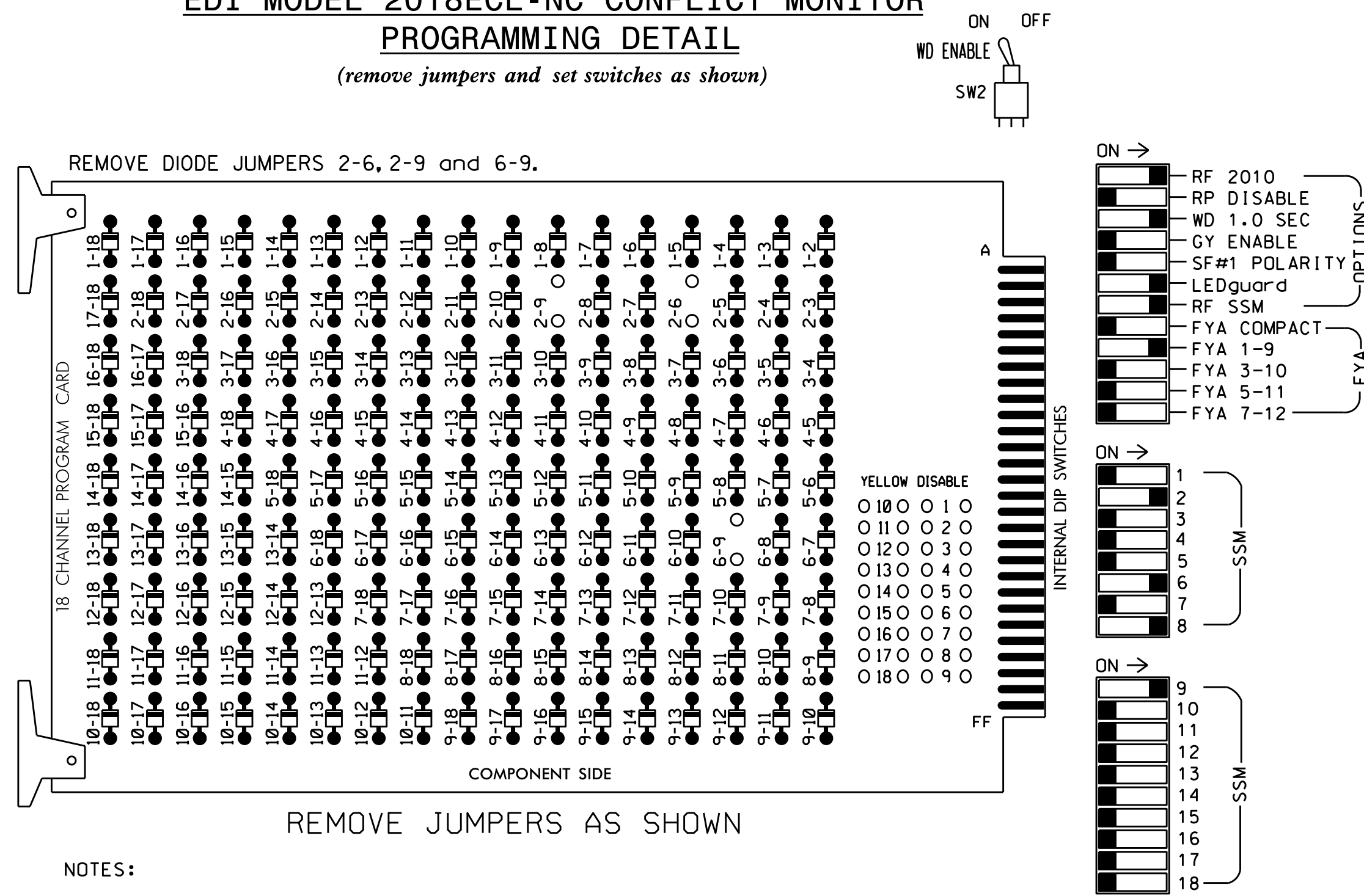
PROPOSED	EXISTING
○→ Traffic Signal Head	●→
○→ Modified Signal Head	N/A
⊥ Sign	⊥
⊥ Pedestrian Signal Head With Push Button & Sign	⊥
○→ Signal Pole with Guy	●→
○→ Signal Pole with Sidewalk Guy	●→
⊠ Inductive Loop Detector	⊠
⊠ Controller & Cabinet	⊠
⊠ Junction Box	⊠
- - - 2-in Underground Conduit	- - -
N/A Right of Way	- - -
→ Directional Arrow	→
⊠ Metal Strain Pole	⊠

New Installation -Temporary Design

	SR 1923 (Booker Road Extension) at SR 1923 (Existing Booker Dairy Road)		SEAL NORTH CAROLINA PROFESSIONAL ENGINEER JASON P. GALLAWAY 029904 8/29/2017
	Division 4 Johnston County Smithfield PLAN DATE: June 2017 REVIEWED BY: JPG PREPARED BY: KGP, Jr. REVIEWED BY:	REVISIONS INIT. DATE	

EDI MODEL 2018ECL-NC CONFLICT MONITOR
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

INPUT FILE POSITION LAYOUT

(front view)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE U	∅ S	∅ 2	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	FS
"I"	∅ S	2A	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	DC ISOLATOR
L	∅ S	∅ 2	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	ST
FILE U	∅ S	NOT USED	∅ 6	∅ S	∅ S	∅ 8	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	DC ISOLATOR
"J"	∅ S	∅ 6	6C	∅ S	∅ S	∅ 8	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	
L	∅ S	6B	NOT USED	∅ S	∅ S	∅ 8	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	∅ S	

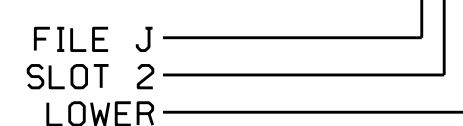
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
6C	TB3-9,10	J3U	64	26	36	6	Y	Y	Y		3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			15

INPUT FILE POSITION LEGEND: J2L



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Startup In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Booker Dairy Road Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
CABINET.....332 W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S8,S11,AUX S1
PHASES USED.....2,6,8
OVERLAP "A".....2
OVERLAP "B".....NOT USED
OVERLAP "C".....NOT USED
OVERLAP "D".....NOT USED

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE: 12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH: X
VEH OVL NOT PED: X
VEH OVL GRN EXT: X
STARTUP COLOR: _ RED _ YELLOW _ GREEN
FLASH COLORS: _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).....0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

SIGNAL HEAD HOOK-UP CHART

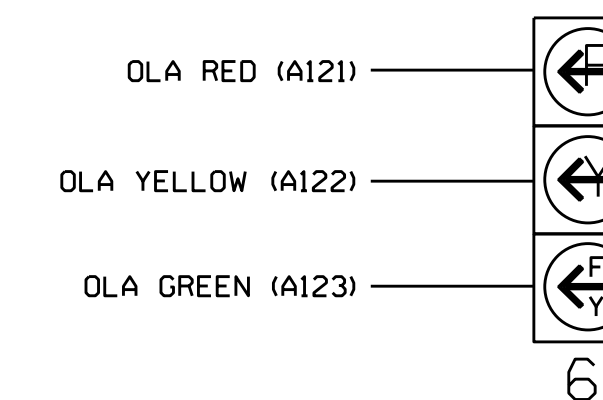
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	21,22	NU	NU	NU	NU	NU	62,63	NU	NU	81,82	NU	61	NU	NU	NU	NU	NU	
RED		128								134		107							
YELLOW		129								135		108							
GREEN		130								136		109							
RED ARROW																		A121	
YELLOW ARROW																			A122
FLASHING YELLOW ARROW																			A123
GREEN ARROW																			

NU = Not Used

* See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

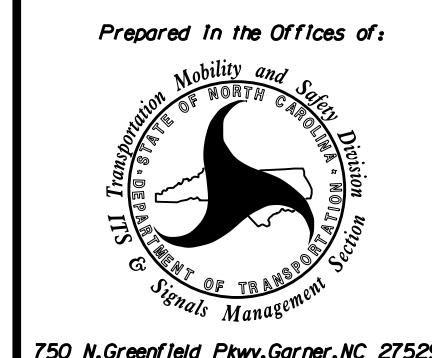


61

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1417T
DESIGNED: June 2017
SEALED: 8-29-17
REVISED: N/A

Electrical Detail - Temporary Design

ELECTRICAL AND PROGRAMMING DETAILS FOR:



SR 1923 (Booker Road Extension) at SR 1923 (Existing Booker Dairy Road)

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY:

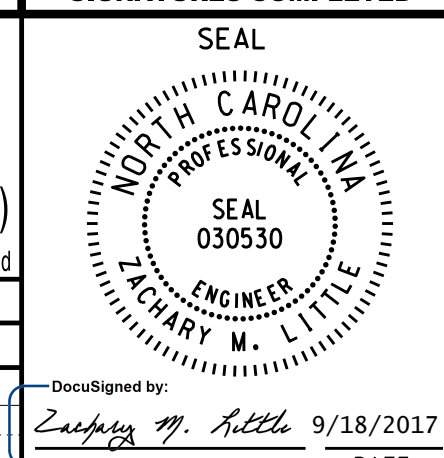
PREPARED BY: James Peterson REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: 9/18/2017

SIG. INVENTORY NO. 04-1417T

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



2 Phase Fully Actuated Booker Dairy Road CLS

PHASING DIAGRAM

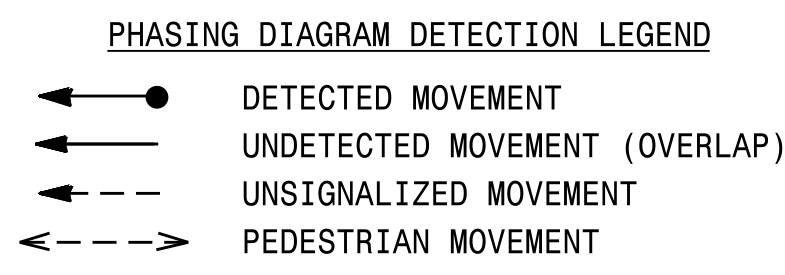
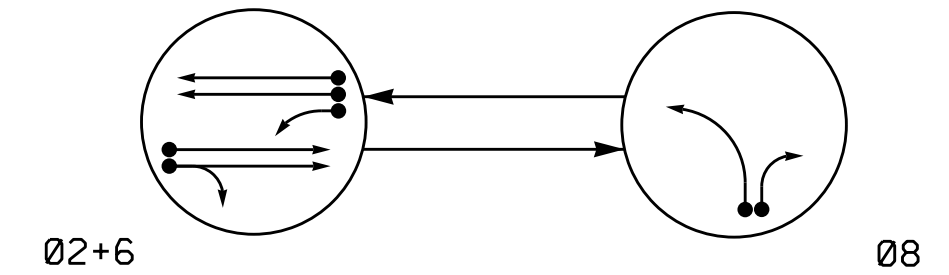
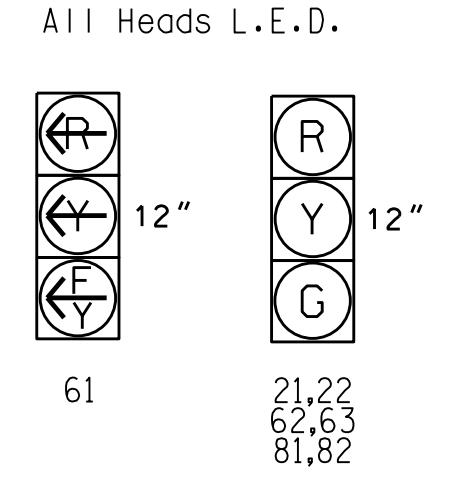


TABLE OF OPERATION

SIGNAL FACE	PHASE		
	02+6	08	FLASH
21,22	G	R	Y
61	F	R	Y
62,63	G	R	Y
81,82	R	G	R

SIGNAL FACE I.D.

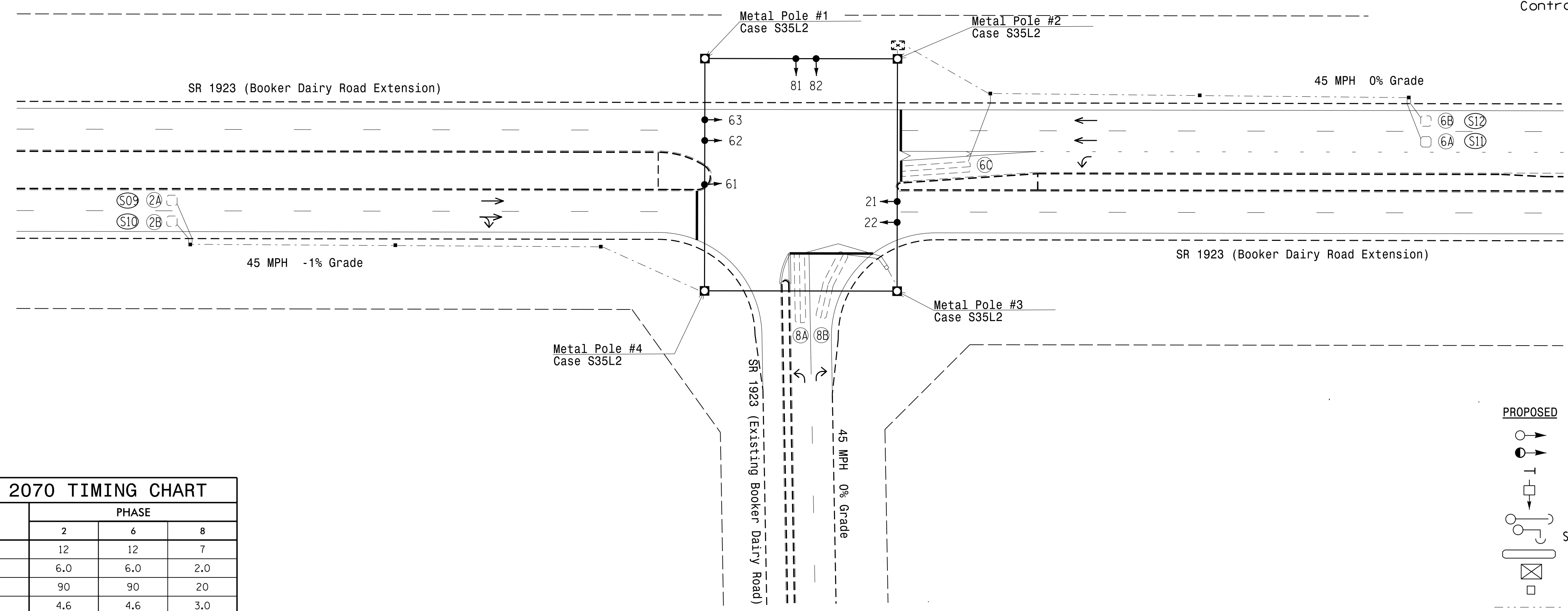


OASIS 2070 LOOP & DETECTOR INSTALLATION CHART

LOOP	INDUCTIVE LOOPS			DETECTOR PROGRAMMING				STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PHASE	CALLING	EXTENSION				
2A/S09	6X6	300	6	-	2	Y	Y	-	-	-	-
2B/S10	6X6	300	6	-	2	Y	Y	-	-	-	-
6A/S11	6X6	300	4	Y	6	Y	Y	-	-	-	Y
6B/S12	6X6	300	4	-	6	Y	Y	-	-	-	-
6C	6X40	0	2-4-2	-	6	Y	Y	Y	-	3	-
8A	6X40	0	2-4-2	-	8	Y	Y	-	-	-	-
8B	6X40	0	2-4-2	-	8	Y	Y	-	-	15	-

NOTES

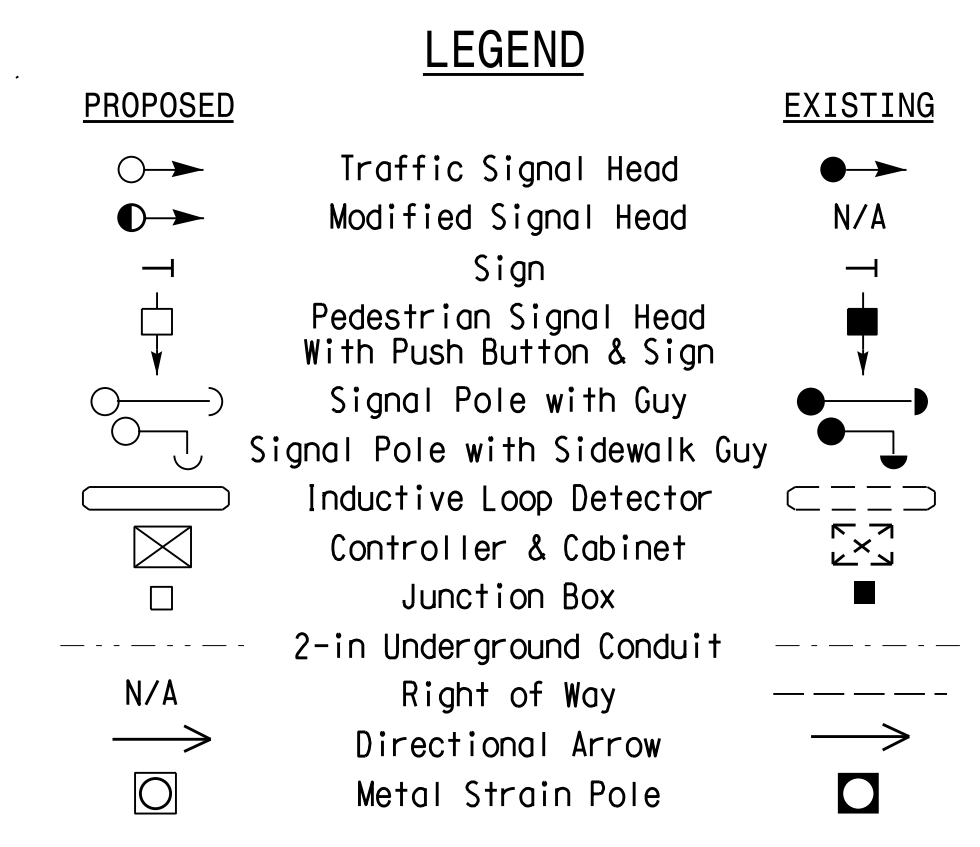
1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Reposition existing signal heads numbered 62, 63.
4. Set all detector units to presence mode.
5. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
6. Closed loop system data: Controller Asset #: 1417.



OASIS 2070 TIMING CHART

FEATURE	PHASE		
	2	6	8
Min Green 1 *	12	12	7
Extension 1 *	6.0	6.0	2.0
Max Green 1 *	90	90	20
Yellow Clearance	4.6	4.6	3.0
Red Clearance	1.4	1.4	2.3
Walk 1 *	-	-	-
Don't Walk 1	-	-	-
Seconds Per Actuation *	1.5	1.5	-
Max Variable Initial *	34	34	-
Time Before Reduction *	15	15	-
Time To Reduce *	30	30	-
Minimum Gap	3.0	3.0	-
Recall Mode	MIN RECALL	MIN RECALL	-
Vehicle Call Memory	YELLOW	YELLOW	-
Dual Entry	-	-	-
Simultaneous Gap	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.



Signal Upgrade - Final Design

Prepared in the Offices of:
Transportation Mobility and Safety Solutions
 A Division of **SMITHFIELD ENGINEERS & ARCHITECTS, P.A.**
 Signal Design Section

SR 1923 (Booker Road Extension) at SR 1923 (Existing Booker Dairy Road)

Division 4 Johnston County Smithfield

PLAN DATE: June 2017 REVIEWED BY: JPG

PREPARED BY: KGP, Jr. REVIEWED BY:

REVISIONS	INIT.	DATE

750 N. Greenfield Pkwy, Garner, NC 27529

SCALE: 0 40
1" = 40'

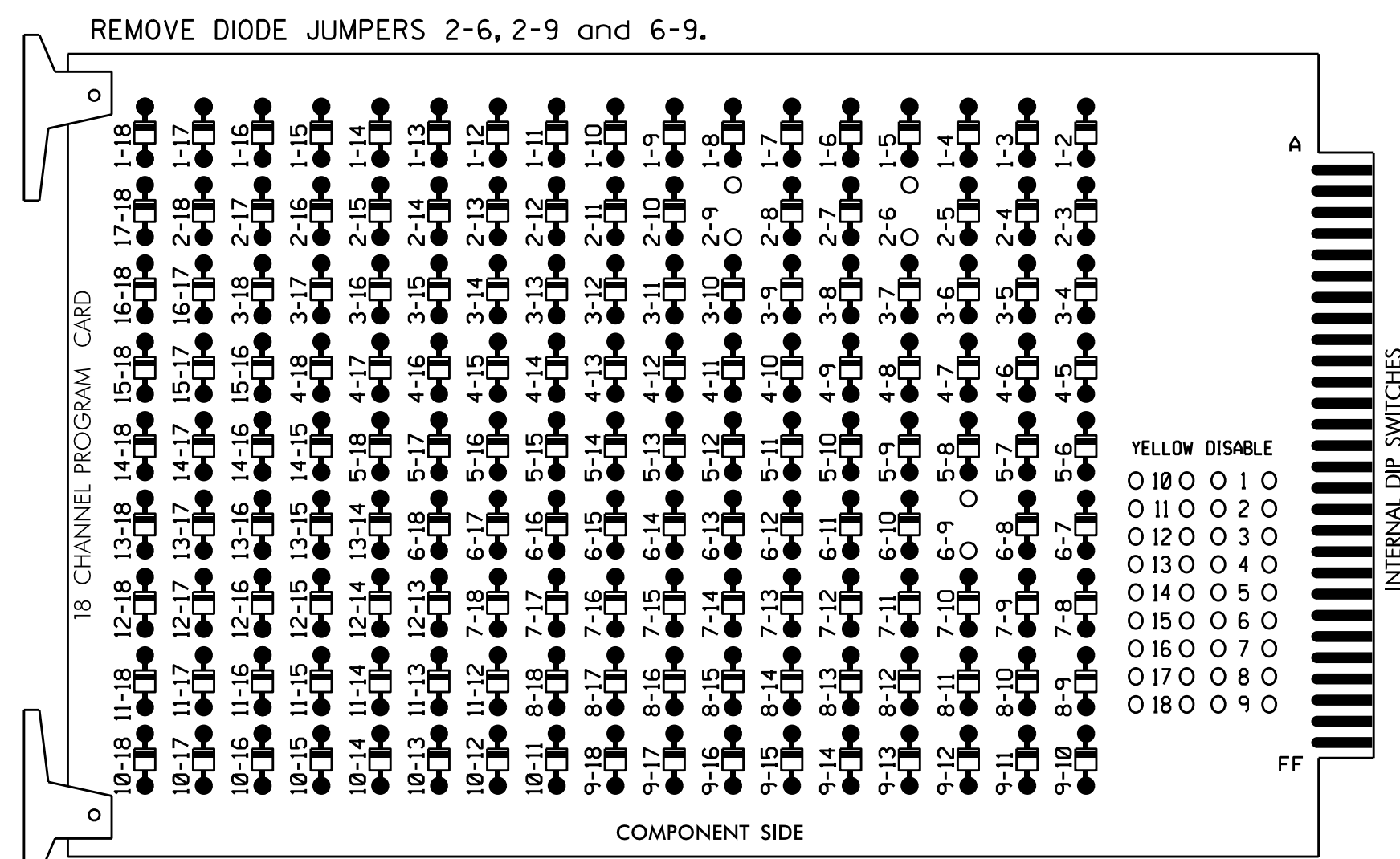
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
 MORRIS C. CARDINA
 PROFESSIONAL ENGINEER
 No. 029904
 JASON P. GALLAGHER
 8/29/2017
 DATE

SIG. INVENTORY NO. 04-1417

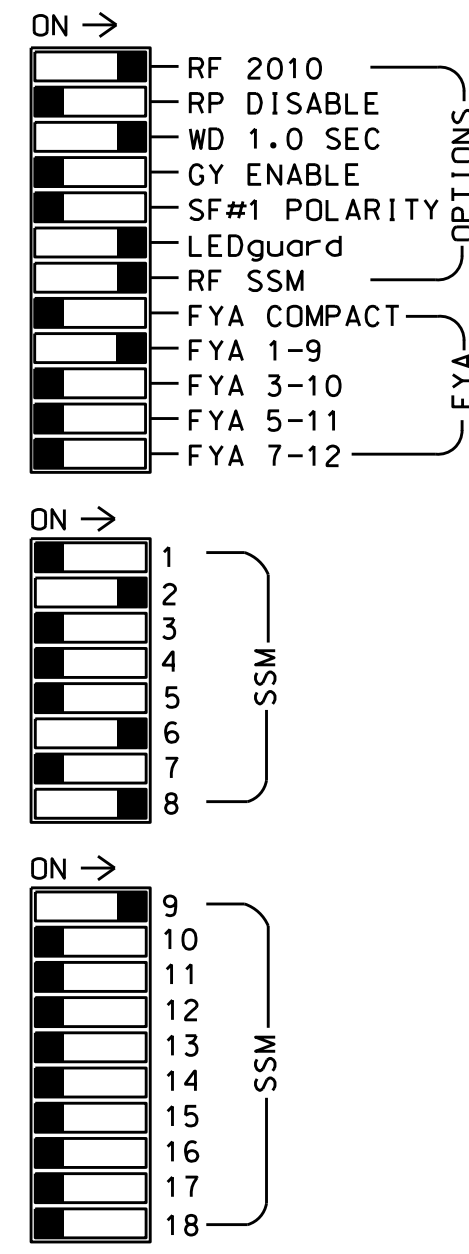
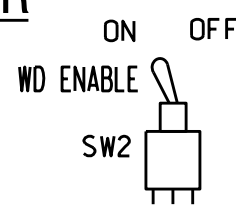
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



■ = DENOTES POSITION OF SWITCH

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all Phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Startup In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the Booker Dairy Road Closed Loop System.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S8,S11,AUX S1
 PHASES USED.....2,6,8
 OVERLAP "A".....2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

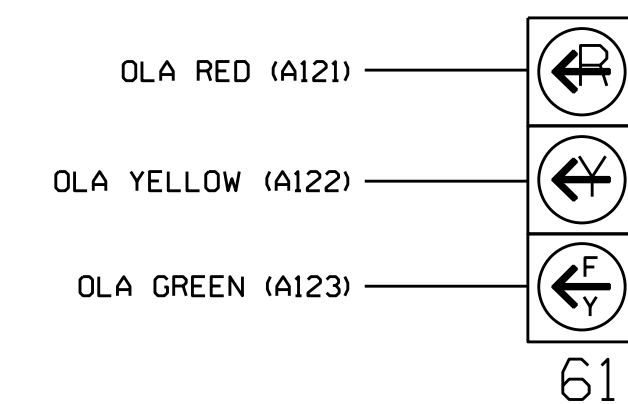
LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	NU	NU	NU	62,63	NU	NU	81,82	NU	61	NU	NU	NU	NU	NU
RED		128						134			107							
YELLOW		129						135			108							
GREEN		130						136			109							
RED ARROW													A121					
YELLOW ARROW													A122					
FLASHING YELLOW ARROW													A123					
GREEN ARROW																		

NU = Not Used

* See pictorial of head wiring in detail this sheet.

FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



INPUT FILE POSITION LAYOUT

(front view)

FILE "I"	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS	∅2/SYS
L	2A/S09	2B/S10												
U	∅6/SYS	∅6/SYS	∅6	∅6/SYS	∅6/SYS	∅8	∅8	∅8	∅8	∅8	∅8	∅8	∅8	∅8
L	6A/S11	6B/S12	6C	6D	6E	8A	8B	8C	8D	8E	8F	8G	8H	8I

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

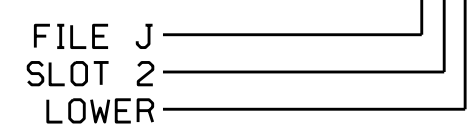
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
 PHASE: 12345678910111213141516
 VEH OVL PARENTS: X
 VEH OVL NOT VEH: X
 VEH OVL NOT PED: X
 VEH OVL GRN EXT: X
 STARTUP COLOR: _ RED _ YELLOW _ GREEN
 FLASH COLORS: _ RED _ YELLOW X GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...Y
 GREEN EXTENSION (0-255 SEC)...0.0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

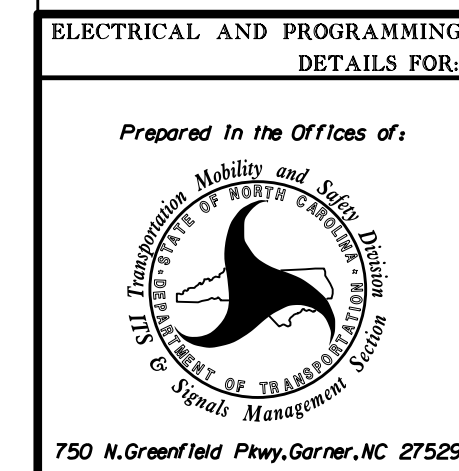
INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A/S09	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B/S10	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
6A/S11	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S12	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
6C	TB3-9,10	J3U	64	26	36	6	Y	Y	Y		3
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			15

INPUT FILE POSITION LEGEND: J2L



Electrical Detail - Final Design

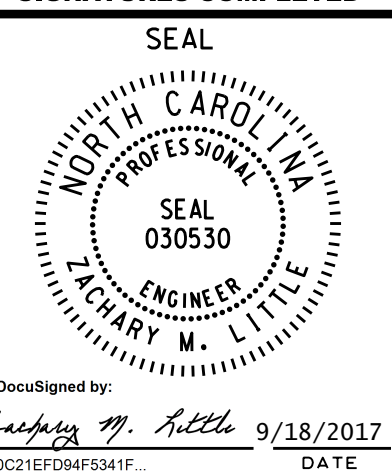


SR 1923 (Booker Road Extension)
 at
 SR 1923 (Existing Booker Dairy Road)

Division 4 Johnston County Smithfield
 PLAN DATE: August 2017 REVIEWED BY:
 PREPARED BY: James Peterson REVIEWED BY:

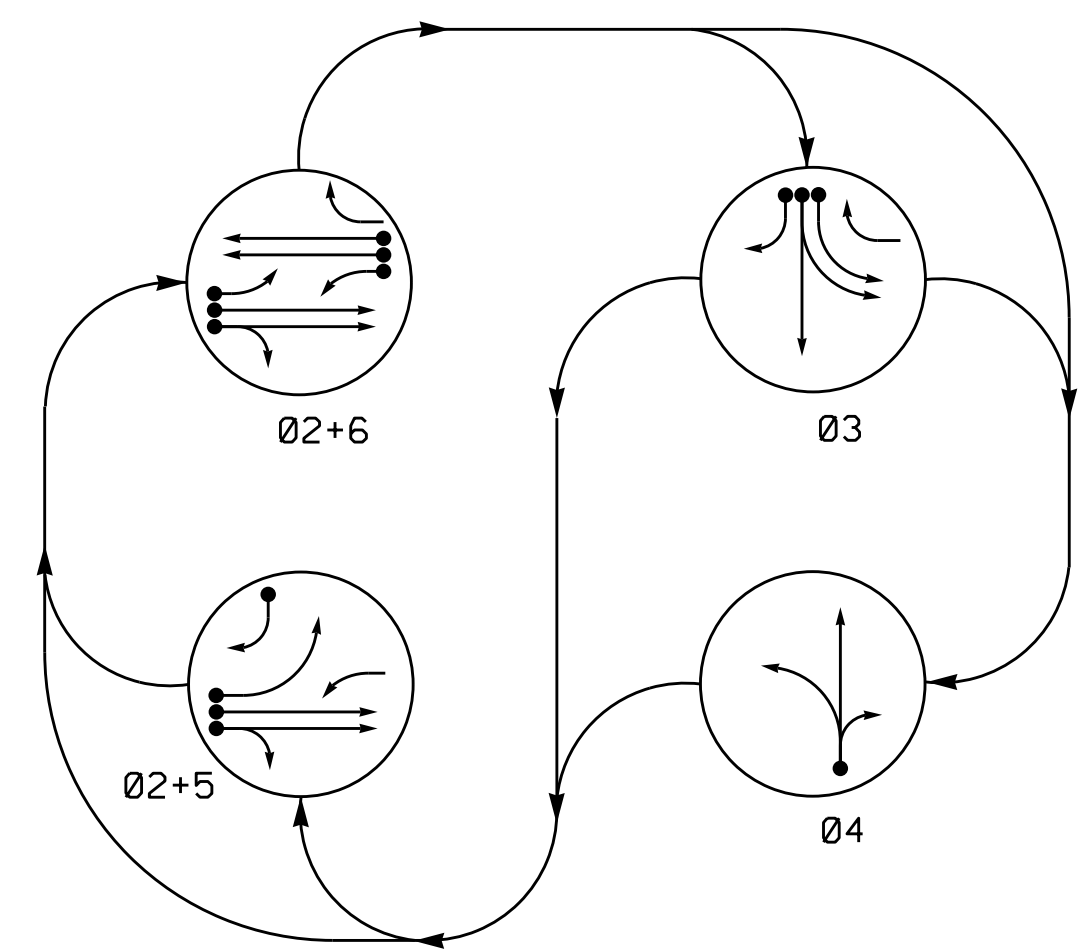
REVISIONS	INIT.	DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED



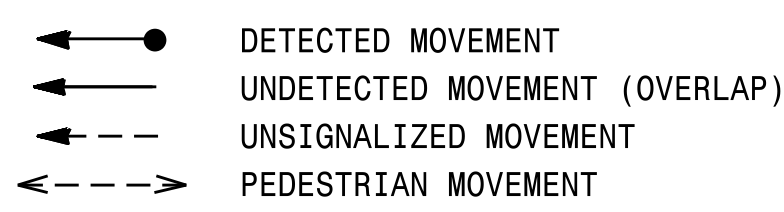
DocuSigned by:
 Zachary M. Little 9/18/2017
 DATE
 SIG. INVENTORY NO. 04-1417

PHASING DIAGRAM



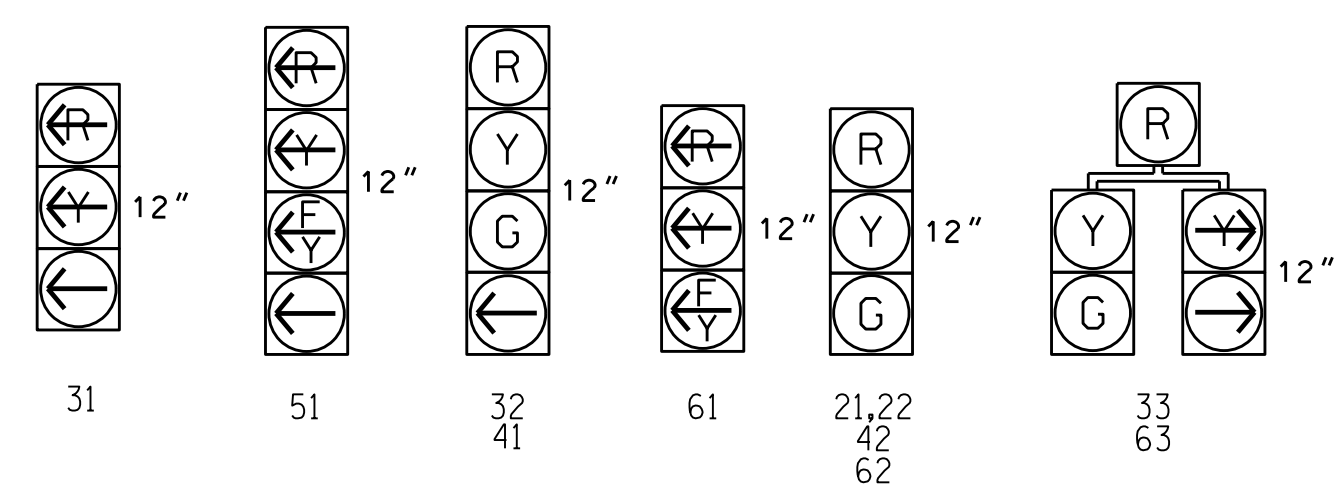
SIGNAL FACE	PHASE				
	02+5	02+6	03	04	F L S B H
21,22	G	G	R	R	Y
31	←	←	←	←	←
32	R	R	G	R	R
33	←	←	R	G	R
41	R	R	R	G	R
42	R	R	R	G	R
51	←	←	←	←	←
61	←	←	R	R	Y
62	R	G	R	R	Y
63	R	G	R	R	Y

PHASING DIAGRAM DETECTION LEGEND



SIGNAL FACE I.D.

All Heads L.E.D.



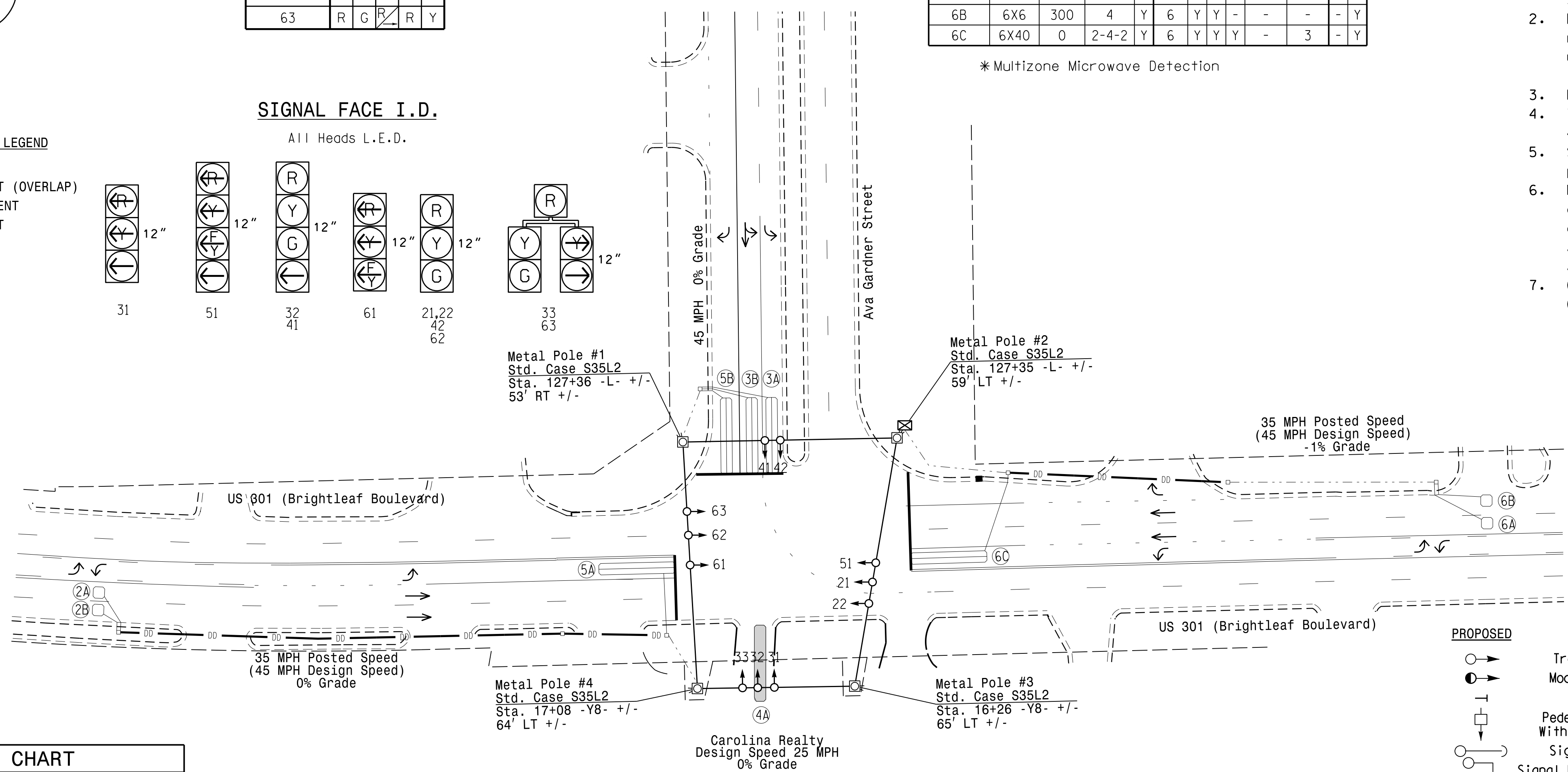
OASIS 2070 LOOP & DETECTOR INSTALLATION CHART												
INDUCTIVE LOOPS				DETECTOR PROGRAMMING								
LOOP	SIZE (FT)	DISTANCE FROM STOPBAR (FT)	TURNS	NEW LOOP	PHASE	CALLING	EXTENSION	FULL TIME DELAY	STRETCH TIME	DELAY TIME	SYSTEM LOOP	NEW CARD
2A	6X6	300	6	Y	2	Y	Y	-	-	-	-	Y
2B	6X6	300	6	Y	2	Y	Y	-	-	-	-	Y
3A	6X40	0	2-4-2	Y	3	Y	Y	-	-	-	-	Y
3B	6X40	0	2-4-2	Y	3	Y	Y	-	-	-	-	Y
4A	6X40	0	*	Y	4	Y	Y	-	-	5	-	Y
5A	6X40	0	2-4-2	Y	2	Y	Y	-	-	3	-	Y
5B	6X40	0	2-4-2	Y	5	Y	Y	-	-	-	-	Y
6A	6X6	300	4	Y	6	Y	Y	-	-	-	-	Y
6B	6X6	300	4	Y	6	Y	Y	-	-	-	-	Y
6C	6X40	0	2-4-2	Y	6	Y	Y	-	-	3	-	Y

* Multizone Microwave Detection

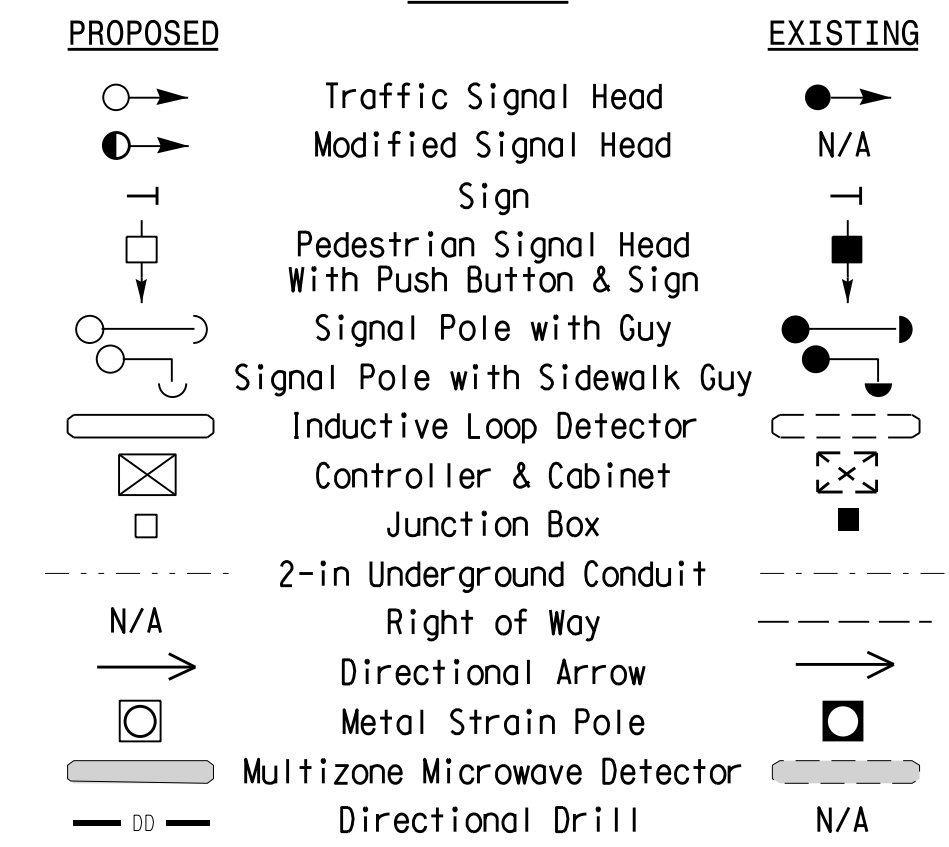
4 Phase Fully Actuated US 301-NC 96 (Brightleaf Blvd.) CLS

NOTES

1. Refer to "Roadway Standard Drawings NCDOT" dated January 2018 and "Standard Specifications for Roads and Structures" dated January 2018.
2. Do not program signal for late night flashing operation unless otherwise directed by the Engineer.
3. Phase 5 may be lagged.
4. The order of phase 3 and phase 4 may be reversed.
5. Set all detector units to presence mode.
6. Maximum times shown in timing chart are for free-run operation only. Coordinated signal system timing values supersede these values.
7. Closed loop system data: Controller Asset #: 1418.



LEGEND



FEATURE	PHASE				
	2	3	4	5	6
Min Green 1 *	12	7	7	7	12
Extension 1 *	6.0	2.0	2.0	2.0	6.0
Max Green 1 *	90	20	20	25	90
Yellow Clearance	4.6	4.5	3.2	3.0	4.6
Red Clearance	1.6	1.6	2.1	2.9	1.6
Walk 1 *	-	-	-	-	-
Don't Walk 1	-	-	-	-	-
Seconds Per Actuation *	1.5	-	-	-	1.5
Max Variable Initial *	34	-	-	-	34
Time Before Reduction *	15	-	-	-	15
Time To Reduce *	30	-	-	-	30
Minimum Gap	3.0	-	-	-	3.0
Recall Mode	MIN RECALL	-	-	-	MIN RECALL
Vehicle Call Memory	YELLOW	-	-	-	YELLOW
Dual Entry	-	-	-	-	-
Simultaneous Gap	ON	ON	ON	ON	ON

* These values may be field adjusted. Do not adjust Min Green and Extension times for phases 2 and 6 lower than what is shown. Min Green for all other phases should not be lower than 4 seconds.

New Installation

Prepared in the Offices of: **TRANSPORTATION MOBILITY AND SAFETY SOLUTIONS**
 STATE OF NORTH CAROLINA
 DEPARTMENT OF TRANSPORTATION
 SIGNAL DESIGN SECTION

US 301 (Brightleaf Boulevard) at Ava Gardner Street

Division 4 Johnston County Smithfield

PLAN DATE: June 2017 REVIEWED BY: JPG

PREPARED BY: KGP, Jr. REVIEWED BY:

750 N. Greenfield Pkwy, Garner, NC 27529

SCALE: 0 40
1" = 40'

REVISIONS: INIT. DATE

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

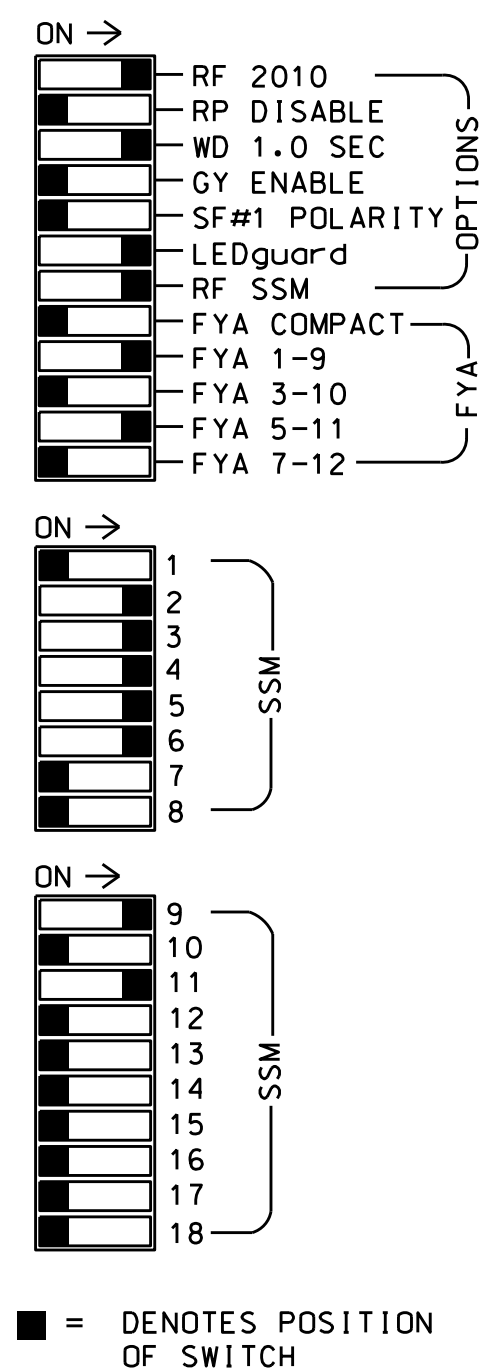
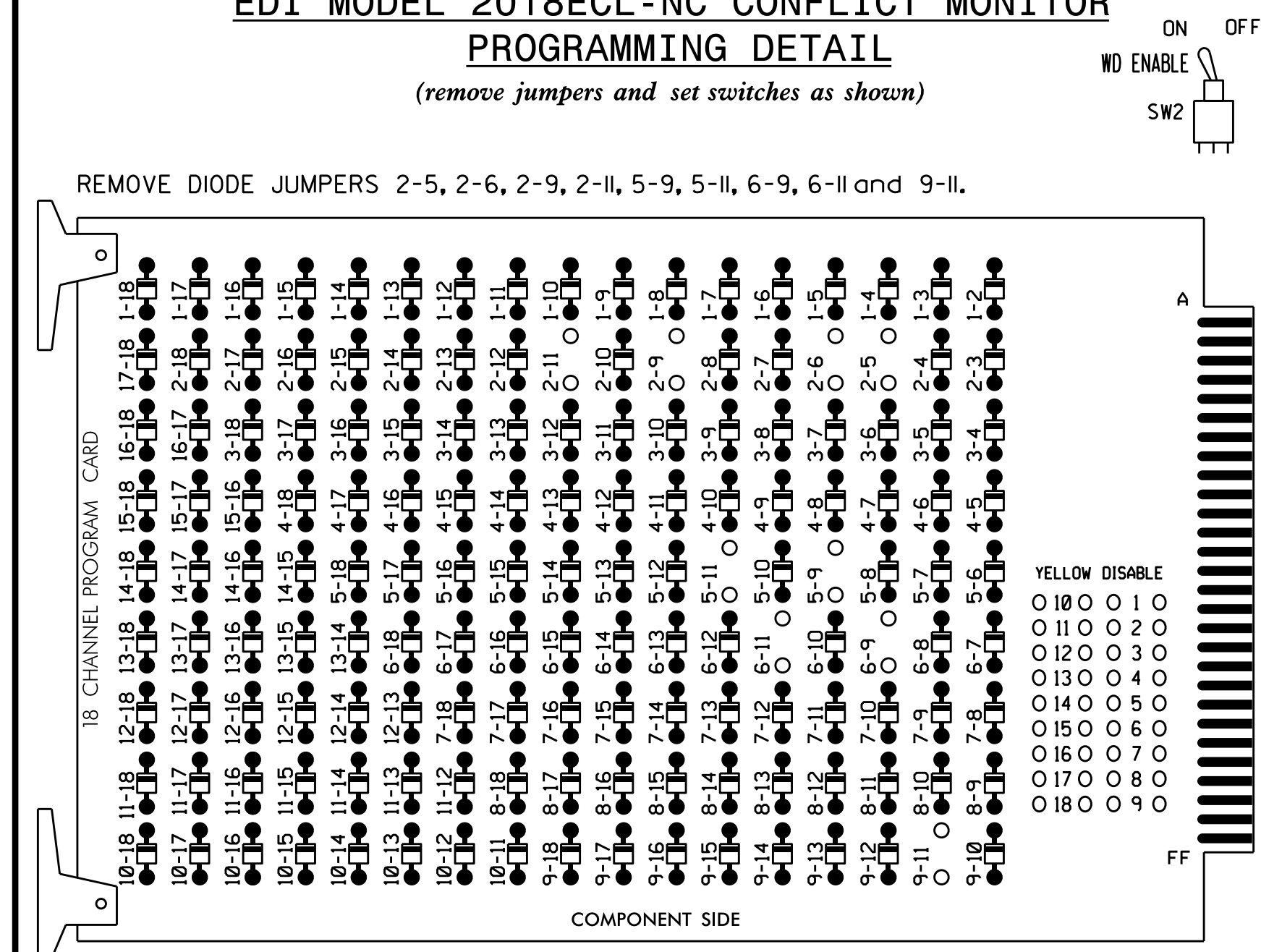
SEAL: JASON P. GALLOWAY, ENGINEER, 029904, 8/30/2017

SIG. INVENTORY NO. 04-1418

29-AUG-2017 16:53 R:\Projects\cans\gms\gms4\04-1418\041118...s\gdsn\2017mtd-gn kgreedrn

EDI MODEL 2018ECL-NC CONFLICT MONITOR
PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
6. The cabinet and controller are part of the US 301-NC 96 (Brightleaf Blvd.) CLS.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S2,S4,S5,S7,S8,AUX S1,AUX S4
 PHASES USED.....2,3,4,5,6
 OVERLAP "A".....2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6				
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18				
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE				
SIGNAL HEAD NO.	NU	21,22	NU	31	32	33	63	41	42	NU	33	51*	62,63	NU	NU	NU	61*	NU	51*	NU	NU	
RED		128		116	116	101	101	*		134												
YELLOW		129		117	117	102	102			135												
GREEN		130		118	118	103	103			136												
RED ARROW				116													A121			A114		
YELLOW ARROW				117		117				132							A122			A115		
FLASHING YELLOW ARROW																	A123			A116		
GREEN ARROW				118	118	118	103			133	133											

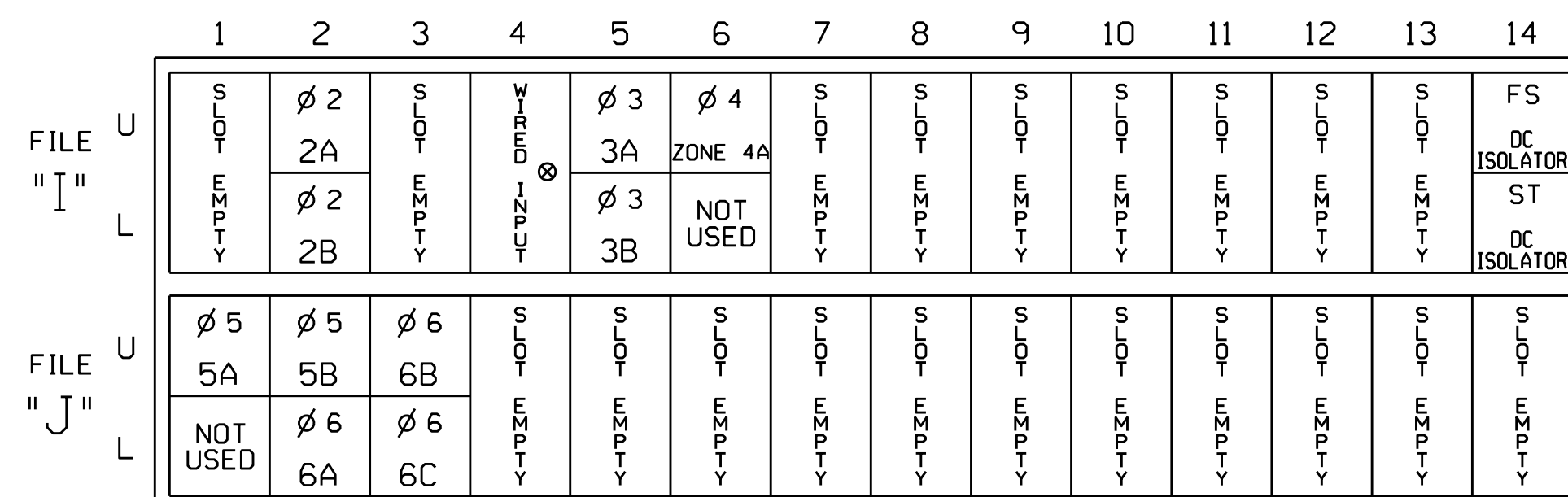
NU = Not Used

* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail this sheet.

INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

INPUT FILE CONNECTION & PROGRAMMING CHART

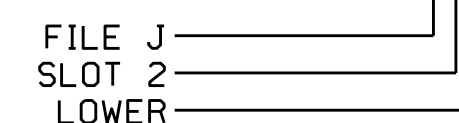
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
3A	TB4-5,6	I5U	58	20	3	3	Y	Y			
3B	TB4-7,8	I5L	58	20	3	3	Y	Y			
4A	★	I6U	41	3	4	4	Y	Y			5
5A ¹	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9	22	2	Y	Y	Y		3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			
6A	TB3-7,8	J2L	44	6	16	6	Y	Y			
6B	TB3-9,10	J3U	64	26	36	6	Y	Y			
6C	TB3-11,12	J3L	77	39	46	6	Y	Y	Y		3

¹Add jumper from J1-W to I4-W, on rear of input file.

★ SPECIAL DETECTOR NOTE

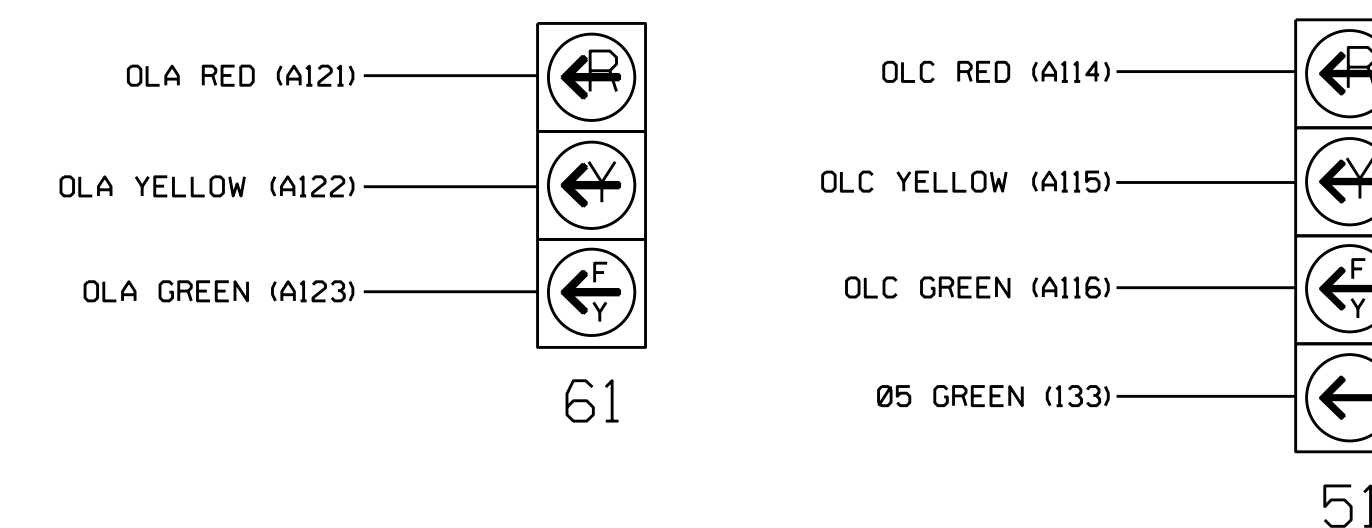
For Zone 4A install a multizone microwave detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plan.

INPUT FILE POSITION LEGEND: J2L



FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)

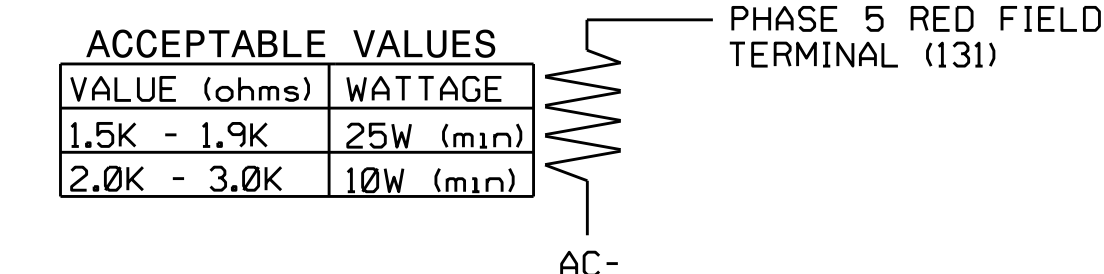


NOTE

The sequence display for signal head 51 requires special logic programming. See sheet 2 for programming instructions.

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)



THIS ELECTRICAL DETAIL IS FOR
 THE SIGNAL DESIGN: 04-1418
 DESIGNED: June 2017
 SEALED: 8/30/2017
 REVISED:

Electrical Detail - Sheet 1 of 2

Electrical and Programming Details for: **US 301 (Brightleaf Boulevard) at Ava Gardner Street**

Prepared In the Offices of:
 G.L. Transportation, Mobility and Safety Division
 NORTH CAROLINA DEPARTMENT OF TRANSPORTATION
 Signal Management Section
 750 N. Greenfield Pkwy, Garner, NC 27529

Division 4 Johnston County Smithfield
 PLAN DATE: August 2017 REVIEWED BY: T. Joyce
 PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

Seal: SEAL 031001
 ENGINEER: TODD JOYCE
 Date: 9/13/2017
 DocuSign by: D. Todd Joyce
 Date: 9/13/2017
 SIG. INVENTORY NO. 04-1418

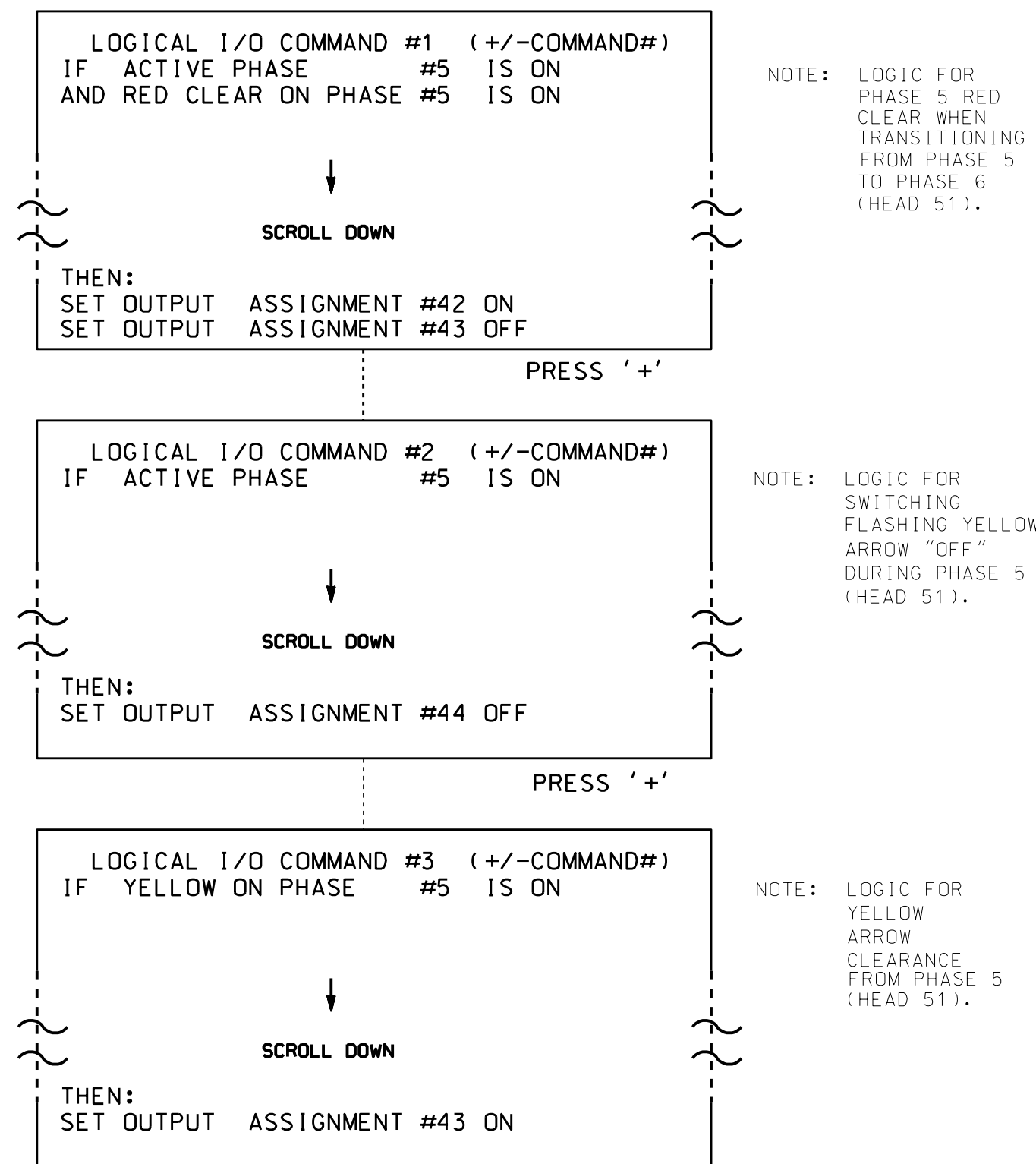
DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

19-SEP-2017 14:53
 S:\IT\ASIS\JIS\SIGNAL\work\hgr\oups\51g_Maps\Strickland\041418_sml_e_041418.dgn
 C:\Users\TJ\Documents

**LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE**

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS: X
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: _ RED _ YELLOW _ GREEN
FLASH COLORS:  _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```

PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:      12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: _ RED _ YELLOW _ GREEN
FLASH COLORS:  _ RED _ YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
  
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 04-1418
DESIGNED: June 2017
SEALED: 8/30/2017
REVISED:

Electrical Detail - Sheet 2 of 2

Prepared In the Offices of:

 750 N. Greenfield Pkwy, Garner, NC 27529

US 301 (Brightleaf Boulevard)
at
Ava Gardner Street

Division 4 Johnston County Smithfield

PLAN DATE: August 2017 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS	INIT.	DATE

DocuSign by: *T. Todd Joyce* 9/13/2017

SIG. INVENTORY NO. 04-1418

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

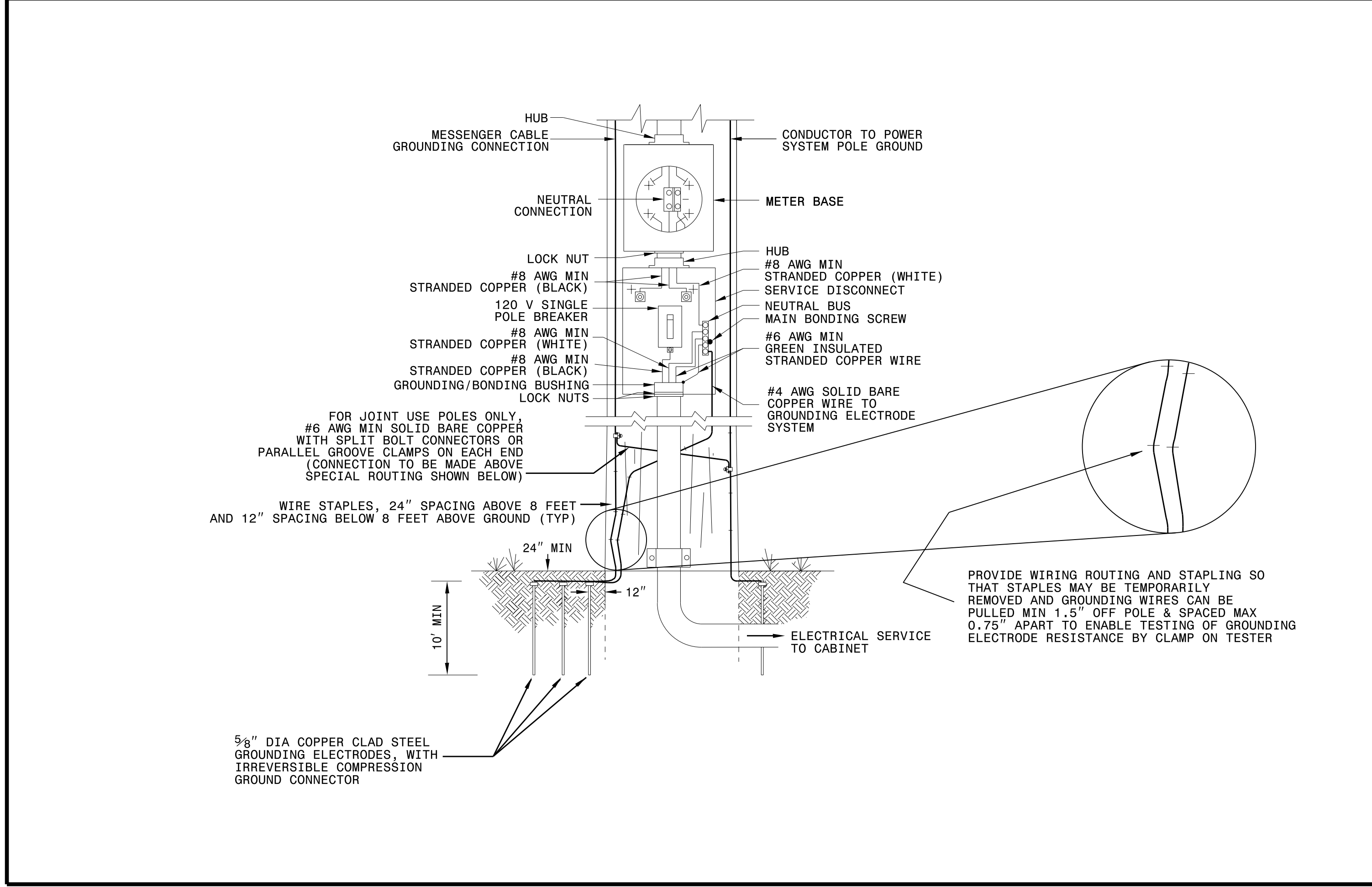
SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 031001
 T. TODD JOYCE

10-SEP-2017 1:54
 S:\PROJECTS\U-3334B\SIG\Signal\work\hgr\output\Sig_Maps\Strickland\041418_sml_e_xxx.dgn
 C:\STRICKLAND

1-18 STATE OF NORTH CAROLINA DEPT. OF TRANSPORTATION DIVISION OF HIGHWAYS RALEIGH, N.C.

ENGLISH STANDARD DRAWING FOR
ELECTRICAL SERVICE GROUNDING
GROUNDING AND BONDING

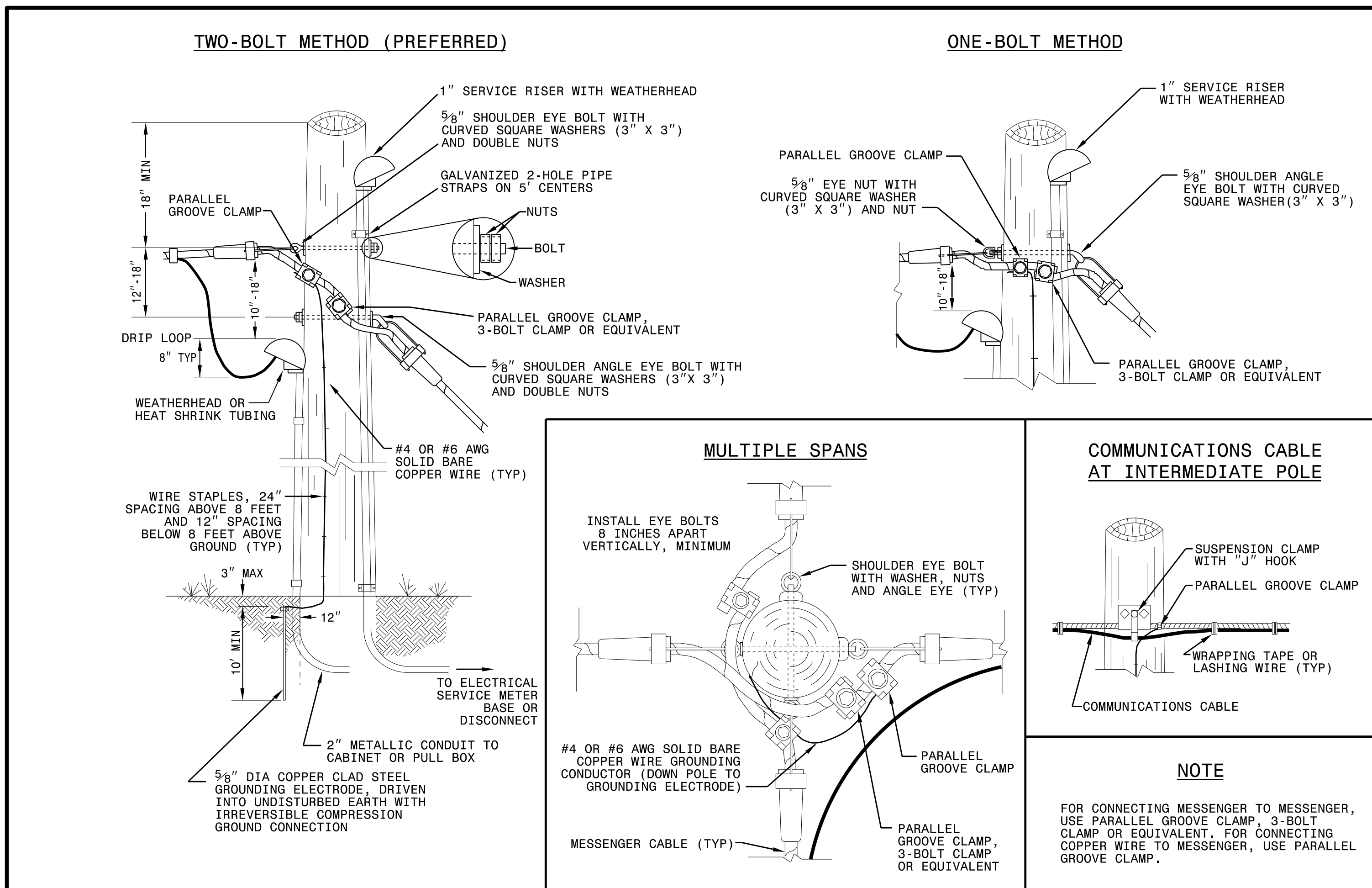
SHEET 1 OF 1
1700D01



1-18 STATE OF NORTH CAROLINA DEPT. OF TRANSPORTATION DIVISION OF HIGHWAYS RALEIGH, N.C.

ENGLISH STANDARD DRAWING FOR
WOOD POLES
METHODS OF ATTACHMENT AND GROUNDING

SHEET 1 OF 1
1720D01



DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

See Plate for Title

Prepared in the Offices of:

SEAL

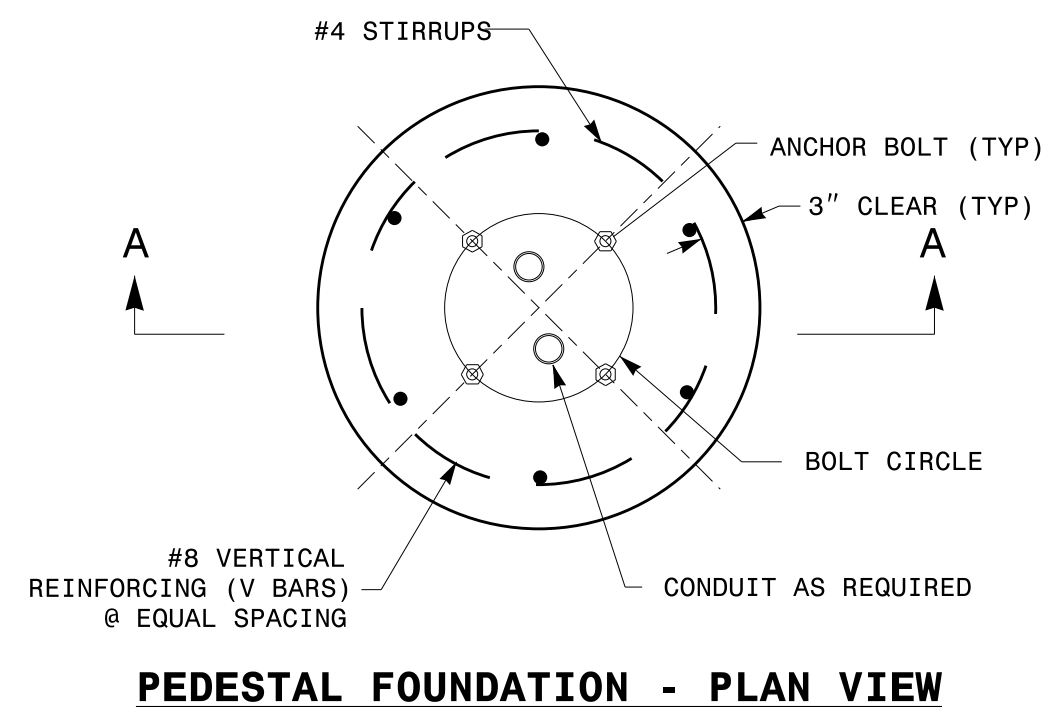
DocuSigned by:
Mohd Aslami

10/11/2017

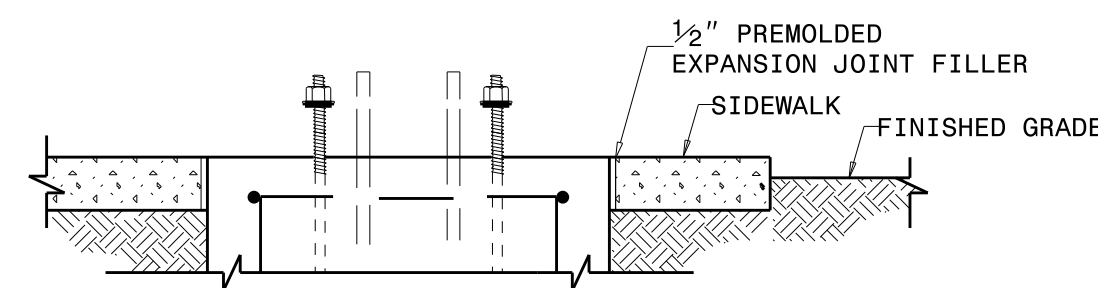
750 N. Greenfield Parkway
Garner, NC 27529

DATE

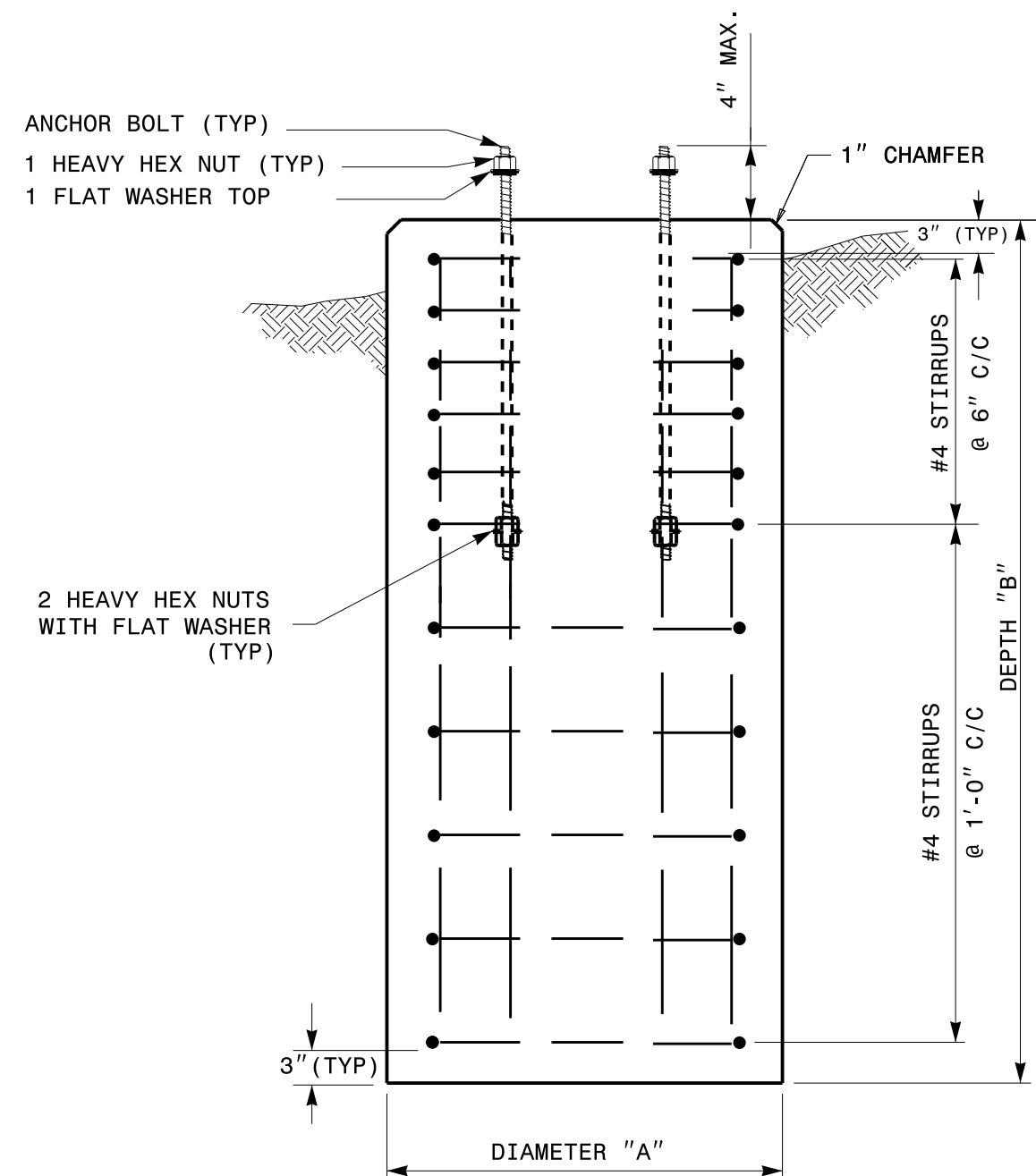
11-0CT-2017_08:56
U:\2018_S14_Drawing\Plate_Sheets\2018_Plate_Sheet.dgn
r:\rough



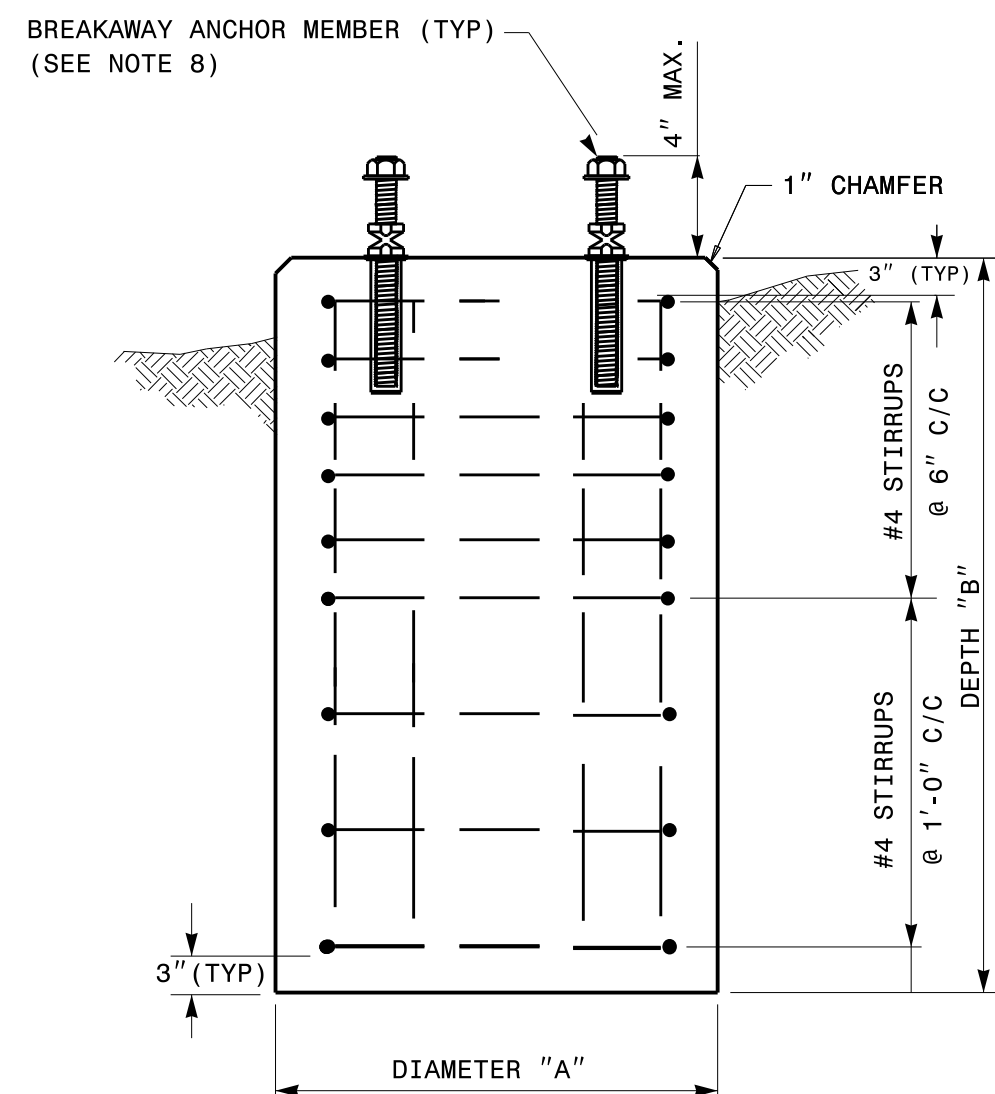
PEDESTAL FOUNDATION - PLAN VIEW



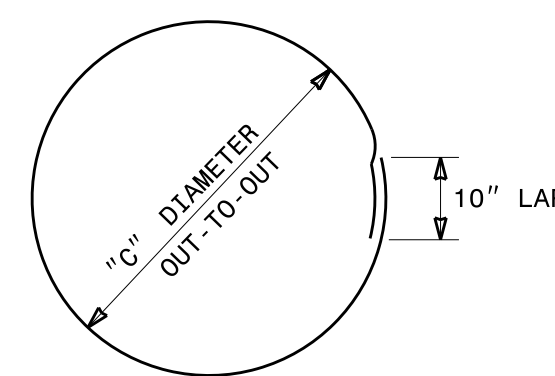
PEDESTAL FOUNDATION DETAILS FOR SIDEWALK



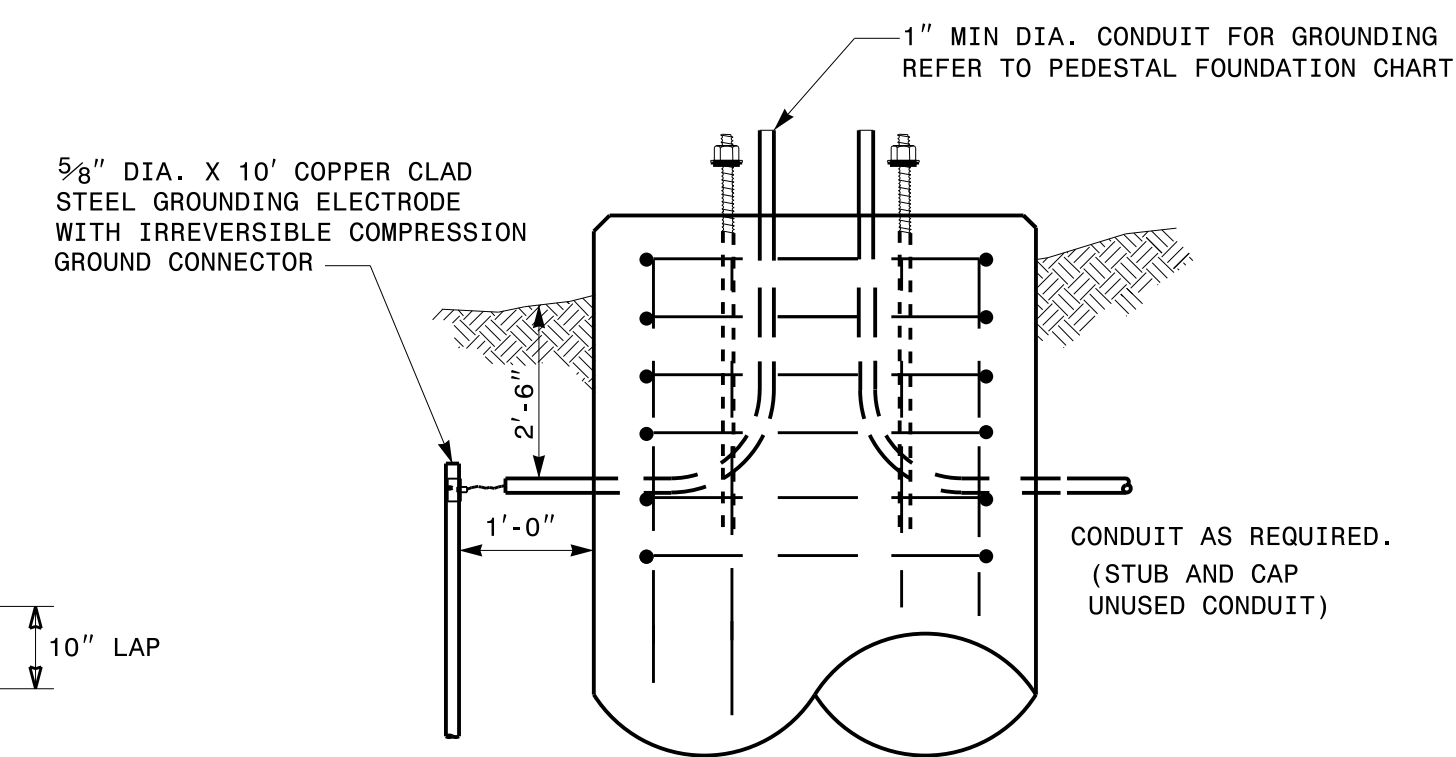
TYPES I, II & III
SECTION A-A



TYPES I & II ONLY
SECTION A-A



CLOSED HOOPS



GROUNDING & CONDUIT DETAIL

NOTES:

- CAST FOUNDATION AGAINST UNDISTURBED SOIL WHEREVER CONDITIONS PERMIT. IN UNSTABLE SOIL, CAST-IN-PLACE TUBE FORMS ARE ALLOWED WITH APPROVAL.
- COMPLY WITH APPLICABLE PROVISIONS OF SECTION 825 FOR CONCRETE CONSTRUCTION.
- USE CLASS "A" CONCRETE THAT MEETS THE REQUIREMENTS OF SECTION 1000 WITH A COMPRESSION STRENGTH AT 28 DAYS OF $F'c = 3000$ PSI (MIN.).
- USE ASTM GRADE 60 DEFORMED BARS FOR ALL REINFORCING STEEL.
- GRADE IS ASSUMED TO BE (8H:1V) OR FLATTER. FOUNDATION SIZE AND DEPTHS ARE BASED ON THE FOLLOWING SOIL DESIGN PARAMETERS:
 - SANDY TYPE SOIL
 - NO GROUND WATER WITHIN 5'-0" OF SURFACE ELEVATION
 - WIND SPEED NOT TO EXCEED 140 MPH
 IF ACTUAL CONDITIONS VARY SUBSTANTIALLY FROM THOSE ASSUMED, THE FOUNDATION DEPTH MAY BE ADJUSTED. IN THIS CASE, CONTACT THE ENGINEER.
- MAINTAIN AT LEAST 3" COVER ON ALL REINFORCEMENT.
- ORIENT CONDUIT AS REQUIRED BY THE DESIGN OR AS DICTATED BY FIELD CONDITIONS.
- USE ADHESIVE ANCHOR FOR THREADED COUPLING INSERT. FOR TYPE I MINIMUM DEPTH NECESSARY IS 0'-4 1/2" AND FOR TYPE II MINIMUM DEPTH NECESSARY IS 0'-6 5/8". FOLLOW MANUFACTURER'S INSTALLATION INSTRUCTIONS.

PEDESTAL FOUNDATION TYPE AND SIZE							
TYPE	PEDESTAL DESCRIPTION	SIZE			ANCHOR BOLT		INSTALL GROUNDING SYSTEM (YES/NO)
		DIAMETER "A" FT	DEPTH "B" FT	CONCRETE VOLUME CY	DIAMETER (MIN.) IN	LENGTH FT-IN	
I	PEDESTRIAN PUSHBUTTON	2'-0"	3'-6"	.41	1/2	1'-6"	NO
II	NORMAL-DUTY	2'-0"	5'-0"	.58	3/4	2'-0"	YES
III	HEAVY-DUTY	2'-6"	7'-0"	1.27	1	4'-0"	YES

REINFORCING STEEL SCHEDULE													
TYPE	V-BAR				STIRRUP								
	SIZE #	QTY	LENGTH	WEIGHT LBS	SIZE #	QUANTITY			LENGTH	DIAMETER "C" FT	OVERLAP MIN.	WEIGHT LBS	TOTAL STEEL WEIGHT LBS
						VERTICAL ON 6" CENTERS	ON 12" CENTERS	TOTAL					
I	8	6	3'-0"	56	4	0	4	4	5'-7"	1'-6"	0'-10"	15	71
II	8	6	4'-6"	86	4	5	3	8	5'-7"	1'-6"	0'-10"	30	116
III	8	6	6'-6"	122	4	7	4	11	7'-2"	2'-0"	0'-10"	53	175

STATE OF NORTH CAROLINA
DEPT. OF TRANSPORTATION
DIVISION OF HIGHWAYS
RALEIGH, N.C.

ENGLISH STANDARD DRAWING FOR
PEDESTALS
FOUNDATIONS

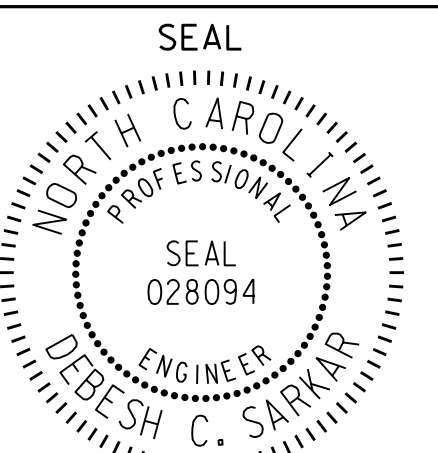
SHEET 1 OF 1
1743D01

See Plate for Title

Prepared in the Offices of:



750 N. Greenfield Parkway
Garner, NC 27529



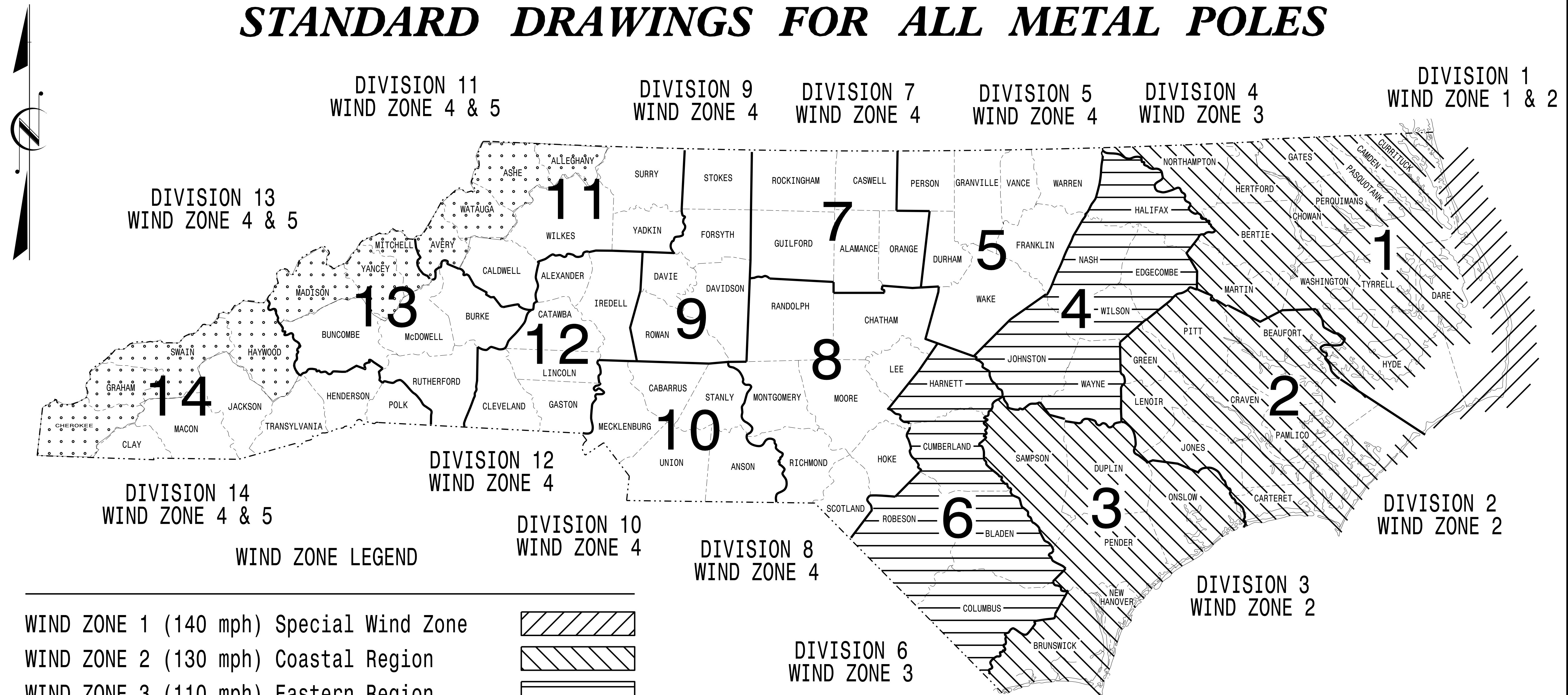
DocuSigned by:
Debesh C. Sarkar
10/11/2017
DATE

DOCUMENT NOT CONSIDERED
FINAL UNLESS ALL
SIGNATURES COMPLETED

STATE OF NORTH CAROLINA DIVISION OF HIGHWAYS

PROJECT I.D. NO. U-3334B	SHEET NO. Sig.M1
------------------------------------	----------------------------

STANDARD DRAWINGS FOR ALL METAL POLES

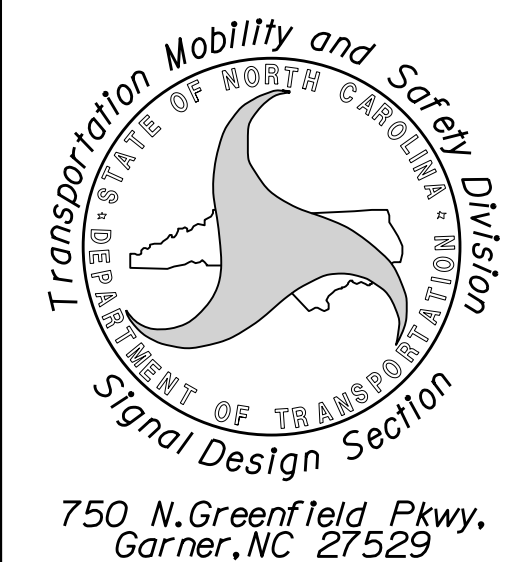


WIND ZONE LEGEND

WIND ZONE 1 (140 mph) Special Wind Zone	
WIND ZONE 2 (130 mph) Coastal Region	
WIND ZONE 3 (110 mph) Eastern Region	
WIND ZONE 4 (90 mph) Central & Mtn. Region	
WIND ZONE 5 (120 mph) Special Wind Zone	

<https://connect.ncdot.gov/resources/safety/Pages/ITS-Design-Resources.aspx>

Prepared In the Offices of:



Designed in conformance
with the latest
2015 Interim to the
6th Edition 2013
AASHTO
Standard Specifications for
Structural Supports for
Highway Signs, Luminaires,
and Traffic Signals

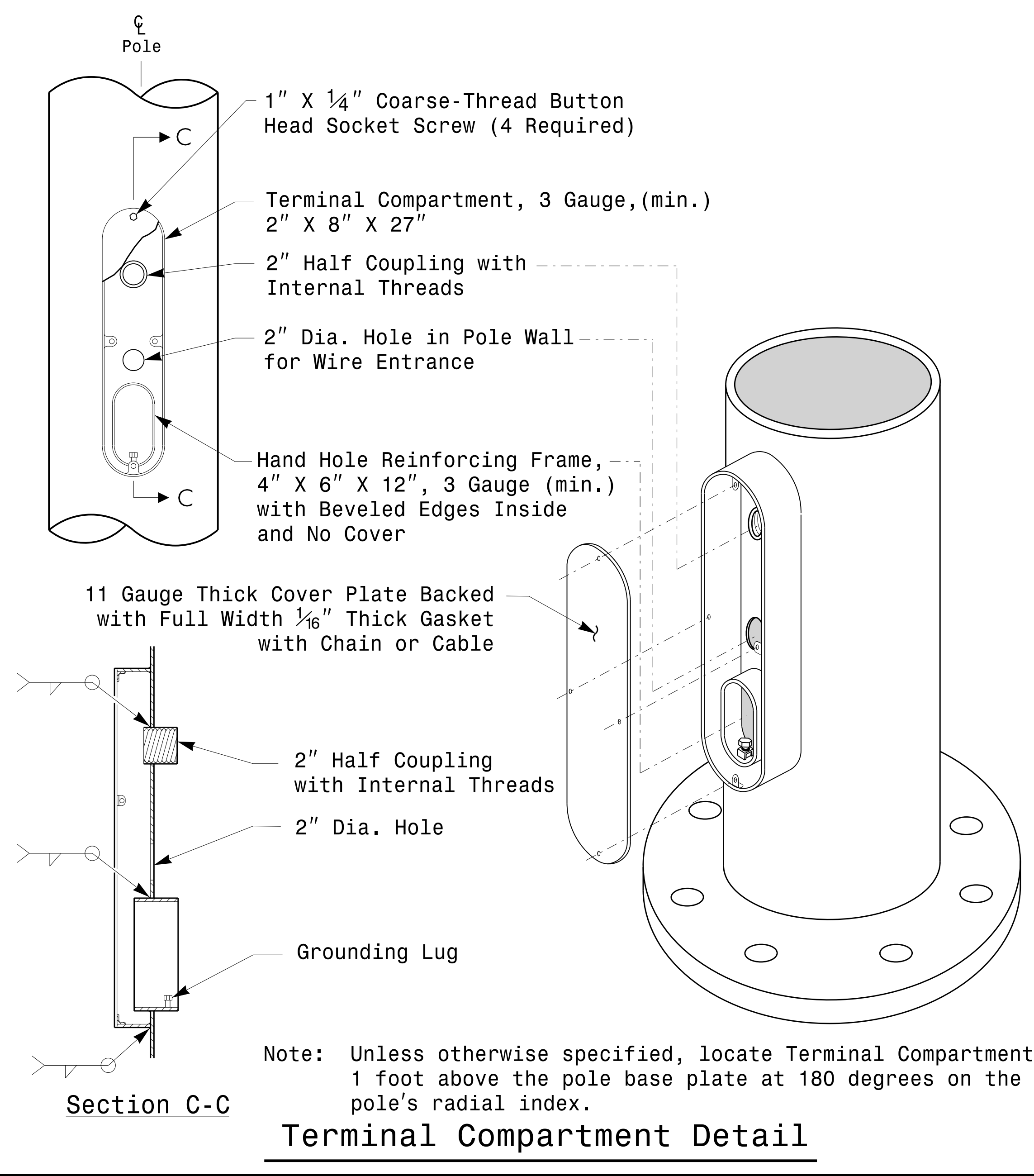
DRAWING NUMBER	DESCRIPTION
Sig. M 1	Statewide Wind Zone Map
Sig. M 2	Typical Fabrication Details-All Metal Poles
Sig. M 3	Typical Fabrication Details-Strain Poles
Sig. M 4	Typical Fabrication Details-Mast Arm Poles
Sig. M 5	Typical Fabrication Details-Mast Arm Connection
Sig. M 6	Typical Fabrication Details-Strain Pole Attachments
Sig. M 7	Construction Details-Foundations
Sig. M 8	Standard Strain Pole Foundation-All Soil Conditions

NC DOT CONTACTS:
MOBILITY AND SAFETY DIVISION - ITS AND SIGNALS UNIT

M.M. MCDIARMID, P.E. - STATE ITS AND SIGNALS ENGINEER
J. P. GALLOWAY, P.E. - STATE SIGNALS ENGINEER
D.C. SARKAR, P.E. - ITS AND SIGNALS SENIOR STRUCTURAL ENGINEER

SEAL

DocuSigned by:
Debesh C. Sarkar
DATE: 10/11/2017

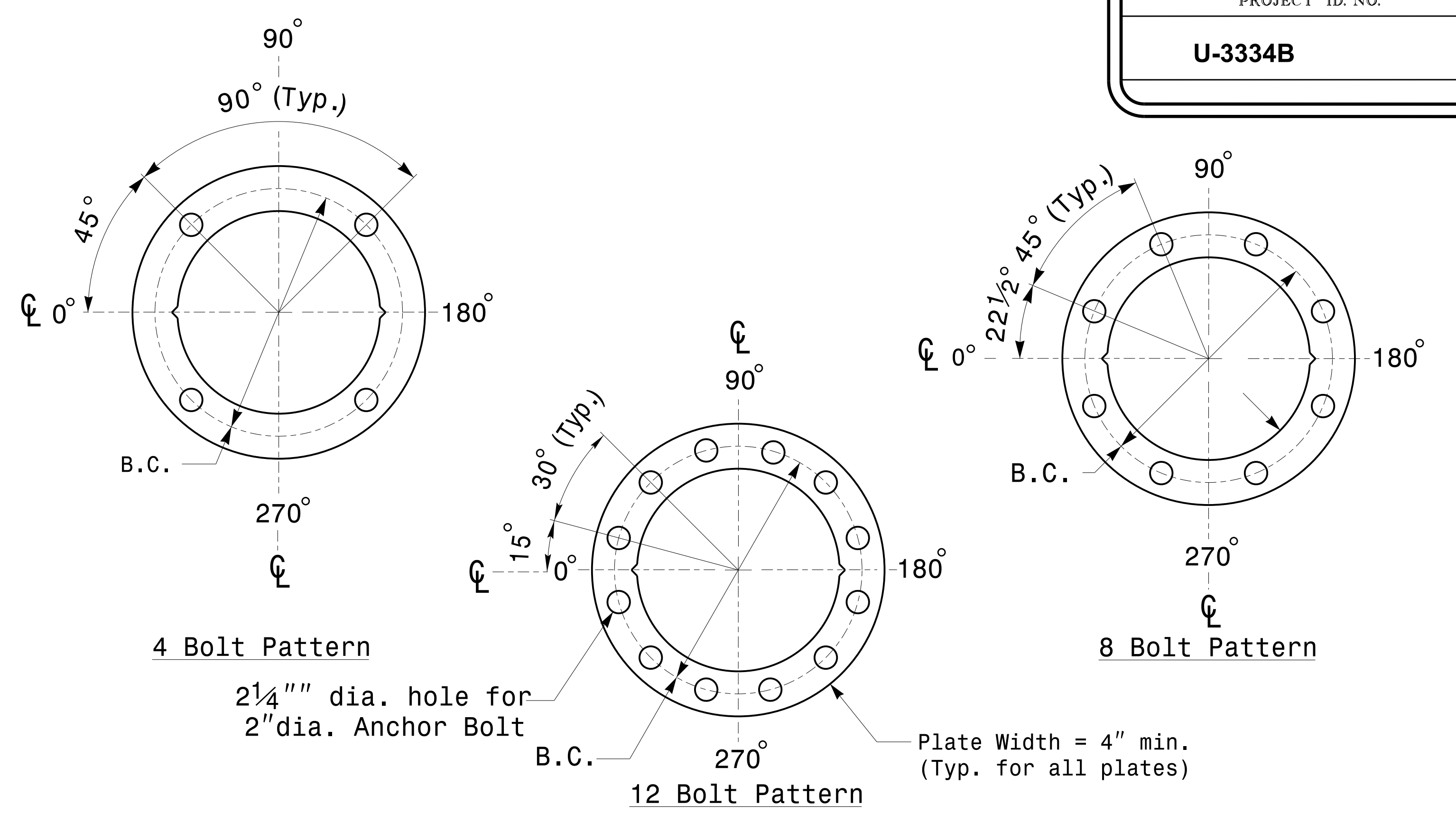


MFG _____	MFG. DATE: MM/YY _____
SHAFT D/T/L/Y _____	_____
ARM-A D/T/L/Y _____	_____
ARM-B D/T/L/Y _____	_____
A.B. DIA./B.C./L/Y _____	_____
NCDOT SIG. INV. NO. _____	_____
NCDOT POLE NO. _____	_____

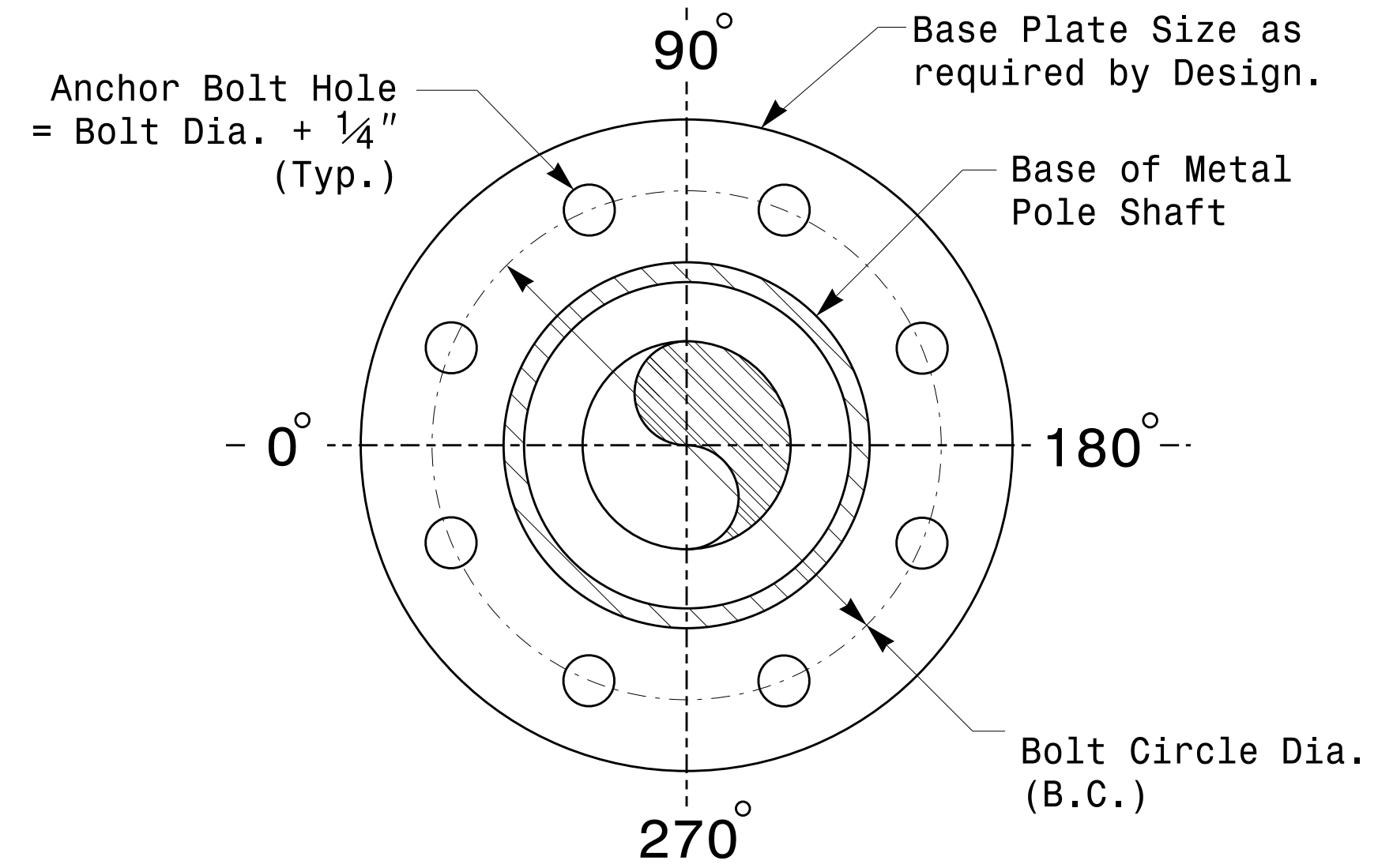
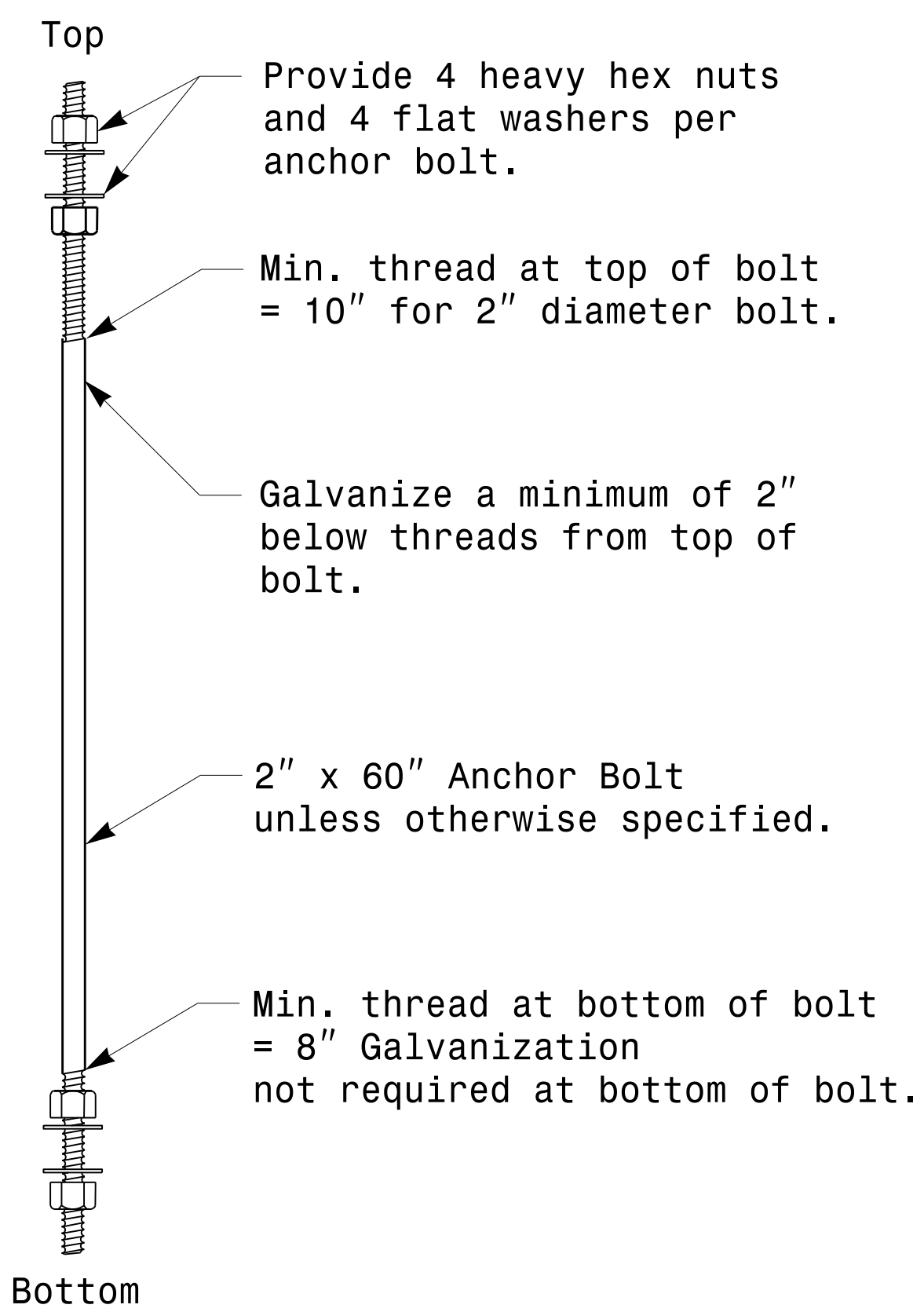
Shaft I.D. Tag
(Provide on Shaft of Strain Poles and Mast Arm Poles Shaft)

- Notes:**
- 1) D= Diameter, T= Thickness, L= Length, Y= Yield Strength
 - 2) A.B. = Anchor Bolt
 - 3) B.C. = Bolt Circle of Anchor Bolts
 - 4) If Custom Design, use "NCDOT STANDARD" line for Signal Inv. Number and pole I.D. number
 - 5) See drawing M3 and M4 for mounting positions of I.D. tags.

Identification Tag Details



Construct Templates and Plates from 1/4" min. thick Steel. Galvanizing is not required.



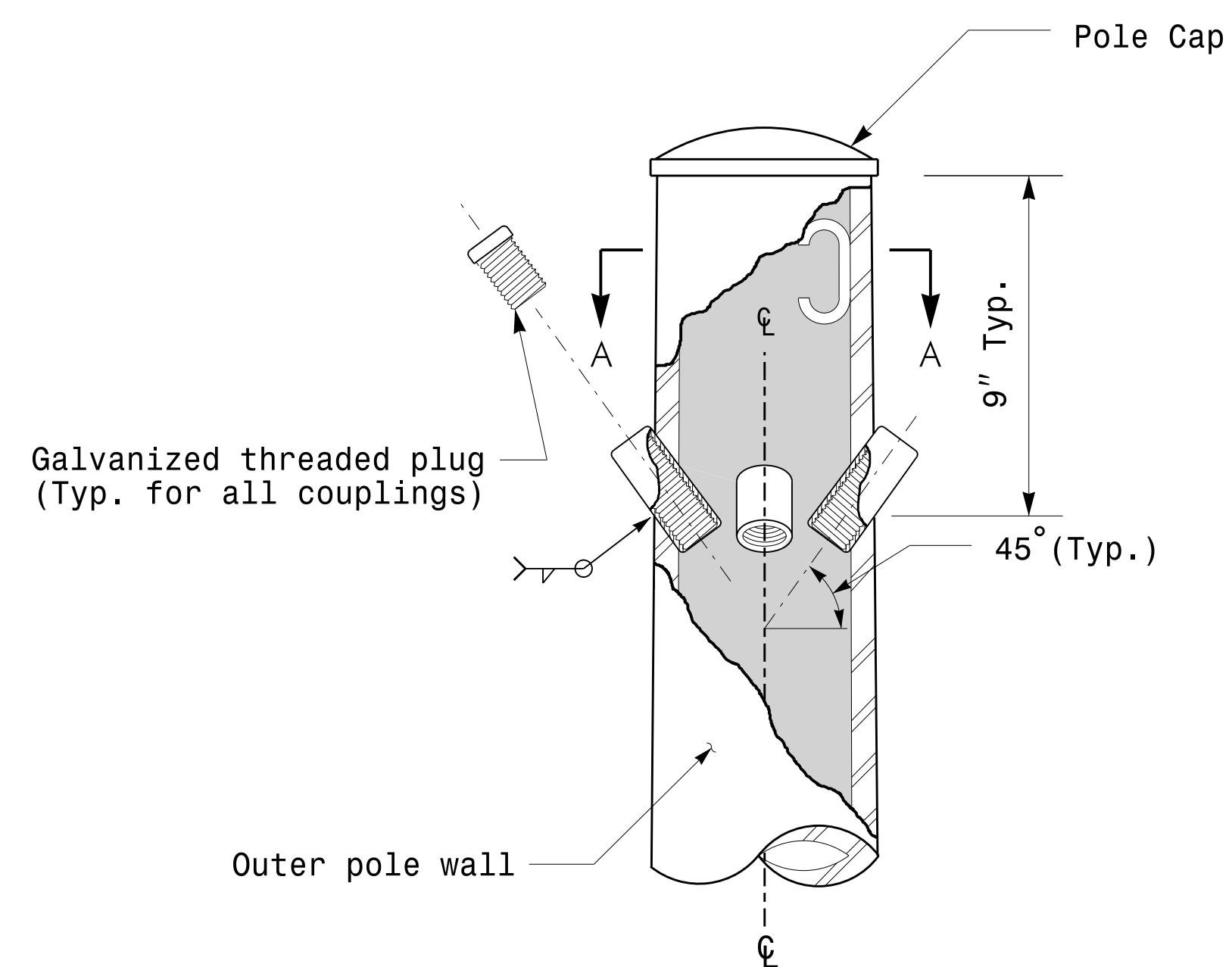
Note: Base plate may be circular, octagonal, square or rectangular in shape.

Typical Base Plate Detail

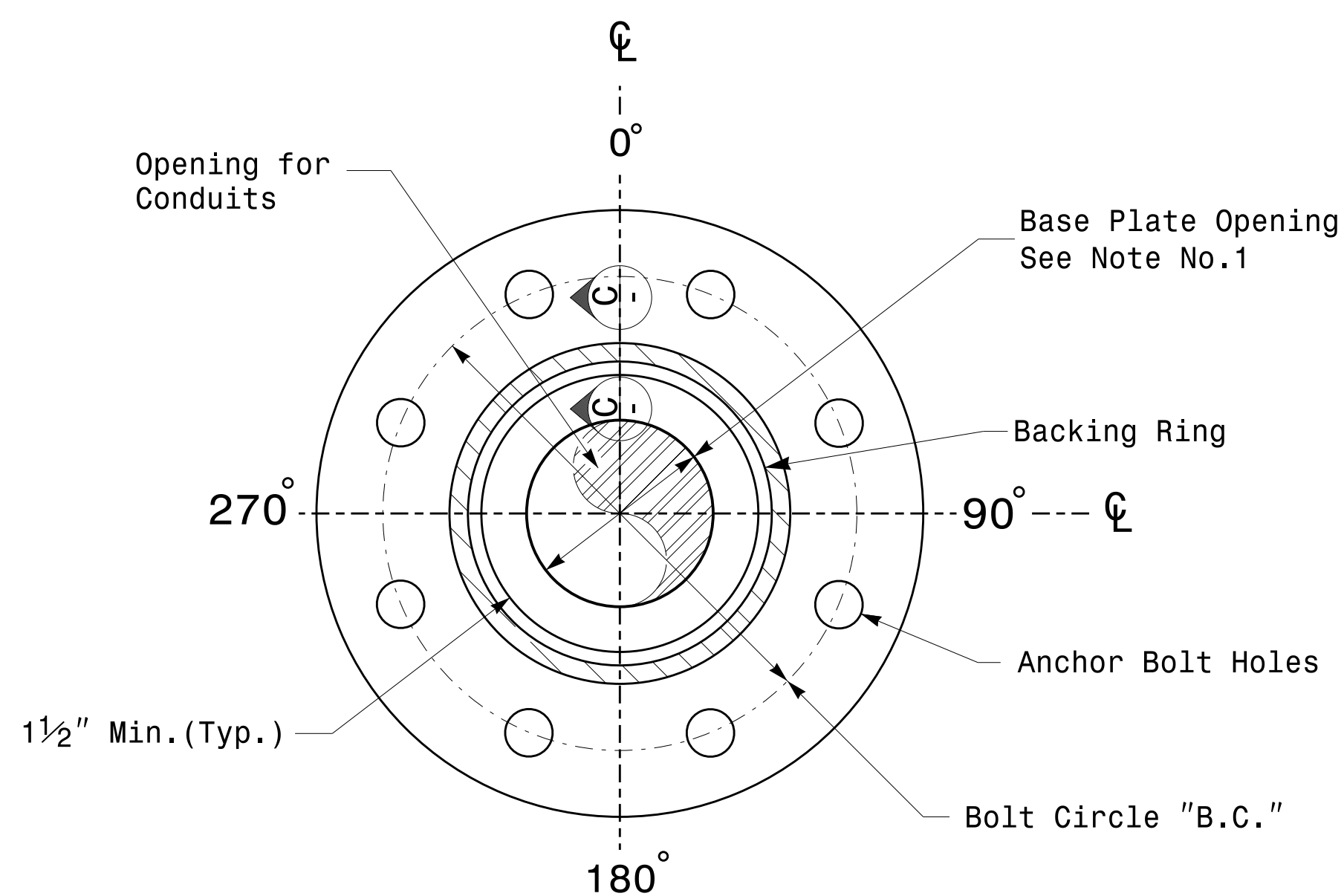
	Typical Fabrication Details For All Metal Poles		
	PLAN DATE: OCTOBER 2017 DESIGNED BY: C.F. ANDREWS	PREPARED BY: N. BITTING REVIEWED BY: D.C. SARKAR	
SCALE: 0 NA NONE	REVISIONS:	INITI:	DATE:
750 N. Greenfield Pkwy, Garner, NC 27529		DocuSign by: <i>Debash C. Sarkar</i>	10/11/2017 DATE

11-001-2017-08:30 136504115 Signal&Sign Design Section Eastern RegionM Sheets20162014 Sig.M2 Std. Fabrication Detail-All Poles.dgn

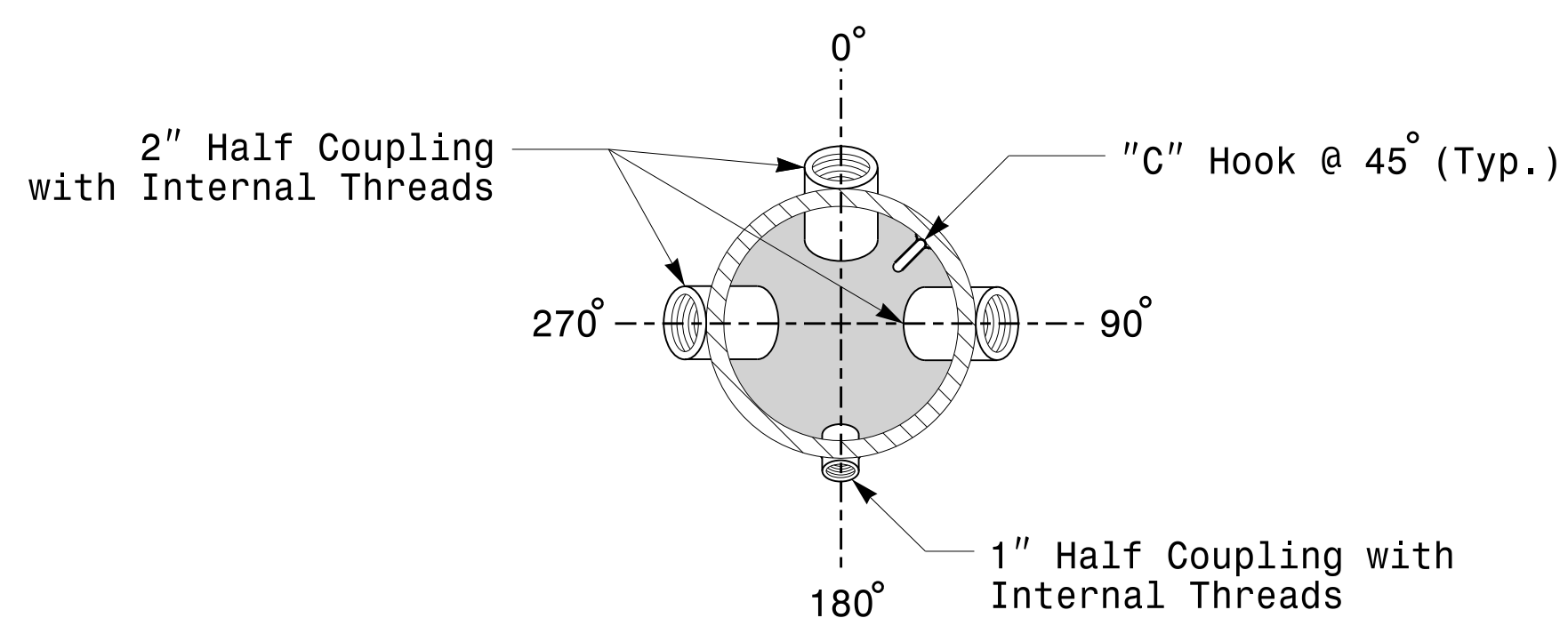
Note:
 1. Opening in pole base plate shall be equal to pole base inside diameter minus 3 1/2" but shall not be less than 8 1/2".



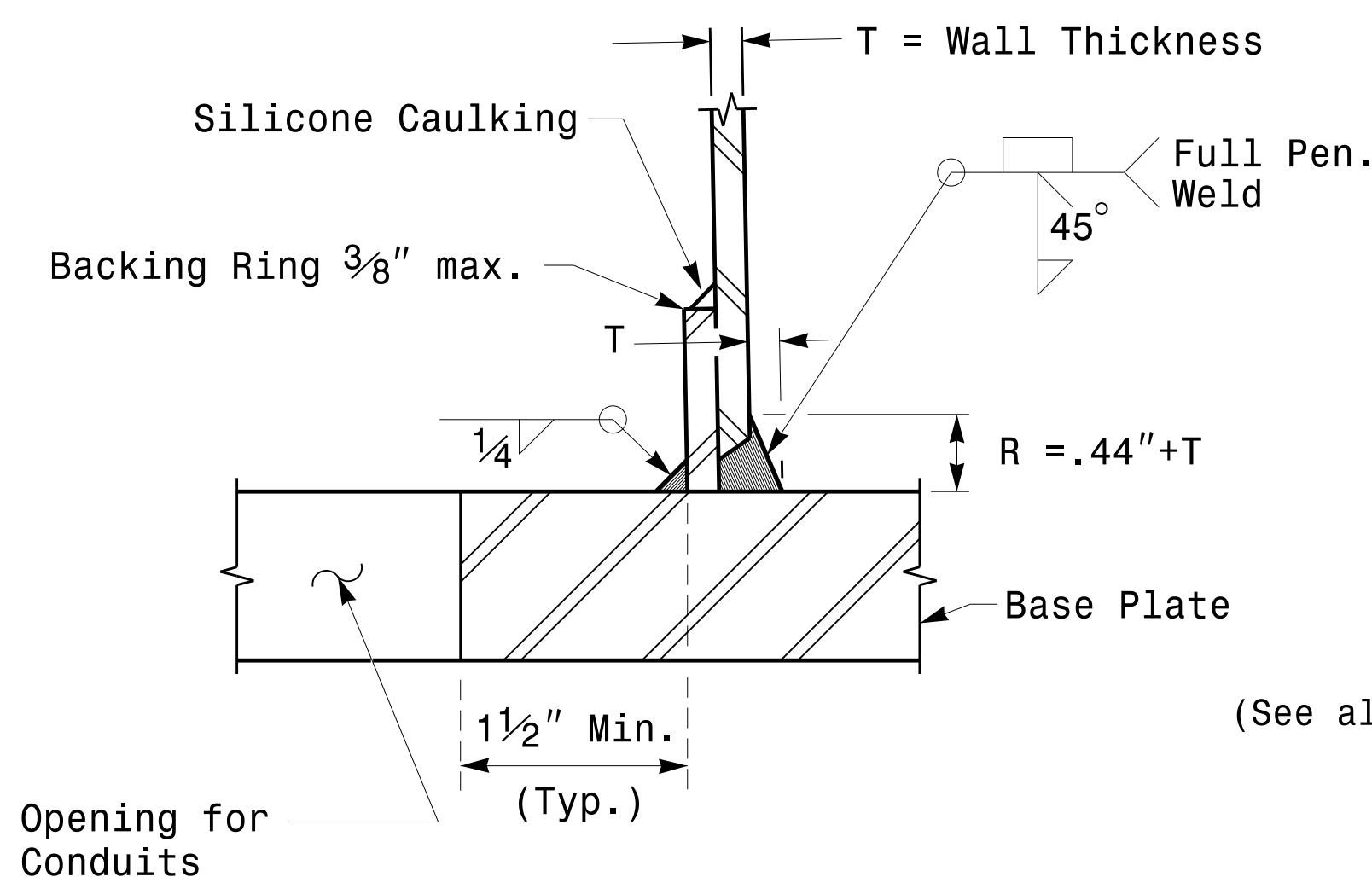
Cable Entrances at Top of Pole



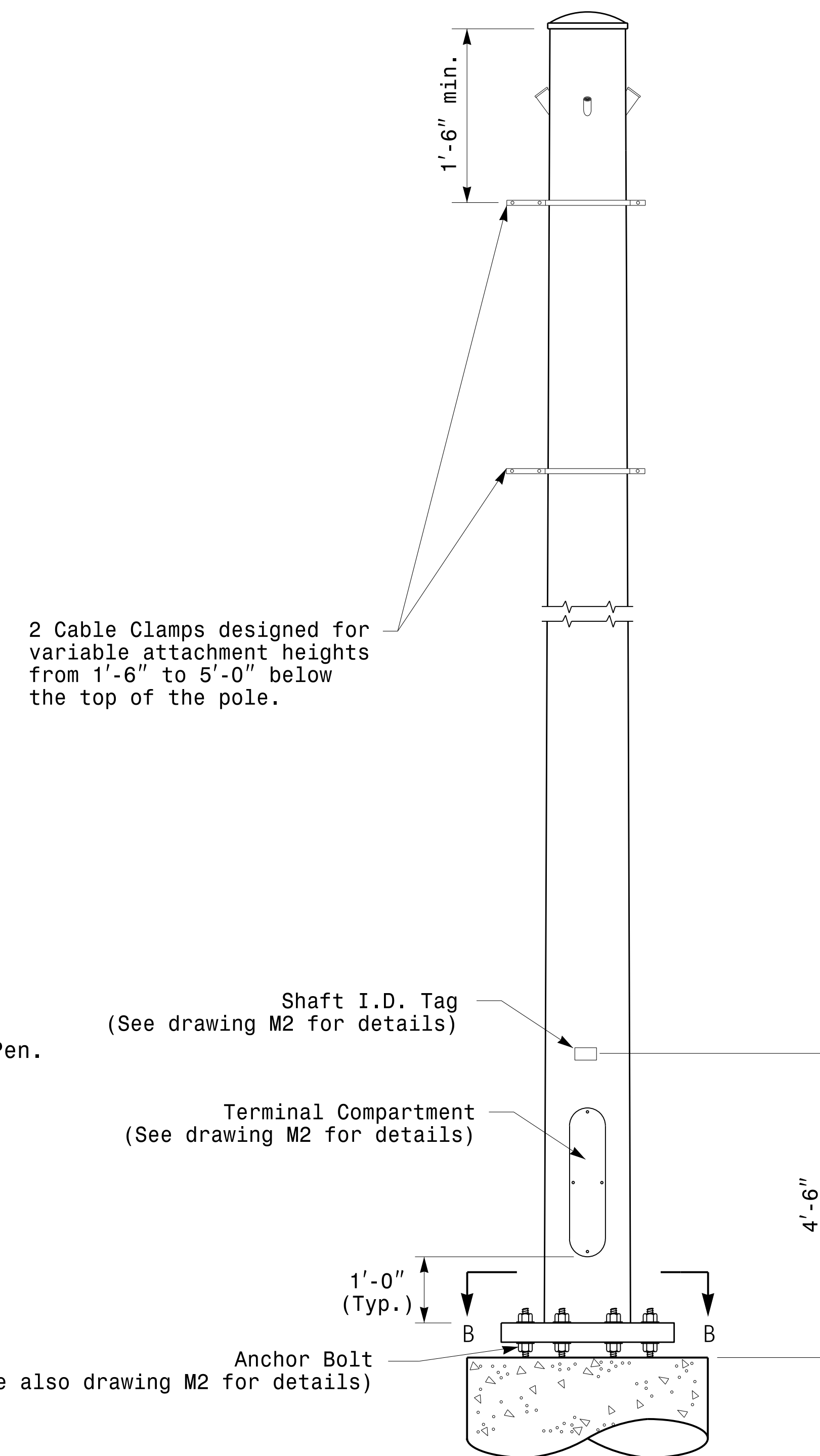
Section B-B Pole Base Plate Details (8 and 12 Bolt Pattern)



Section A-A Radial Orientation for Factory Installed Accessories at Top of Pole



Section C-C Full-Penetration Groove Weld Detail (Pole Attachment to Base Plate)



Monotube Strain Pole

Prepared in the Offices of:

 750 N. Greenleaf Pkwy, Garner, NC 27529

SCALE: NONE

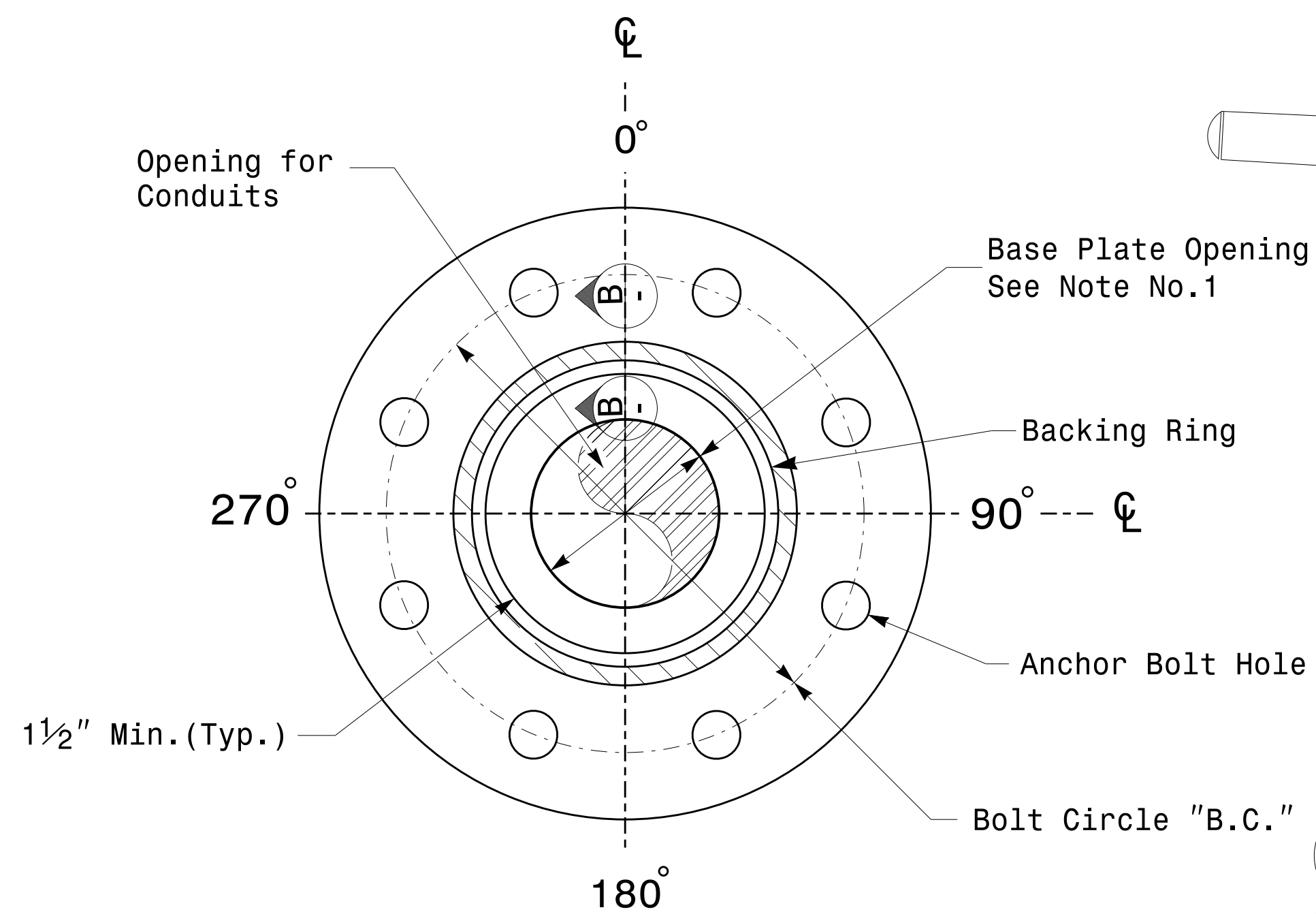
Typical Fabrication Details For Strain Poles			
PLAN DATE:	OCTOBER 2017	DESIGNED BY:	K.C. DURIGON
PREPARED BY:	N. BITTING	REVIEWED BY:	D.C. SARKAR
REVISIONS	INIT.	DATE	

SEAL

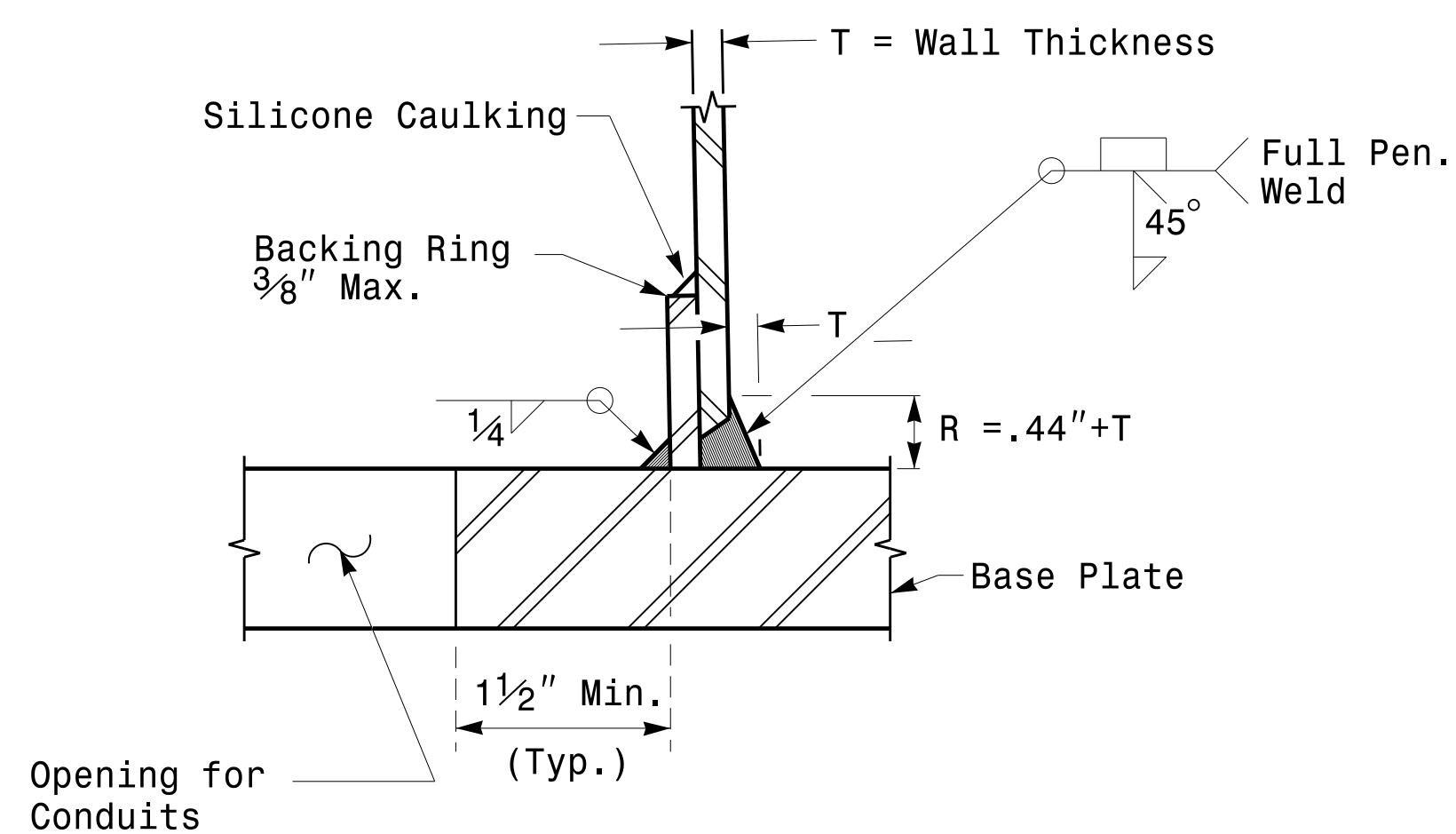
 DocuSigned by: D.C. Sarkar
 44E8E7816FA4F49E
 10/11/2017
 DATE

Fabrication Details - Strain Poles

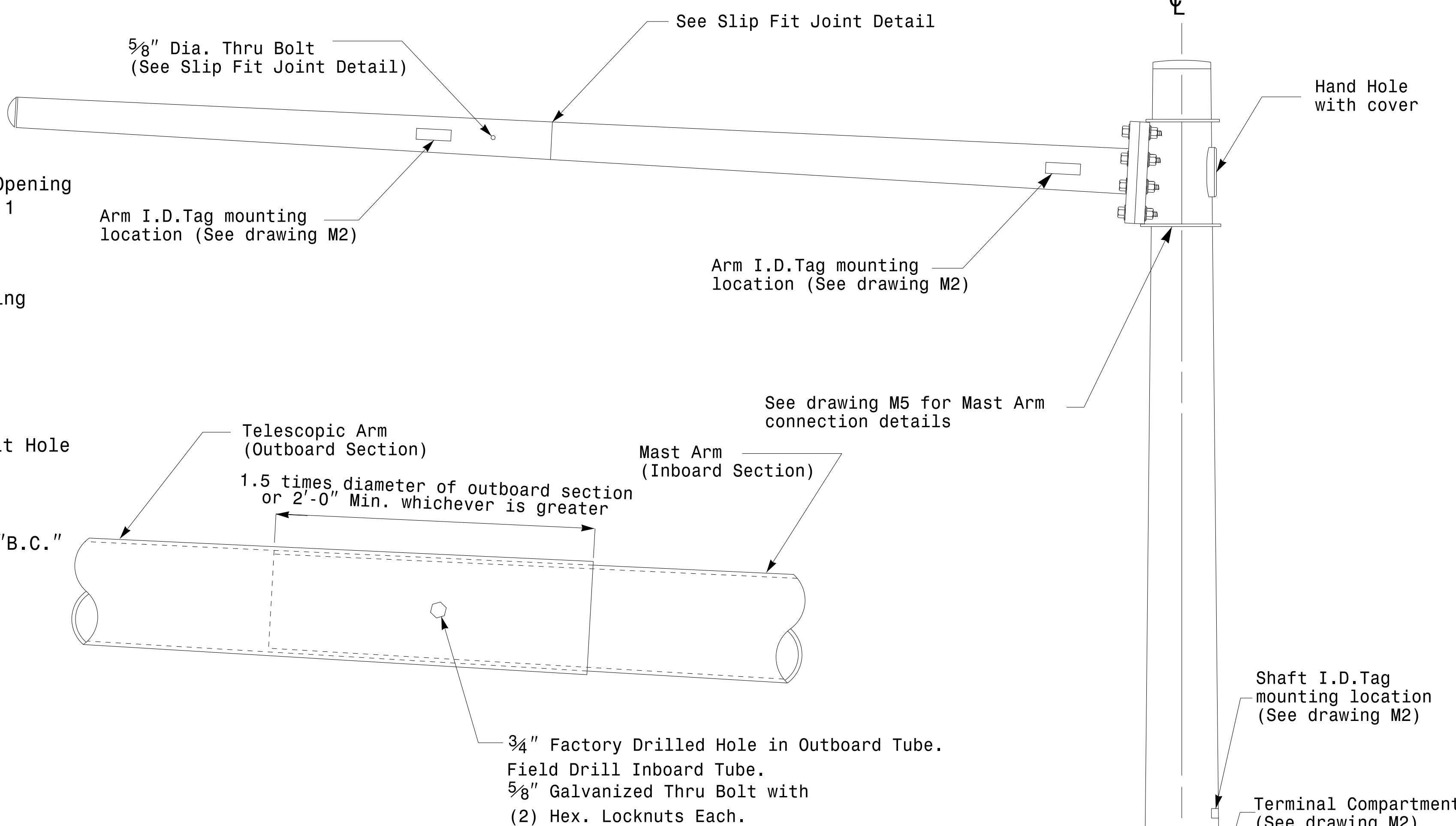
Note:
 1. Opening in pole base plate shall be equal to pole base inside diameter minus 3 1/2" but shall not be less than 8 1/2".



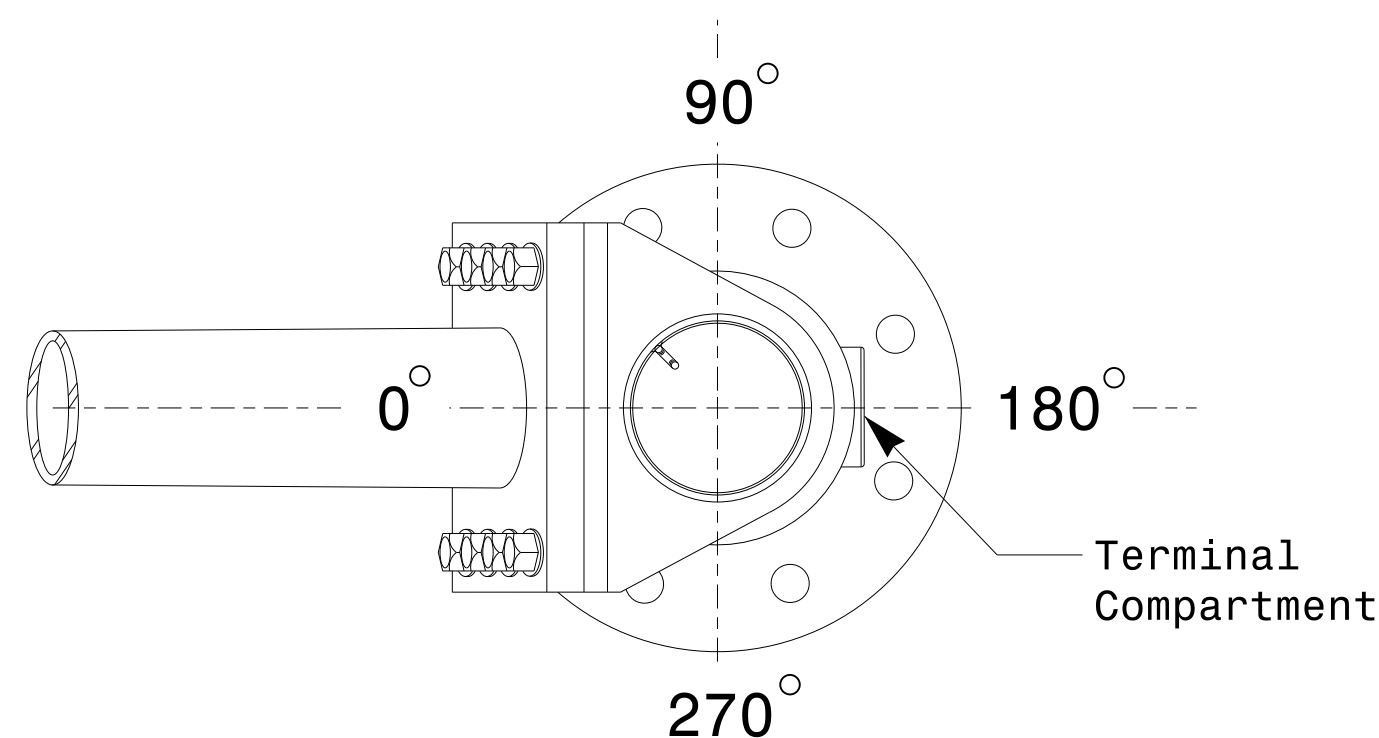
Section A-A
 Pole Base Plate Details



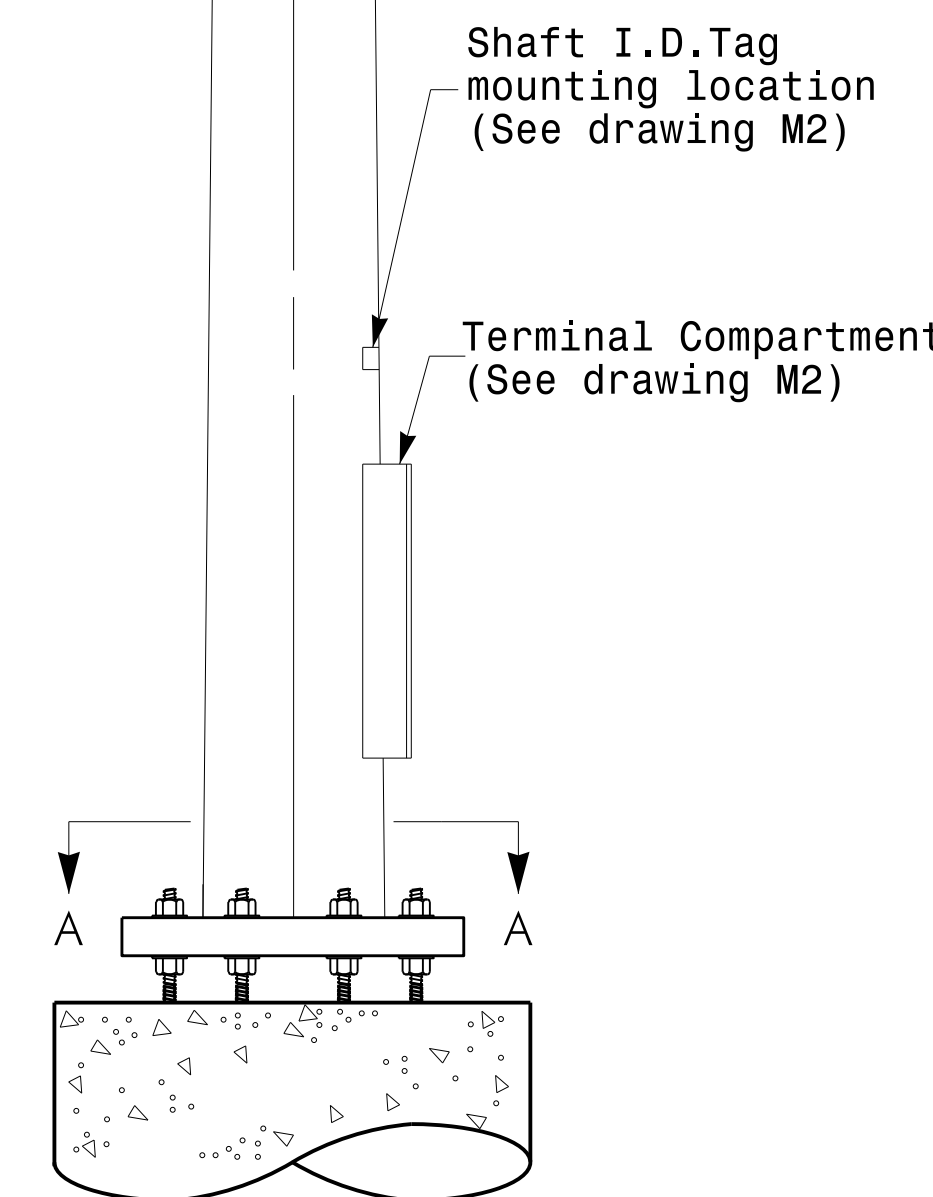
Section B-B
 (Pole Attachment to Base Plate)
 Full-Penetration Groove Weld Detail



Slip Fit Joint Detail for Mast Arm



Mast Arm Radial Orientation



Mast Arm Pole

Fabrication Details - Mast Arm Poles

	<p>Typical Fabrication Details For Mast Arm Poles</p>		
	<p>PLAN DATE: OCTOBER 2017</p>	<p>DESIGNED BY: K.C. DURIGON</p>	
<p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>PREPARED BY: N. BITTING</p>	<p>REVIEWED BY: D.C. SARKAR</p>	<p>DocuSigned by: Dinesh C. Sarkar</p>
<p>SCALE: 0 NA NONE</p>	<p>REVISIONS</p>	<p>INIT. DATE</p>	<p>10/11/2017</p>

11-0CT-2017 08:33 136560115 Signal&Sigr Design Section Eastern Region\m4 Sheets\2016\2014 Sig.M4 Std. Fabrication Detail-Mast Arm Poles.dgn

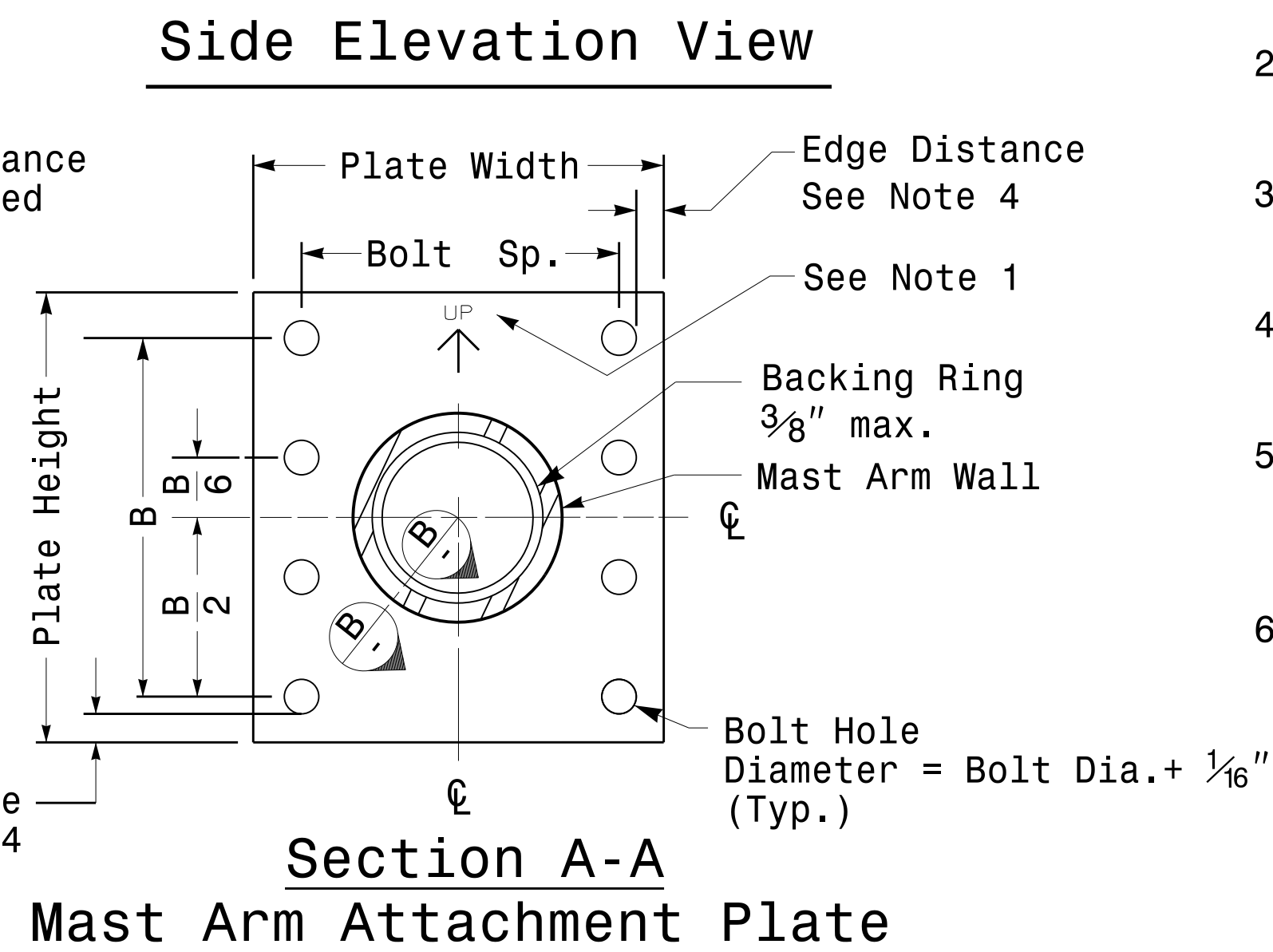
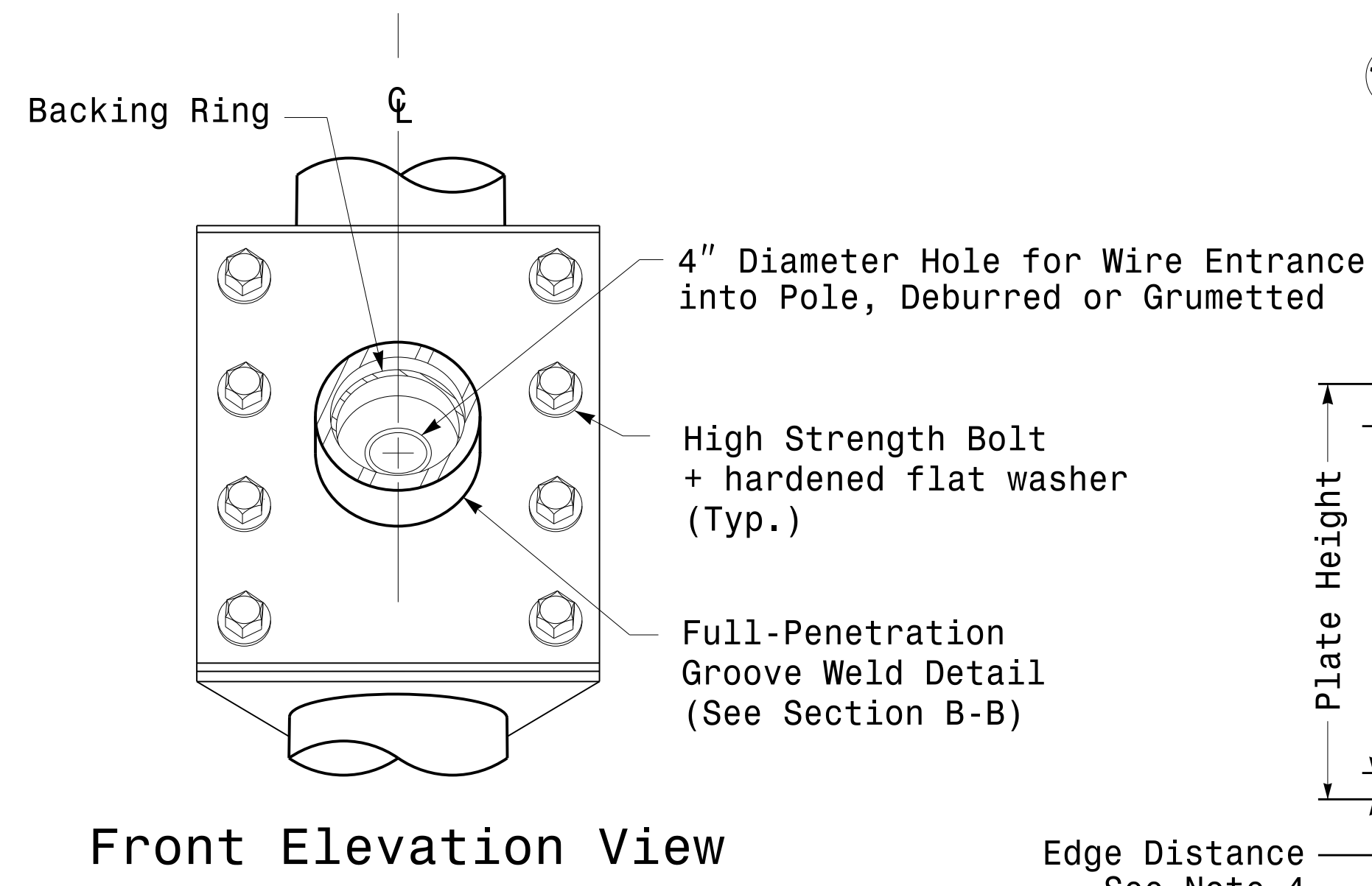
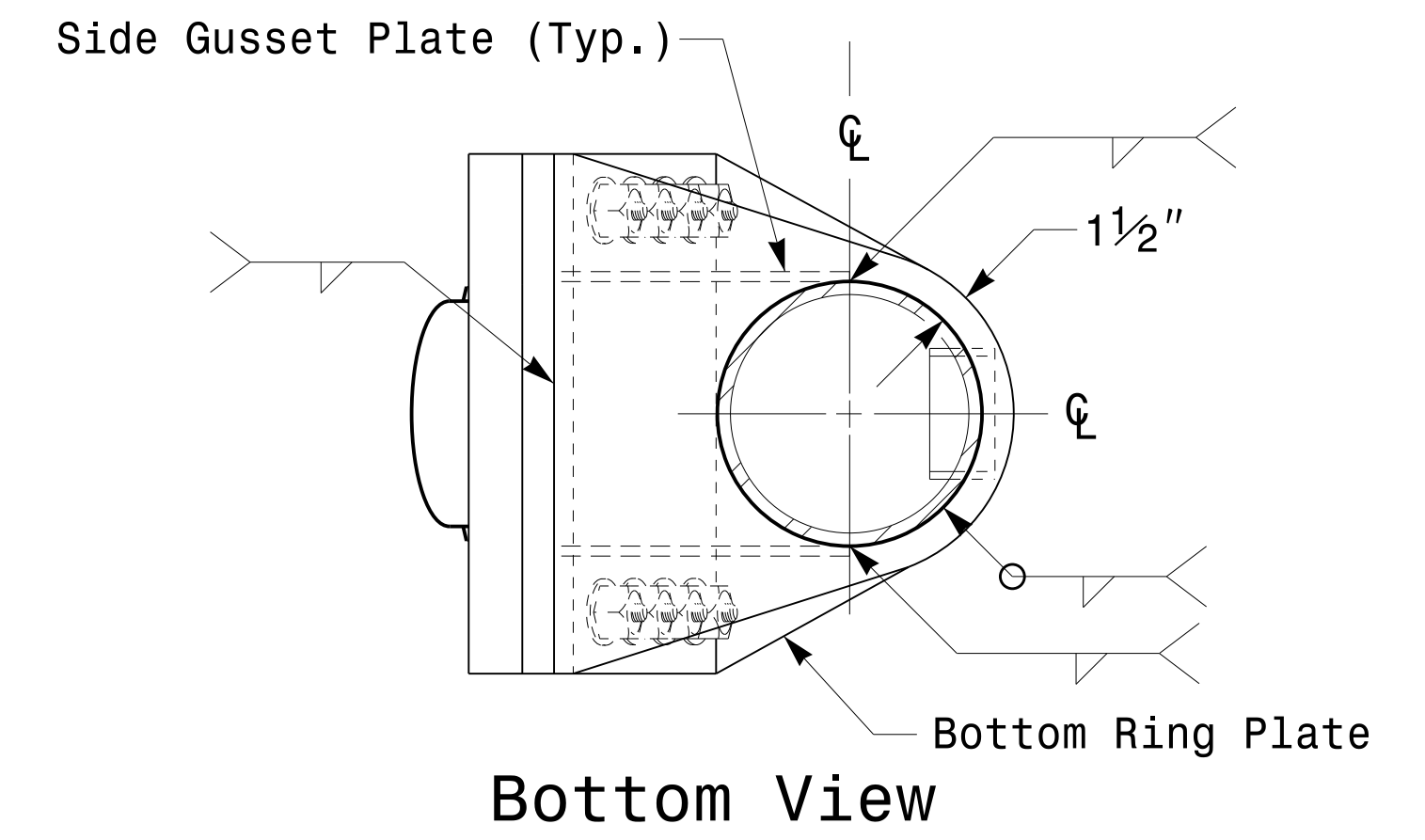
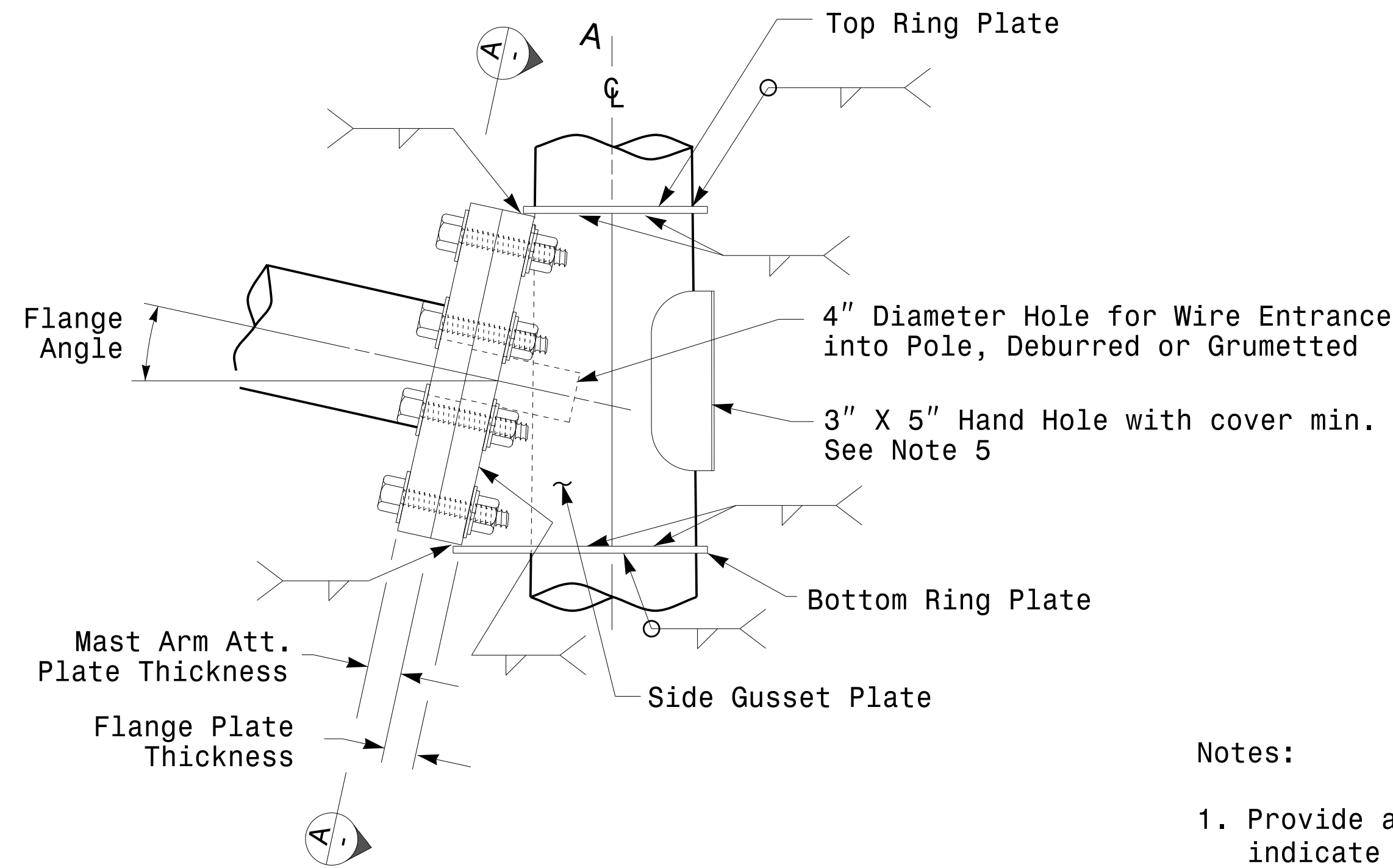
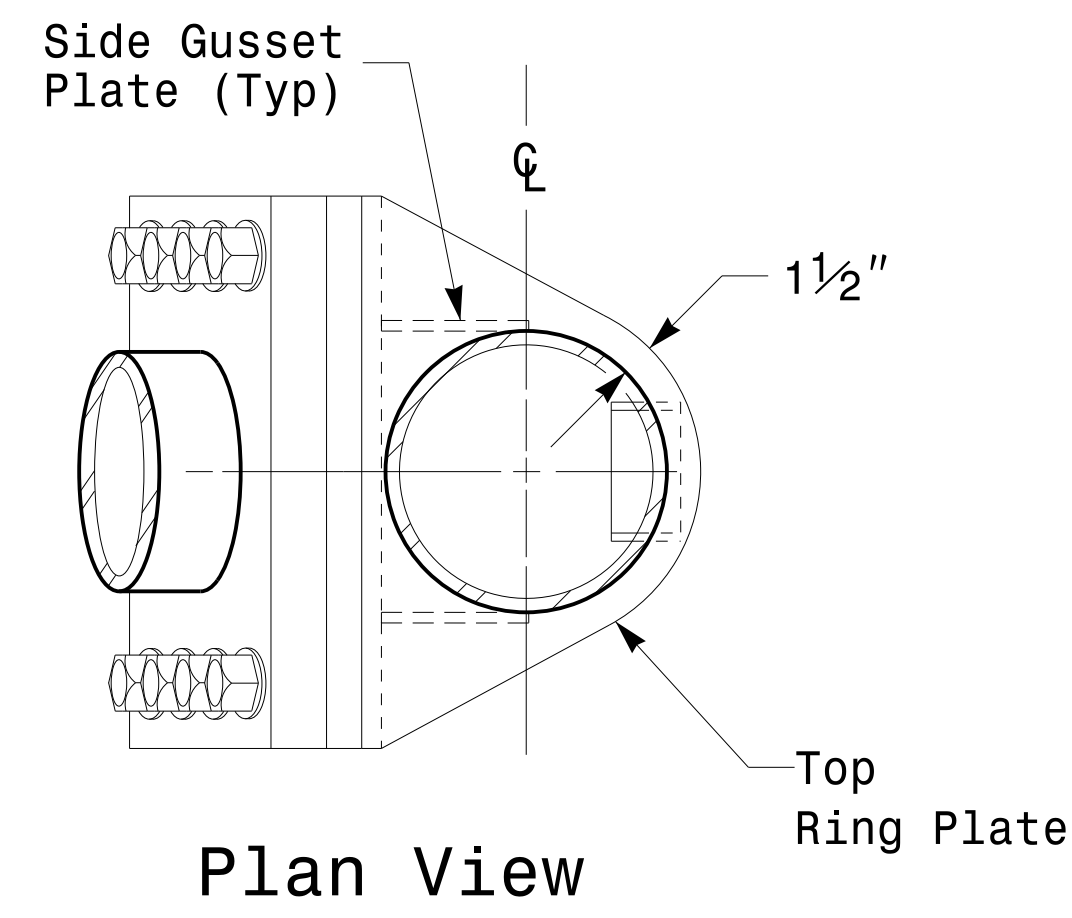
Welded Ring Stiffened Mast Arm Connection

PROJECT ID. NO.

SHEET NO.

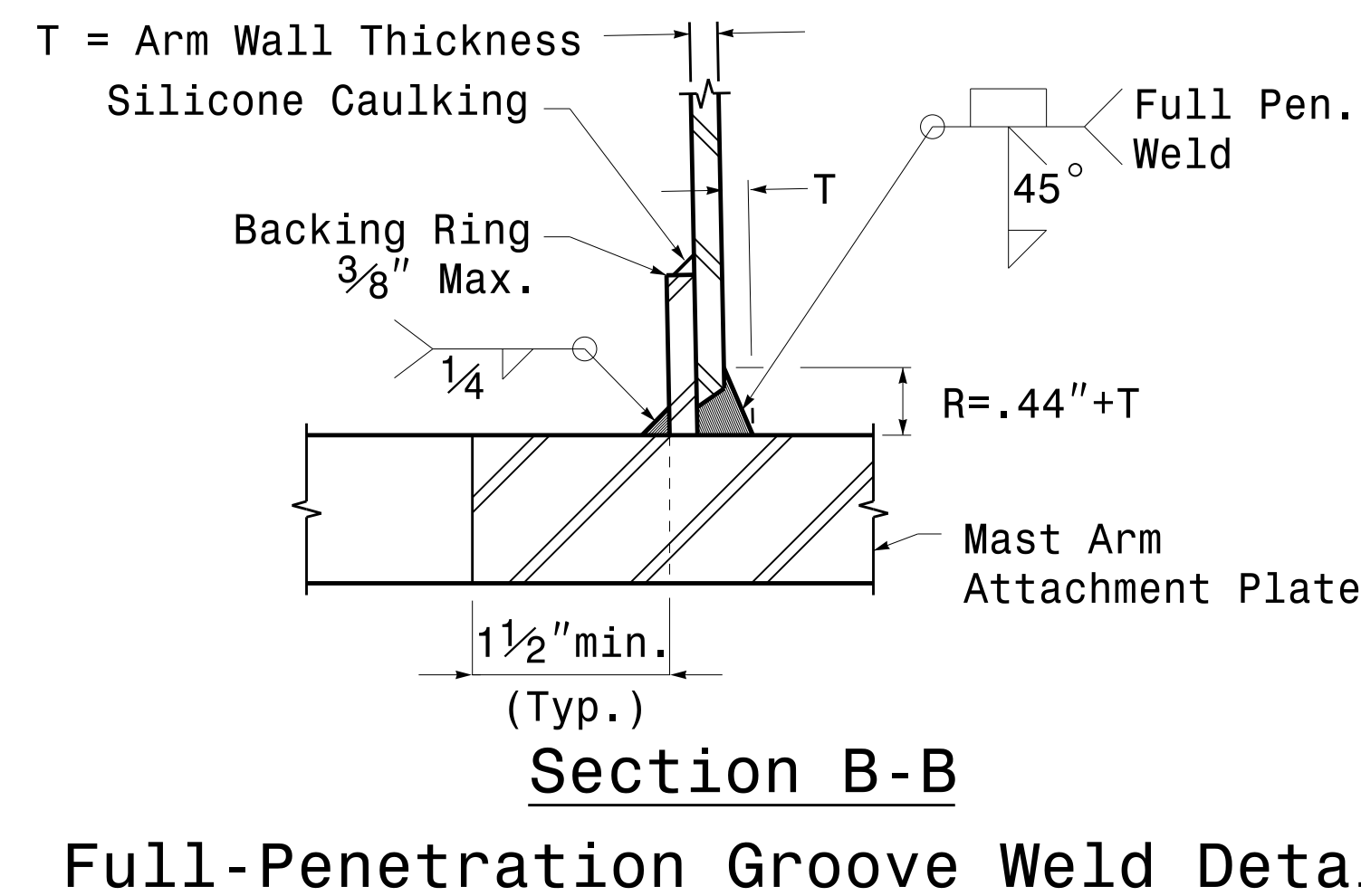
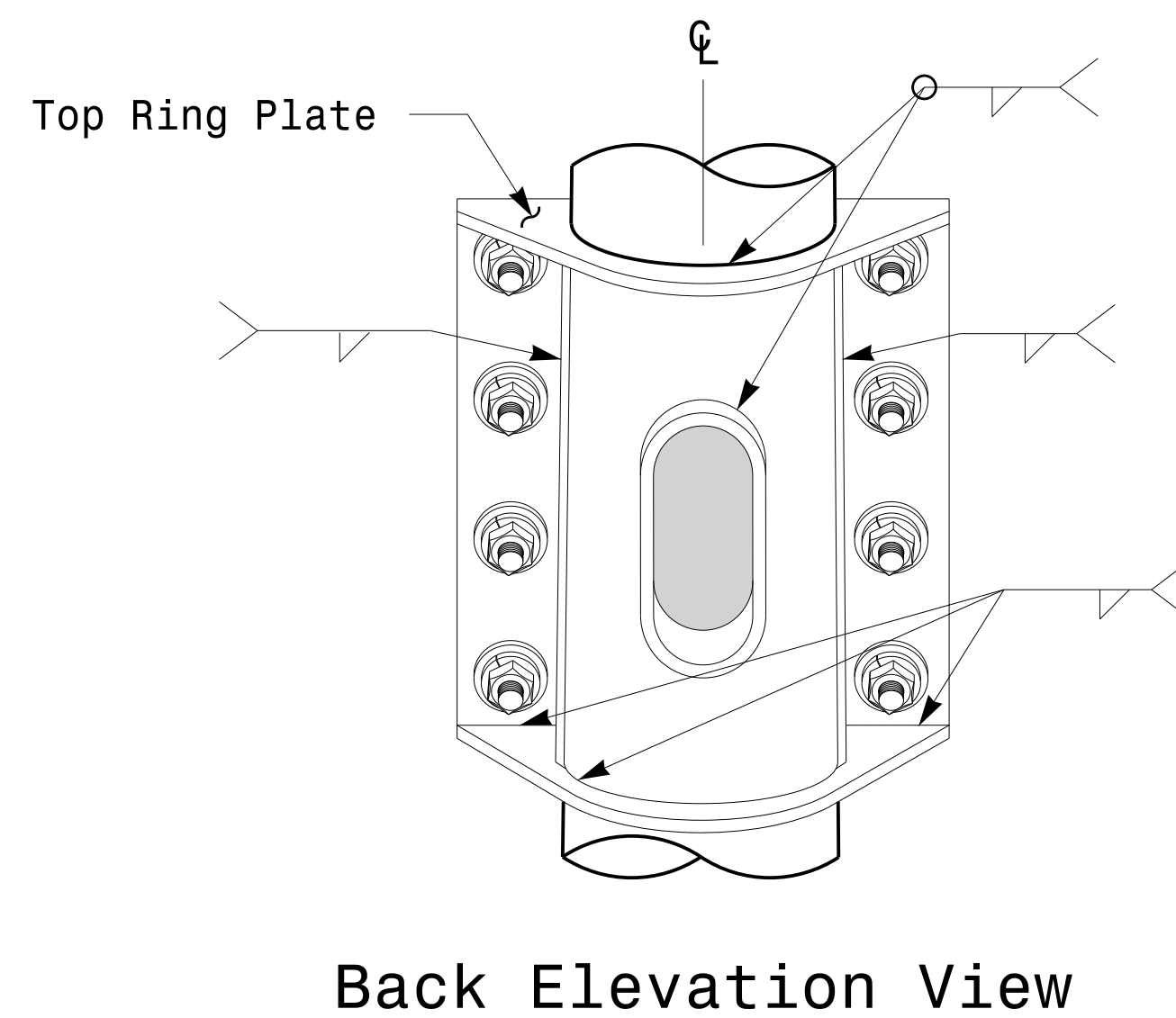
U-3334B

Sig.M5



Notes:

1. Provide a permanent means of identification above the mast arm to indicate proper attachment orientation of the mast arm.
2. Designer will determine the size of all structural components, plates, fasteners, and welds shown unless they are already specified.
3. Fabricator is responsible for providing appropriate holes at drainage points to drain galvanizing materials.
4. For minimum edge distance follow AISC Table J3.4 and J3.5. For nominal bolt hole size use Table J3.3.
5. Provide upper handhole as necessary when shaft extensions are required for luminaire arms or camera. For poles without luminaires/camera, wiring can be done through the top of pole.
6. Allowable range of flange tilt angle will vary from 0° to as required.



Prepared in the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

SCALE: 0 NA NONE

Typical Fabrication Details For Mast Arm Connection To Pole

PLAN DATE: OCTOBER 2017	DESIGNED BY: C.F. ANDREWS
PREPARED BY: N. BITTING	REVIEWED BY: D.C. SARKAR
REVISIONS	INIT. DATE

SEAL

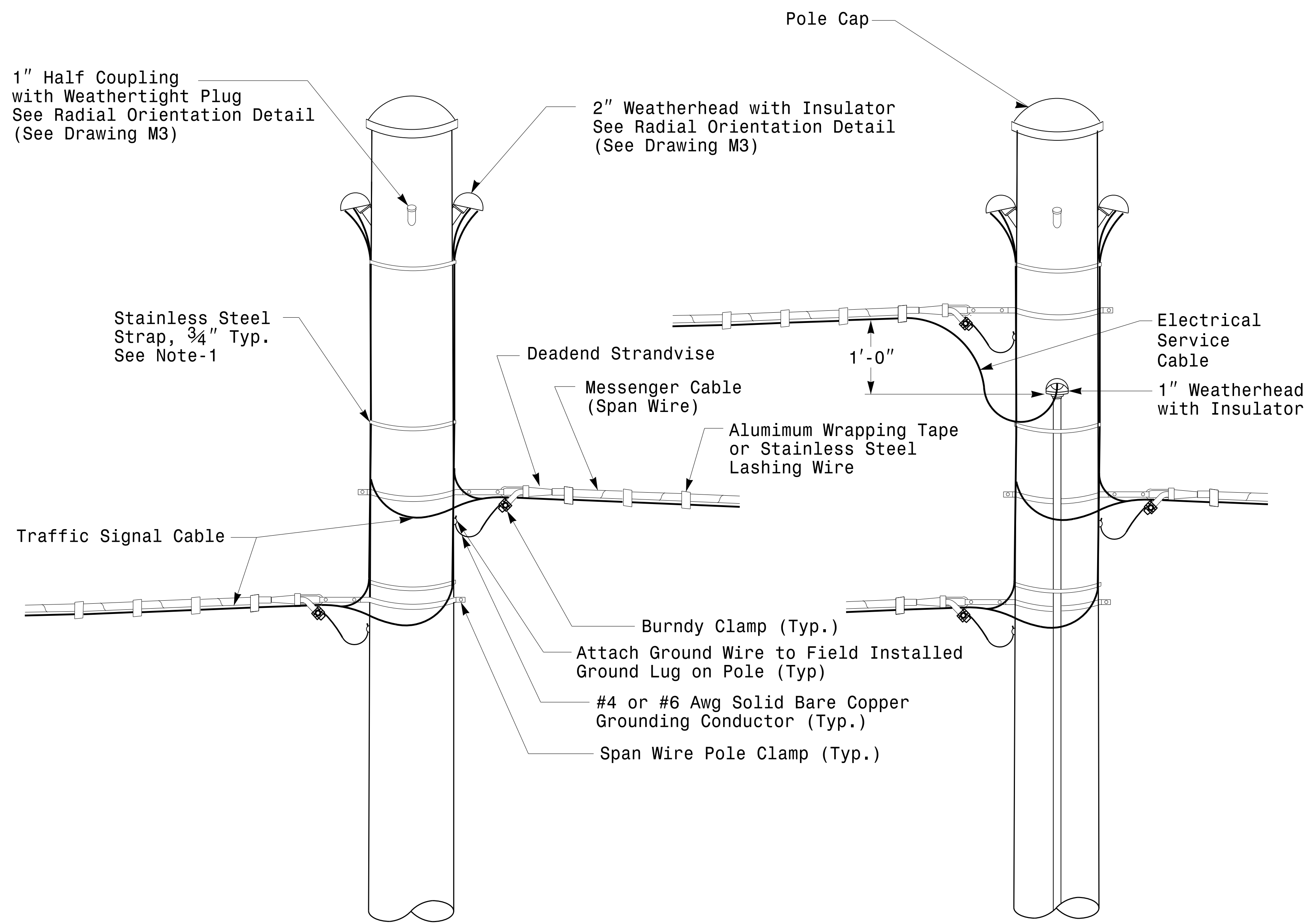
Debesh C. Sarkar

10/11/2017

DATE

Fabrication Details - Mast Arm Connection

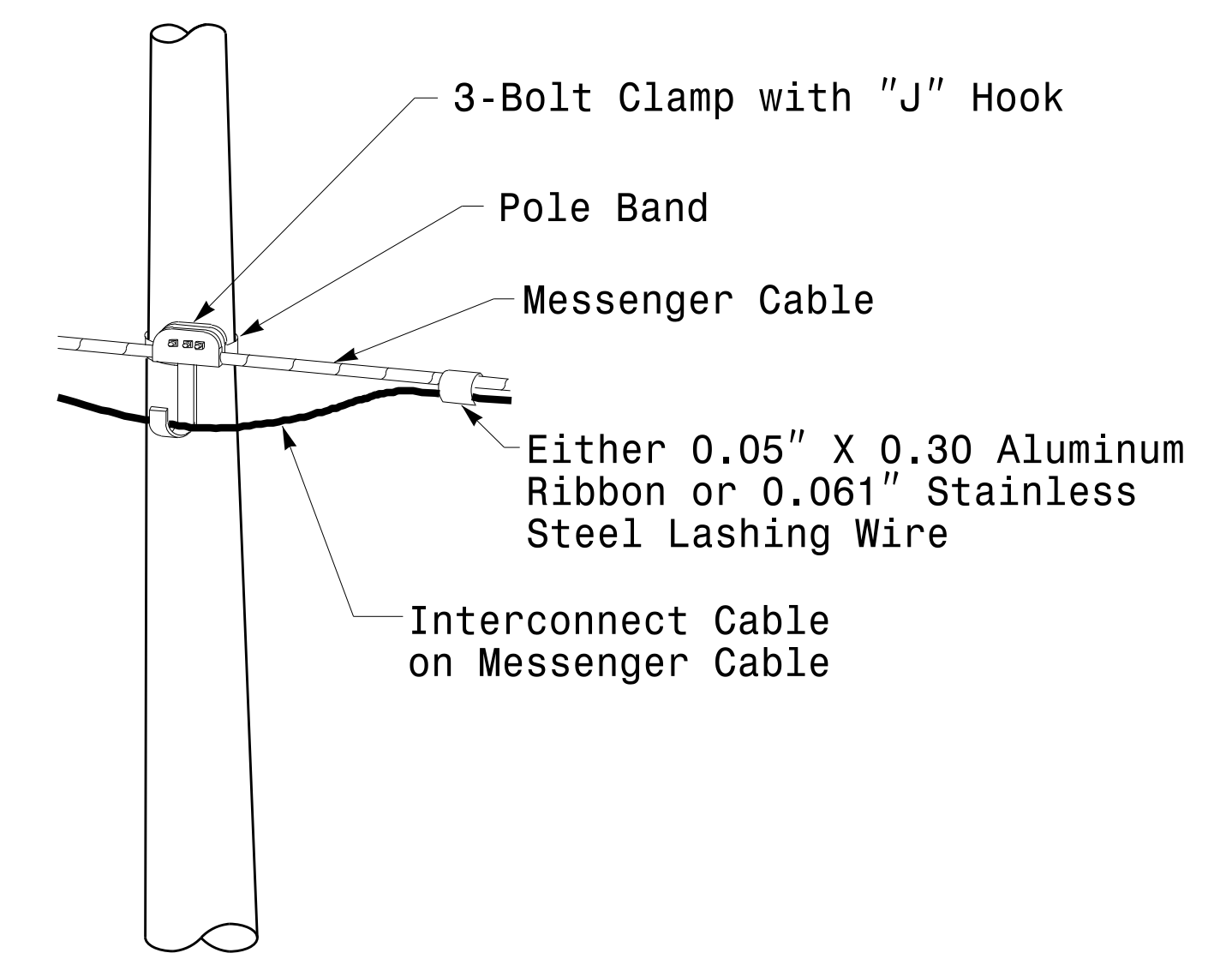
11-OCT-2017 08:35:13:560115:Stipal:Sig.M5:Design Section:Eastern Region:Sheet:2016:2014:Sig.M5:Std. Connection Fabrication Detail:is:Mast Arm Poles.dgn



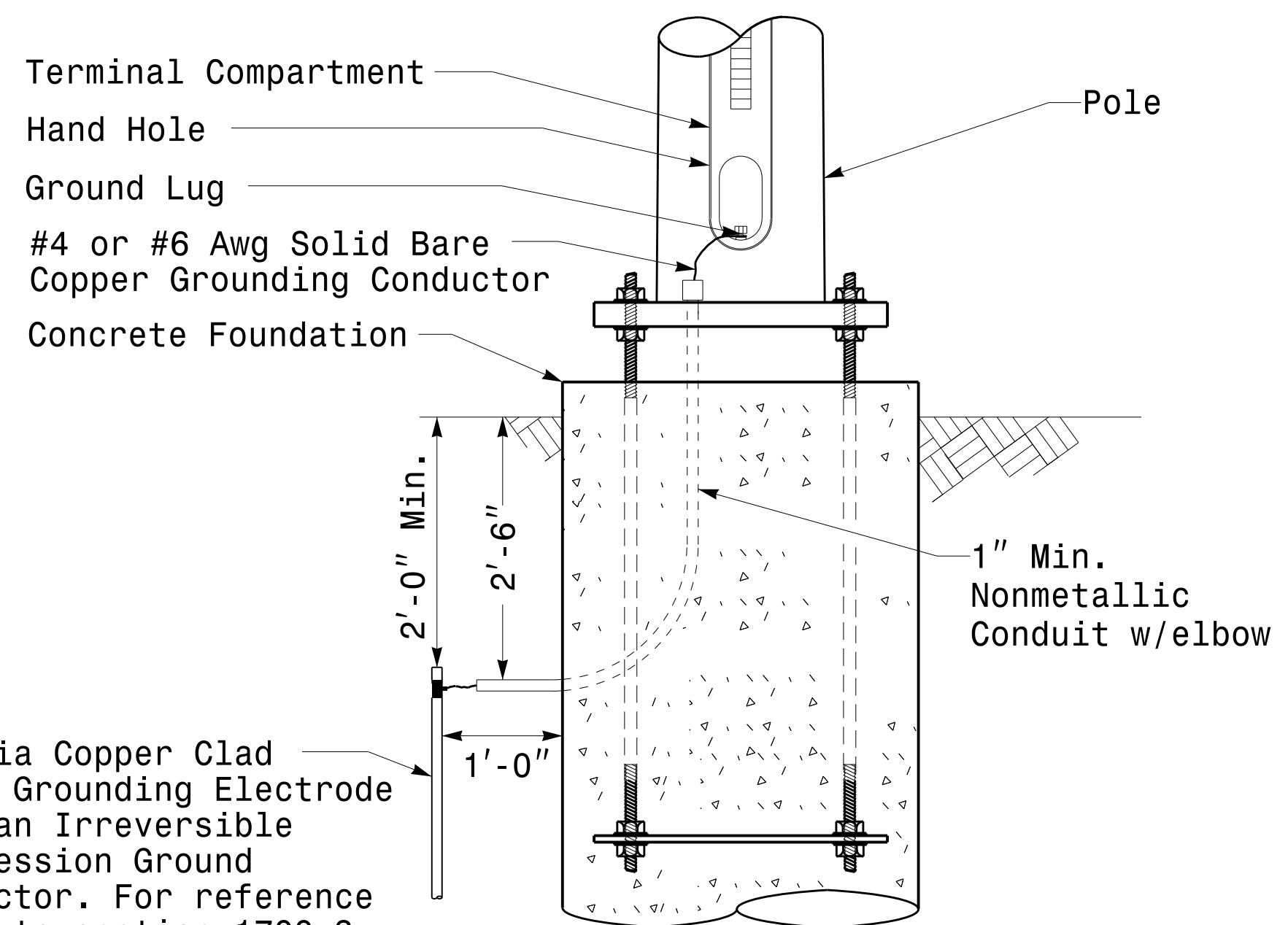
Strain Pole Attachments

NOTE:

1. Strap all signal cables to the side of the pole with 3/4" stainless steel straps when the distance between the spanwire attachment clamp and the weatherheads exceeds 3'-0".
2. Provide minimum two spanwire pole clamps per pole.
3. It is prohibited to attach two span wires at one pole clamp.
4. For general requirements refer to NCDOT Standard Specifications for Roadway and Structures, January 2018.



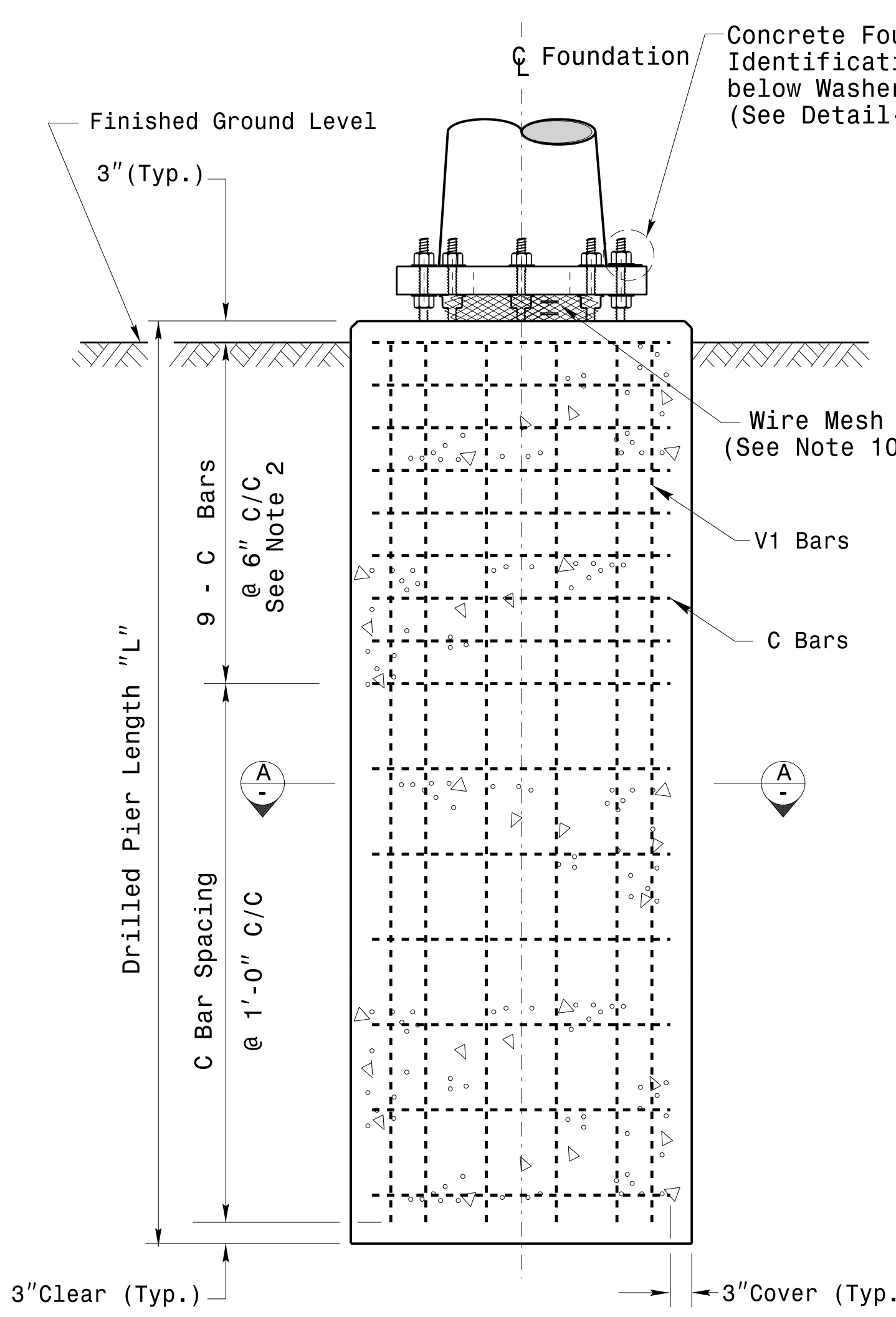
Attachment of Cable to Intermediate Metal Pole



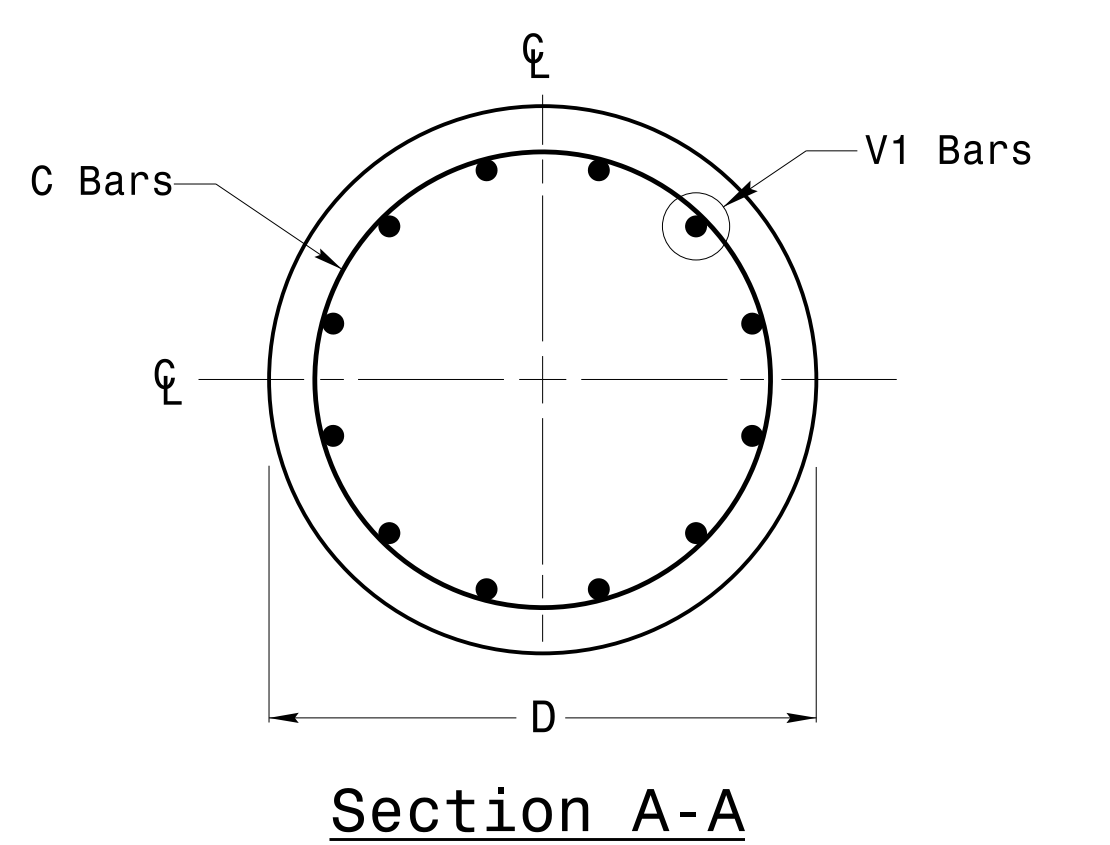
Metal Pole Grounding Detail For Strain Pole and Mast Arm

11-DEC-2017 08:36 136504115 StrainPole.dgn Design Section Eastern Region\m\ Sheets\2016\2014 Sig.M6 Std. Fabrication Detail-Strain Poles.dgn

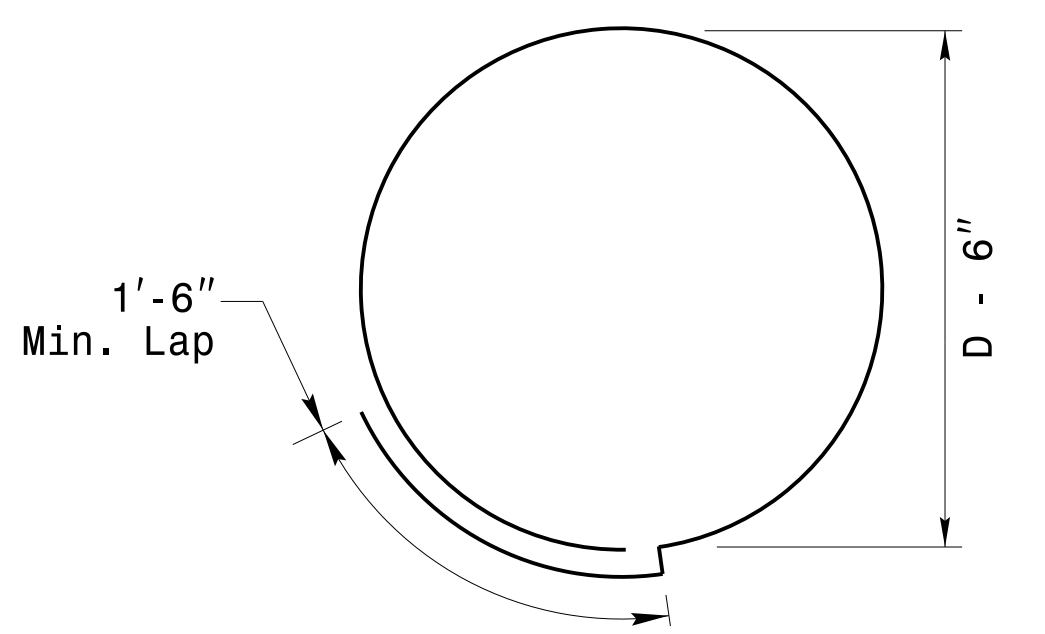
	<p>Typical Fabrication Details For Strain Pole Attachments</p>		
	<p>PLAN DATE: OCTOBER 2017</p>	<p>DESIGNED BY: C.F. ANDREWS</p>	
<p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>SCALE: 0 NA NONE</p>	<p>REVISIONS</p>	<p>INIT. DATE</p>
<p>DocuSigned by: <i>Deshi C. Sarkar</i></p>		<p>10/11/2017 DATE</p>	



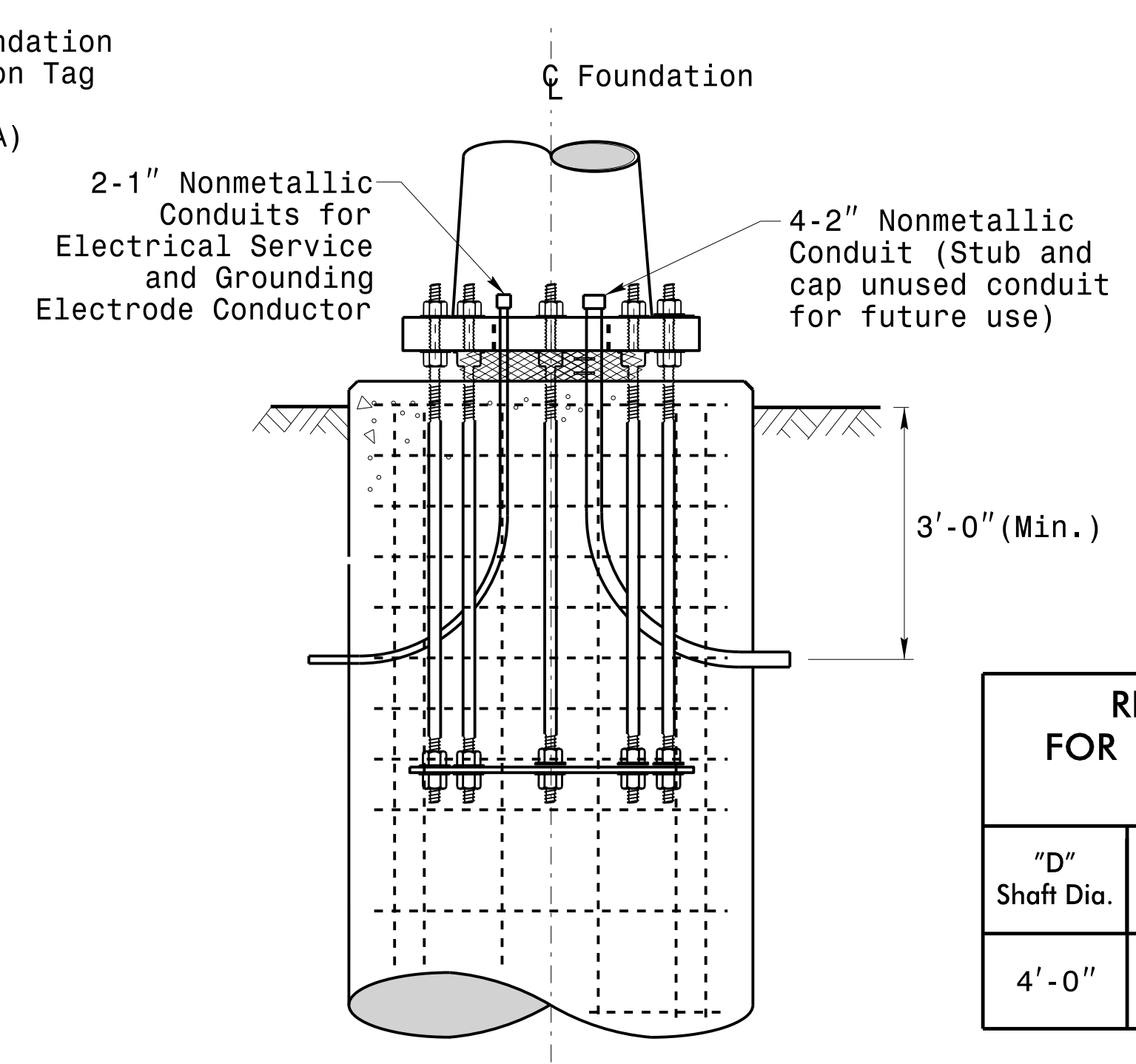
Concrete Shaft Elevation



Section A-A



Typical "C" Bar Detail



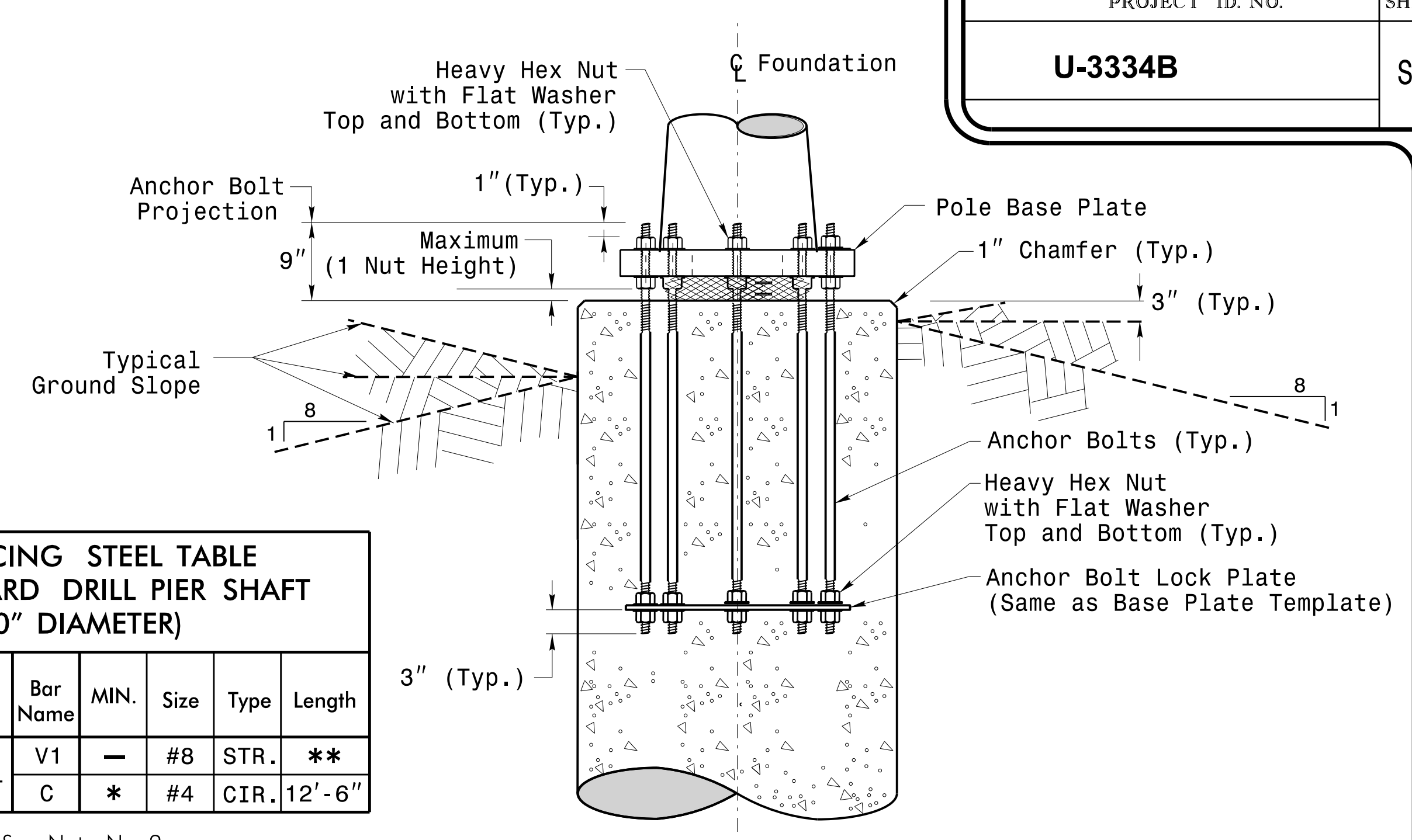
Typical Foundation Conduit Details

"D" Shaft Dia.	Conc. Volume (cu. yds.)	Bar Name	MIN.	Size	Type	Length
4'-0"	.465 x L	V1	-	#8	STR.	**
		C	*	#4	CIR.	12'-6"

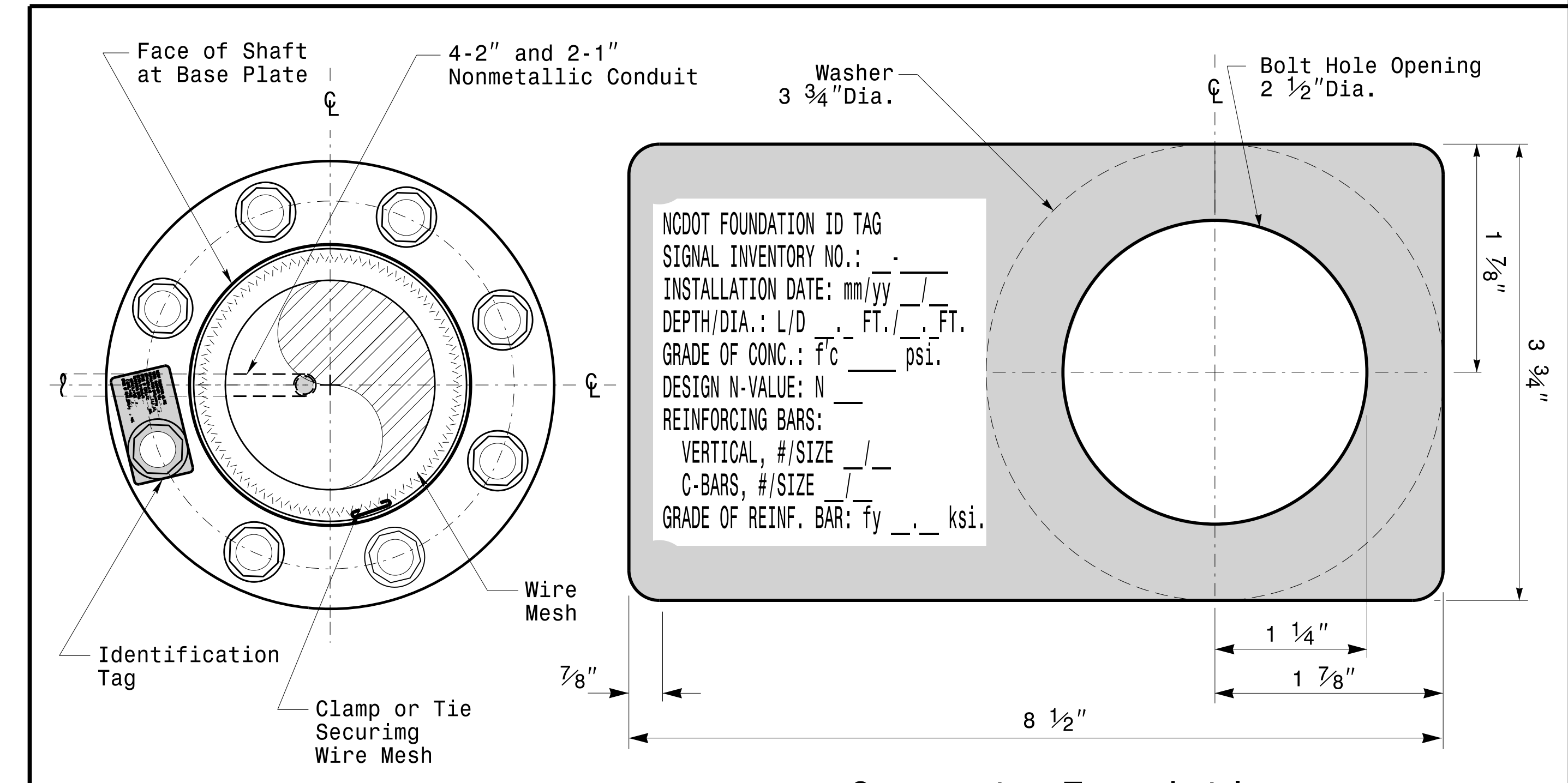
* See Note No. 2
** See Note No. 3

General Notes:

1. If actual subsurface conditions differ significantly from boring data contact the Engineer before excavating or placing concrete.
2. Circular tie reinforcing rings may be vertically adjusted by +/-3" at a depth between 2'-0" and 3'-0" to facilitate the installation of electrical conduit entering in the cage.
3. For standard foundations, see sheet Sig. M8 for details. Vertical reinforcing bars (V1) may be horizontally adjusted by +/-3" to facilitate the installation of electrical conduit entering into the cage.
4. Provide 2" to 5" foundation projection above ground level depending on the ground slope.
5. Unless otherwise shown, foundation designs are based on non-sloping level ground surfaces with slope ratios of 8:1 (H:V) or flatter. If actual ground line slopes are steeper contact the Engineer before excavating or placing concrete.
6. Construct foundations in accordance with NCDOT Standard Provisions SP09 R005- Foundations and Anchor Rod Assemblies for Metal Poles. All applicable 2018 NCDOT Standard Specifications are referenced in this provision. Refer to the NCDOT Resources/Specifications page located on the Connect NCDOT website.
[https://connect.ncdot.gov/resources/Specifications and Special Provisions.aspx](https://connect.ncdot.gov/resources/Specifications%20and%20Special%20Provisions.aspx)
7. Use air entrained AA concrete mix with a compression strength of f'c=4500 psi.(min.) after 28 days.
8. Use ASTM A615 grade 60 deformed bars for all reinforcing steel. Maintain at least 3" cover on all reinforcement.
9. Locate the Identification Tag on the top of the base plate, directly above the conduit's entry point.
10. Provide two layers of galvanized welded 23 gauge (0.25) 6" wide 4 mesh wire around pipes under the base plate and secure it with ties if necessary.
11. Preferred location for the I.D. Tag is as shown in Detail-A; directly above the conduit entering the foundation.



Typical Foundation Anchor Bolt Details
(Reinforcing Cage Not Shown for Clarity)



Concrete Foundation Identification Tag Details

D = Diameter
L = Length/Depth
mm = Month
yy = Year

Detail-A

<p>750 N. Greenfield Pkwy, Garner, NC 27529</p>	<p>Construction Details For Foundations</p>		
	<p>PLAN DATE: OCTOBER 2018</p> <p>DESIGNED BY: C.B. COGDILL</p> <p>PREPARED BY: N. BITTING</p> <p>REVIEWED BY: D.C. SARKAR</p>	<p>REV. NO.</p> <p>COMMENTS</p> <p>INIT.</p> <p>DATE</p>	

Construction Details - Foundations

11-001-2017-08:33T:13560W115:Signal:Design:Section:Eastern:Region:MM:Sheets:2016:2014:Sig.M7:Std.:Construction:Detail:IS-Strain:Poles.dgn

SOIL CONDITION

PROJECT ID. NO. SHEET NO.

U-3334B

Sig.M8

		STANDARD STRAIN POLES					STANDARD FOUNDATIONS 48" Diameter Drilled Pier Length (L) - Feet							Reinforcement				
		Case No.	Pole Height (Ft.)	Base Plate BC (In.)	Reactions at the Pole Base			Clay				Sand			Longitudinal		Stirrups	
					Axial (kip)	Shear (kip)	Moment (ft-kip)	Medium N-Value 4-8	Stiff N-Value 9-15	Very Stiff N-Value 16-30	Hard N-Value >30	Loose N-Value 4-10	Medium N-Value 11-30	Dense N-Value >30	Bar Size (#)	Quantity (ea.)	Bar Size (#)	Spacing (in.)
WIND ZONE 1	LIGHT	S26L3	26	25	2	11	270	19	13	10	8	17	14.5	12.5	8	12	4	12
		S30L3	30	25	2	11	300	19.5	13.5	10	8	17.5	15	13	8	14	4	12
		S35L3	35	25	3	11	320	20	13.5	10.5	8	17.5	15	13	8	14	4	12
	HEAVY	S30H3	30	29	3	16	450	24.5	16	12	9	21	17.5	15	8	16	4	6
		S35H3	35	29	4	16	515	26	17	12.5	9.5	22	18.5	16	8	16	4	6
WIND ZONE 2	LIGHT	S26L2	26	23	2	10	245	18	12.5	9.5	8	16.5	14	12	8	12	4	12
		S30L2	30	23	2	10	270	18.5	12.5	10	8	16.5	14	12.5	8	12	4	12
		S35L2	35	23	3	10	300	19.5	13	10	8	17	14.5	13	8	12	4	12
	HEAVY	S30H2	30	29	3	15	415	23	15.5	11.5	9	20	17	14.5	8	16	4	6
		S35H2	35	29	4	15	475	25	16.5	12	9.5	21	17.5	15.5	8	16	4	6
WIND ZONE 3	LIGHT	S26L2	26	23	2	10	245	18	12.5	9.5	8	16.5	14	12	8	12	4	12
		S30L2	30	23	2	10	270	18.5	12.5	10	8	16.5	14	12.5	8	12	4	12
		S35L2	35	23	3	10	300	19.5	13	10	8	17	14.5	13	8	12	4	12
	HEAVY	S30H2	30	29	3	15	415	23	15.5	11.5	9	20	17	14.5	8	16	4	6
		S35H2	35	29	4	15	475	25	16.5	12	9.5	21	17.5	15.5	8	16	4	6
WIND ZONE 4	LIGHT	S26L1	26	22	2	8	190	16	11.5	8.5	8	15	12.5	11	8	12	4	12
		S30L1	30	22	2	8	205	16.5	11.5	9	8	15	13	11.5	8	12	4	12
		S35L1	35	22	3	8	230	17	12	9	8	15.5	13.5	11.5	8	12	4	12
	HEAVY	S30H1	30	25	3	12	320	20.5	13.5	10.5	8	18	15	13.5	8	16	4	6
		S35H1	35	25	4	12	350	21	14	10.5	8.5	18.5	15.5	13.5	8	16	4	6
WIND ZONE 5	LIGHT	S26L2	26	23	2	10	245	18	12.5	9.5	8	16.5	14	12	8	12	4	12
		S30L2	30	23	2	10	270	18.5	12.5	10	8	16.5	14	12.5	8	12	4	12
		S35L2	35	23	3	10	300	19.5	13	10	8	17	14.5	13	8	12	4	12
	HEAVY	S30H2	30	29	3	15	415	23	15.5	11.5	9	20	17	14.5	8	16	4	6
		S35H2	35	29	4	15	475	25	16.5	12	9.5	21	17.5	15.5	8	16	4	6

General Notes:

- Values shown in the "Reactions at the Pole Base" column represent the minimum acceptable capacity allowed for design using a design CSR of 1.00.
- Use chairs and spacers to maintain proper clearance.
- For foundation, always use air-entrain concrete mix.

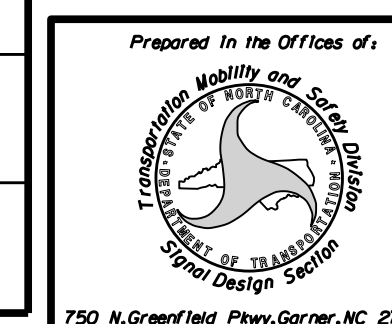
Foundation Selection:

- Perform a standard penetration test at each proposed foundation site to determine "N" value.
- Select the appropriate wind zone from M 1 drawing.
- Select the soil type (Clay or Sand) that best describes the soil characteristics.
- Get the appropriate standard pole case number from the plans or from the Engineer.
- Select the appropriate column under "Standard Foundations" based on soil type and "N" value. Select the appropriate row based on the pole load case.
- The foundation depth is the value shown in the "Standard Foundations" category where the column and the row intersect.
- Use Construction Procedures and Design Methods prescribed by FHWA-NHI-10-016 for Reference Drilled Shafts.

Standard Strain Pole Foundation-All Soil Condition

I:\Projects\2017_08-10\Sigs\Sig.M8\15 Signal\Signal Design Section\Eastern Region\M_Sig.M8_Std_Strain Pole Found_Saturated Soil_Condition.dgn

48" Dia. Foundations Concrete Volume (cubic yards) = (0.465) x Drilled Pier Length

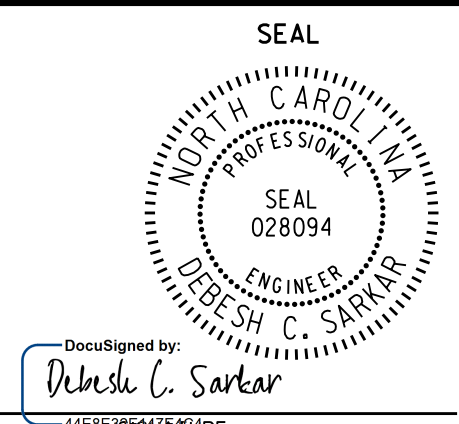


Standard Strain Pole Foundation for All Soil Conditions

PLAN DATE: OCTOBER 2017 DESIGNED BY: C.B. COGDELL
 PREPARED BY: N. BITTING REVIEWED BY: D.C. SARKAR

REVISIONS	INIT.	DATE
Changed "Foundation Depth" to "Drilled Pier Length" in Conc. Egn.	N.B.	7/12/2015

SCALE: 0 NA NONE



Designed by: *D. C. SARKAR* DATE: 10/11/2017

- 1 INSTALL REA, PE - 22, SHIELDED, TWISTED PAIR COMMUNICATIONS CABLE
- 2 INSTALL COAX CABLE
- 3 INSTALL ETHERNET CABLE
- 4 INSTALL SMFO CABLE
- 5 INSTALL MMFO CABLE
- 6 INSTALL FIBER OPTIC DROP CABLE
- 7 INSTALL TRACER WIRE
- 8 TRENCH
- 9 INSTALL PVC CONDUIT
- 10 INSTALL RIGID, GALVANIZED STEEL CONDUIT
- 11 INSTALL RIGID, GALVANIZED STEEL RISER WITH WEATHERHEAD
- 12 INSTALL RIGID, GALVANIZED STEEL RISER WITH FIBER OPTIC CABLE SEAL
- 13 INSTALL OUTER-DUCT POLYETHYLENE CONDUIT
- 14 INSTALL POLYETHYLENE CONDUIT
- 15 DIRECTIONAL DRILL CONDUIT
- 16 BORE AND JACK CONDUIT
- 17 INSTALL CABLE(S) IN EXISTING CONDUIT
- 18 INSTALL CABLE(S) IN NEW CONDUIT
- 19 INSTALL CABLE(S) IN EXISTING RISER
- 20 INSTALL CABLE(S) IN NEW RISER
- 21 INSTALL CABLE(S) IN EXISTING CONDUIT STUB-OUTS
- 22 INSTALL NEW CONDUIT INTO EXISTING CABINET BASE (USE EXISTING CONDUIT STUB-OUTS WHEN AVAILABLE)
- 23 INSTALL NEW RISER INTO EXISTING CABINET BASE (USE EXISTING CONDUIT STUB-OUTS WHEN AVAILABLE)
- 24 INSTALL NEW CONDUIT INTO EXISTING POLE MOUNTED CABINET
- 25 INSTALL NEW RISER INTO EXISTING POLE MOUNTED CABINET
- 26 MODIFY EXISTING INTERCONNECT CENTER /SPLICE ENCLOSURE
- 27 INSTALL NEW FIBER OPTIC TRANSCEIVER
- 28 INSTALL INTERCONNECT CENTER, PATCH PANEL, JUMPERS AND FUSION SPLICE CABLE IN CABINET
- 29 INSTALL UNDERGROUND SPLICE ENCLOSURE
- 30 INSTALL AERIAL SPLICE ENCLOSURE
- 31 INSTALL POLE MOUNTED SPLICE CABINET
- 32 INSTALL BASE MOUNTED SPLICE CABINET
- 33 REMOVE EXISTING SPLICE CABINET

- 34 INSTALL CABINET FOUNDATION
- 35 REMOVE EXISTING CABINET FOUNDATION
- 36 INSTALL CCTV CAMERA ASSEMBLY
- 37 INSTALL CCTV CAMERA WOOD POLE
- 38 INSTALL CCTV CAMERA METAL POLE AND FOUNDATION
- 39 INSTALL JUNCTION BOX
- 40 INSTALL OVERSIZED JUNCTION BOX
- 41 REMOVE EXISTING JUNCTION BOX
- 42 INSTALL WOOD POLE
- 43 REMOVE EXISTING WOOD POLE
- 44 INSTALL AERIAL GUY ASSEMBLY
- 45 INSTALL STANDARD GUY ASSEMBLY
- 46 INSTALL SIDEWALK GUY ASSEMBLY
- 47 INSTALL MESSENGER CABLE
- 48 REMOVE EXISTING COMMUNICATIONS AND MESSENGER CABLE
- 49 REMOVE EXISTING MESSENGER CABLE
- 50 INSTALL TELEPHONE SERVICE
- 51 INSTALL CABLE STORAGE RACKS (SNOW SHOES) AND STORE 100 FEET OF CABLE
- 52A INSTALL DELINEATOR MARKER
- 52B INSTALL JUNCTION BOX MARKER
- 53 STORE 20 FEET OF COMMUNICATIONS CABLE
- 54 LASH CABLE(S) TO EXISTING SIGNAL/COMMUNICATIONS CABLE
- 55 LASH CABLE(S) TO EXISTING MESSENGER CABLE
- 56 LASH CABLE(S) TO NEW MESSENGER CABLE
- 57 MODIFY EXISTING ELECTRICAL SERVICE
- 58 INSTALL NEW ELECTRICAL SERVICE
- 59 INSTALL NEW FIELD ETHERNET SWITCH
- 60 BOND TRACER WIRE TO EQUIPMENT GROUND BUS
- 61 DO NOT BOND TRACER WIRE TO EQUIPMENT GROUND BUS
- 62 BOND RISER AND MESSENGER CABLE TO POLE GROUND

ATTACHMENT POINT:

- XX"/SS YYY DISTANCE ABOVE (IN)/ATTACHMENT POINT REFERENCE POINT
 - YYY XX"/SS REFERENCE POINT DISTANCE BELOW (IN)/ATTACHMENT POINT
- "SS" REFERENCE LOCATION

FS = FRONT SIDE OF POLE

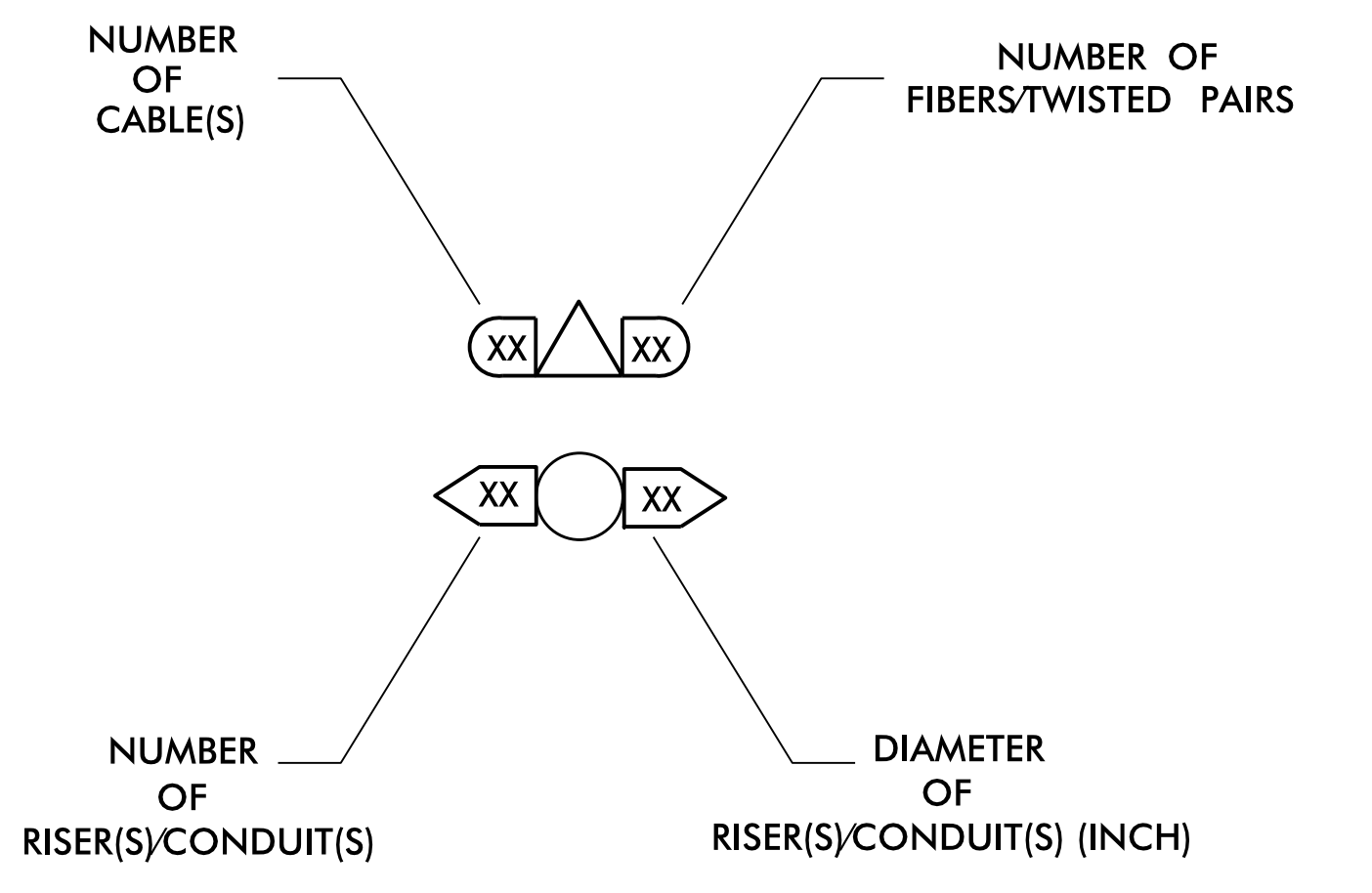
BS = BACK SIDE OF POLE

LEGEND

- FO NEW FIBER OPTIC COMMUNICATIONS CABLE
- TWIST PR NEW TWISTED PAIR COMMUNICATIONS CABLE
- EXI EXISTING COMMUNICATIONS CABLE
- REM EXISTING COMMUNICATIONS CABLE TO BE REMOVED
- NEW AERIAL GUY ASSEMBLY
- NEW CONDUIT
- EXISTING CONDUIT
- DD NEW DIRECTIONAL DRILLED CONDUIT
- B&J NEW BORED AND JACKED CONDUIT
- NEW JUNCTION BOX
- EXISTING JUNCTION BOX
- NEW WOOD POLE
- EXISTING WOOD POLE
- AERIAL SPLICE ENCLOSURE
- NEW METAL POLE
- EXISTING METAL POLE
- NEW CCTV ASSEMBLY
- NEW STANDARD GUY ASSEMBLY
- NEW SIDEWALK GUY ASSEMBLY
- NEW CABLE STORAGE RACKS (SNOW SHOES)
- EXISTING CONTROLLER AND CABINET
- EXISTING SPLICE CABINET
- NEW SPLICE CABINET
- SP SIGNAL POLE
- XX-XXXX SIGNAL INVENTORY NUMBER

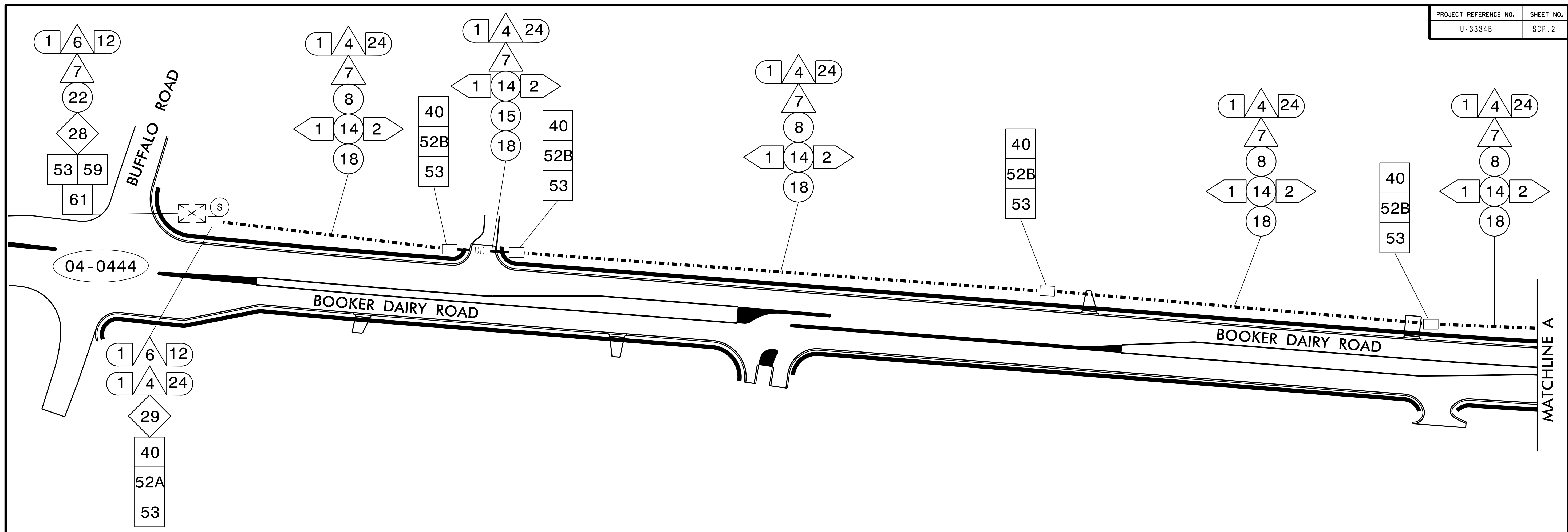
CONSTRUCTION NOTE SYMBOLOGY KEY

- XX INDICATES NUMBER OF CABLES, LOOPS, ETC.
- XX INDICATES NUMBER OF FIBERS PER CABLE, TWISTED PAIRS PER CABLE, ETC.
- XX INDICATES NUMBER OF RISER(S)/CONDUIT(S)
- XX INDICATES DIAMETER OF RISER(S)/CONDUIT(S) (INCH)



DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

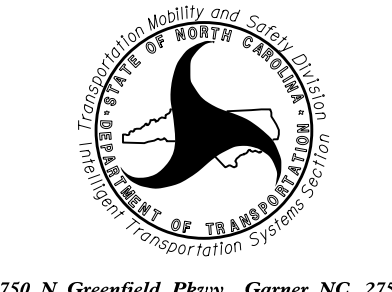
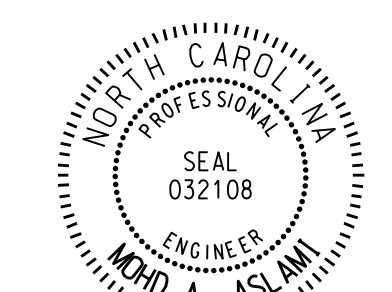

	CONSTRUCTION NOTES		SEAL
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD PLAN DATE: AUGUST 2017 REVIEWED BY: <i>Neil Avery</i> PREPARED BY: A. J. SKUCE	REVISIONS INIT. DATE	

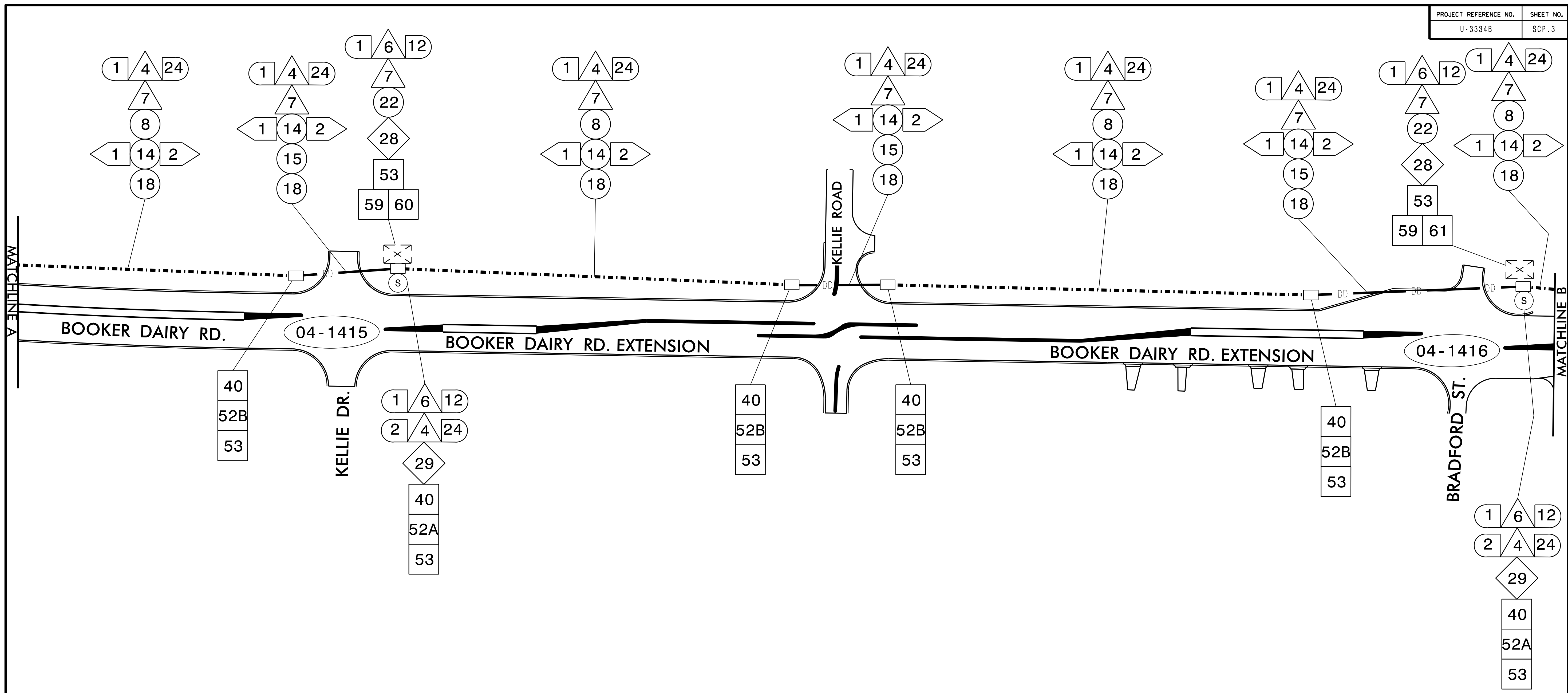


- 1) FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 04 DEPUTY TRAFFIC ENGINEER, RUSSELL BROADWELL, AT (252) 640-6507 TO ARRANGE FOR THE DIVISION TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DEPUTY TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.
- 2) PROVIDE AS-BUILT PLANS TO THE ENGINEER IF FINAL SPLICE ARRANGEMENT DIFFERS FROM THE SUPPLIED SPLICE DETAILS.

FINAL

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

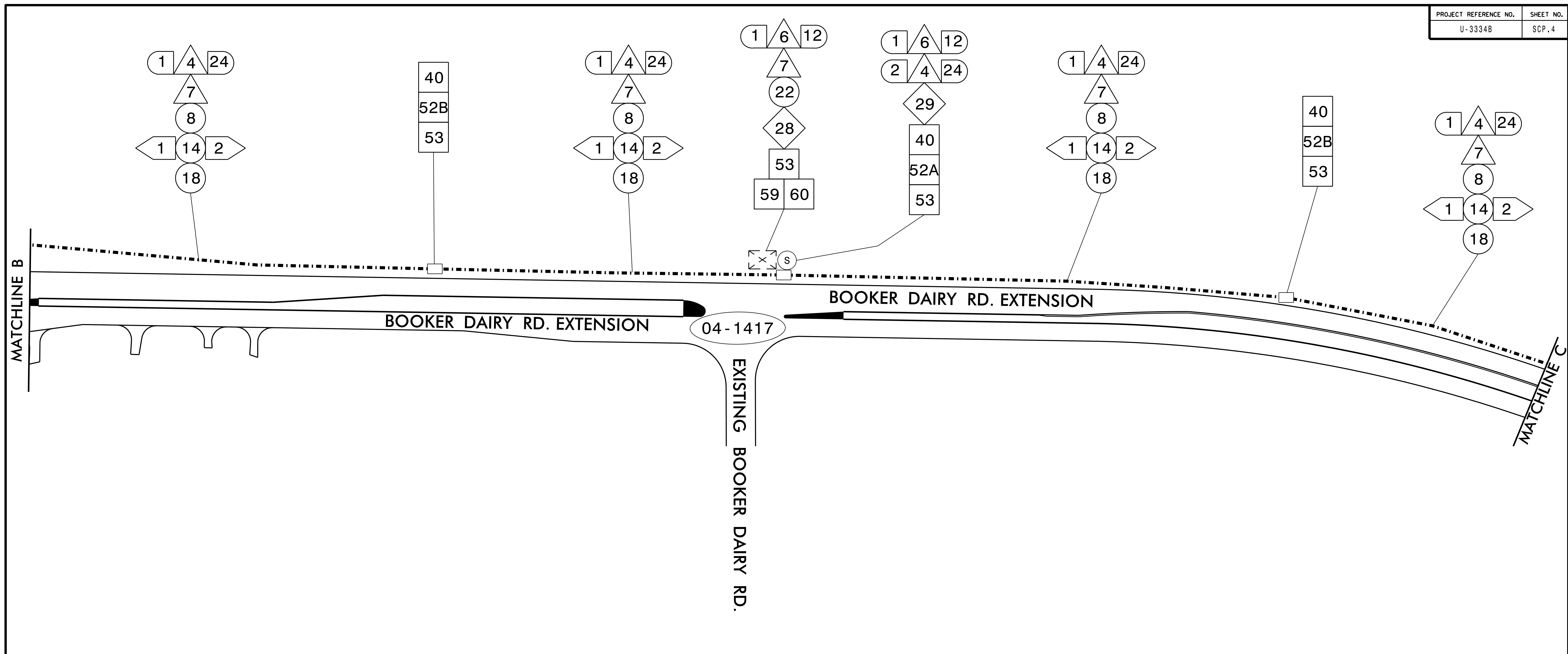
 Prepared in the Offices of: 750 N. Greenfield Pkwy., Garner, NC 27529	COMMUNICATIONS CABLE AND CONDUIT ROUTING PLANS		SEAL  M. A. ASLAMI ENGINEER 032108
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD PLAN DATE: AUGUST 2017 PREPARED BY: A. J. SKUCE REVISIONS:	REVIEWED BY: <i>Neil Avery</i> DATE:	
 SCALE: 1" = 70'			



- 1) FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 04 DEPUTY TRAFFIC ENGINEER, RUSSELL BROADWELL, AT (252) 640-6507 TO ARRANGE FOR THE DIVISION TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DEPUTY TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.
- 2) PROVIDE AS-BUILT PLANS TO THE ENGINEER IF FINAL SPLICE ARRANGEMENT DIFFERS FROM THE SUPPLIED SPLICE DETAILS.

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

 <small>750 N. Greenfield Pkwy., Garner, NC 27529</small>	COMMUNICATIONS CABLE AND CONDUIT ROUTING PLANS		SEAL
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD PLAN DATE: AUGUST 2017 REVIEWED BY: <i>Neil Avery</i> PREPARED BY: A. J. SKUCE		 M. A. ASLAMI ENGINEER 032108
 SCALE 1" = 70' 0 70'	REVISIONS _____ _____	INIT. DATE _____ _____	DATE 9/27/2017 DATE

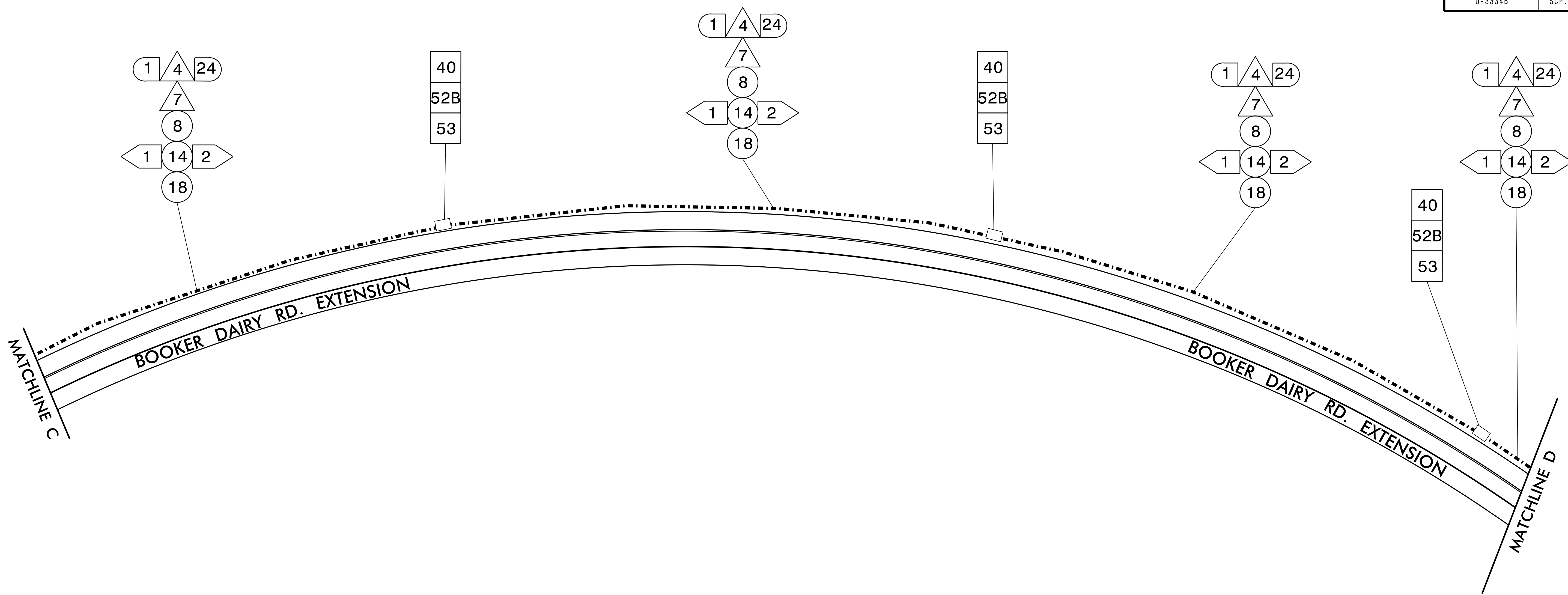


- 1) FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 04 DEPUTY TRAFFIC ENGINEER, RUSSELL BROADWELL, AT (252) 640-6507 TO ARRANGE FOR THE DIVISION TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DEPUTY TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.
- 2) PROVIDE AS-BUILT PLANS TO THE ENGINEER IF FINAL SPLICE ARRANGEMENT DIFFERS FROM THE SUPPLIED SPLICE DETAILS.

FINAL

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

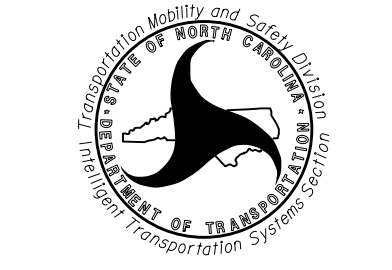
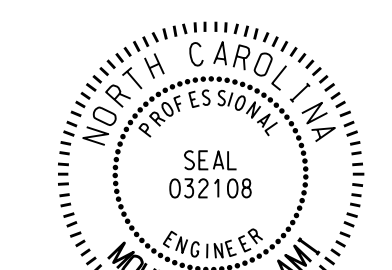
	COMMUNICATIONS CABLE AND CONDUIT ROUTING PLANS		SEAL 032108 ENGINEER M. A. ASLAMI 9/27/2017 DATE
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD PLAN DATE: AUGUST 2017 REVIEWED BY: <i>Mil Avery</i> PREPARED BY: A. J. SKUCE	REVISIONS INIT. DATE	
	SCALE 0 70' 1" = 70'		

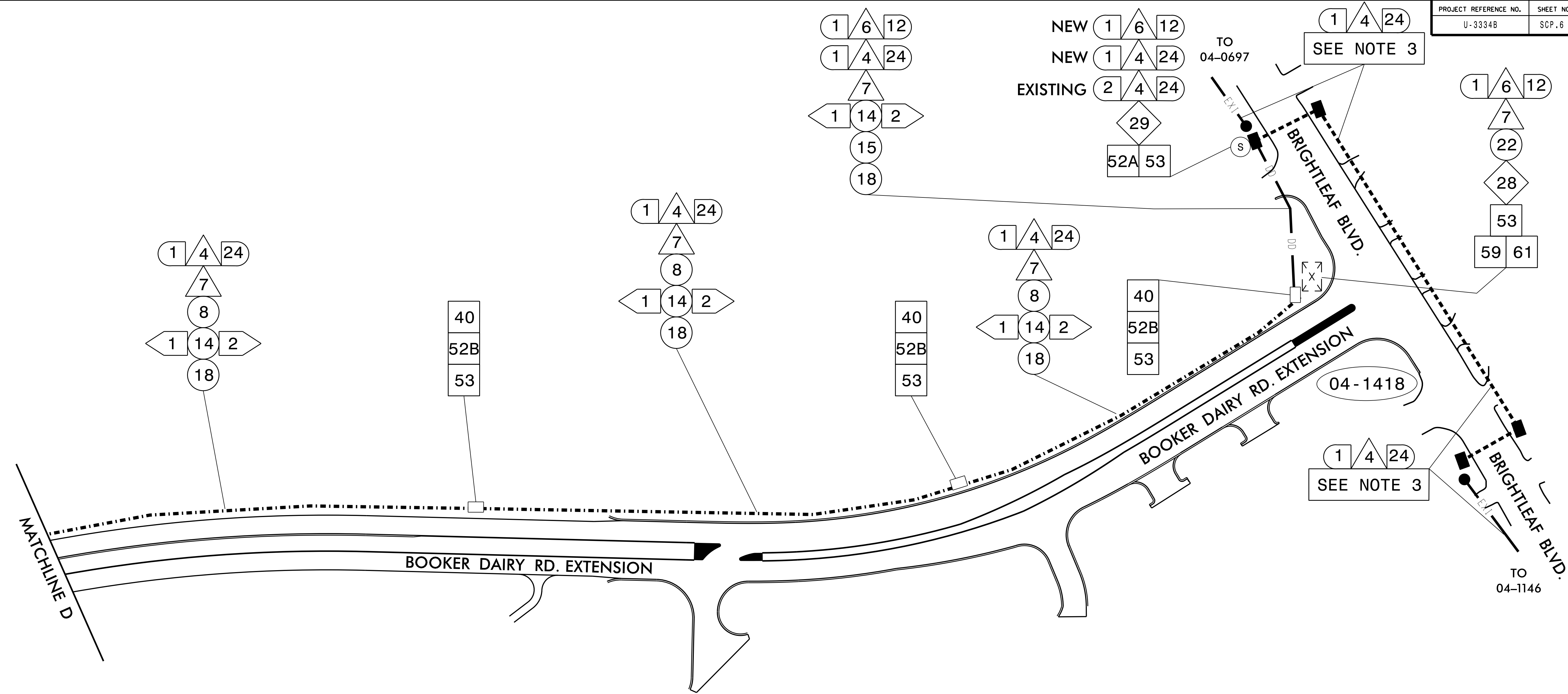


1) FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 04 DEPUTY TRAFFIC ENGINEER, RUSSELL BROADWELL, AT (252) 640-6507 TO ARRANGE FOR THE DIVISION TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DEPUTY TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.

FINAL

**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

 Prepared in the Offices of: 750 N. Greenfield Pkwy., Garner, NC 27529	COMMUNICATIONS CABLE AND CONDUIT ROUTING PLANS		SEAL  ENGINEER MOHD A. ASLAMANI 032108
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD		
SCALE 0 70' 1" = 70'	PREPARED BY: A. J. SKUCE	REVISIONS	INIT. DATE <i>Mohd Aslami</i> 9/27/2017

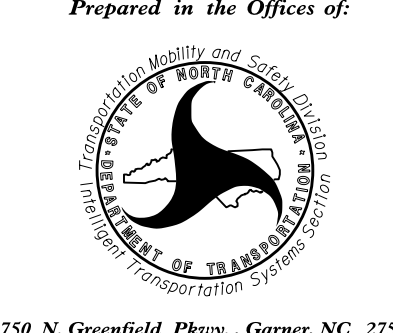


- 1) FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 04 DEPUTY TRAFFIC ENGINEER, RUSSELL BROADWELL, AT (252) 640-6507 TO ARRANGE FOR THE DIVISION TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DEPUTY TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.

- 2) CONTRACTOR TO RECORD EXISTING SPLICE ARRANGEMENT FOR COMPARISON TO THE SUPPLIED SPLICE DETAILS. IF DISCREPANCIES EXIST, CONTACT THE ENGINEER TO DETERMINE HOW TO PROCEED WITH RESPLICING. PROVIDE AS-BUILT PLANS TO THE ENGINEER IF FINAL SPLICE ARRANGEMENT DIFFERS FROM THE SUPPLIED SPLICE DETAILS.

- 3) EXISTING CONDUIT AND FIBER INSTALLED UNDER W-5601DO.

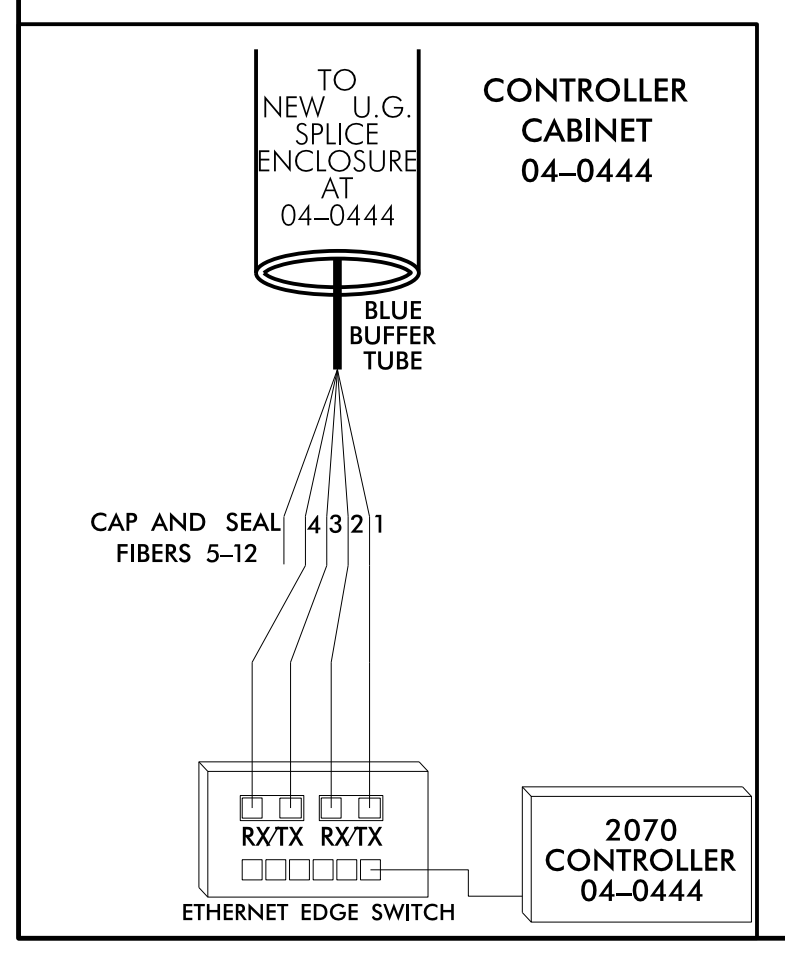
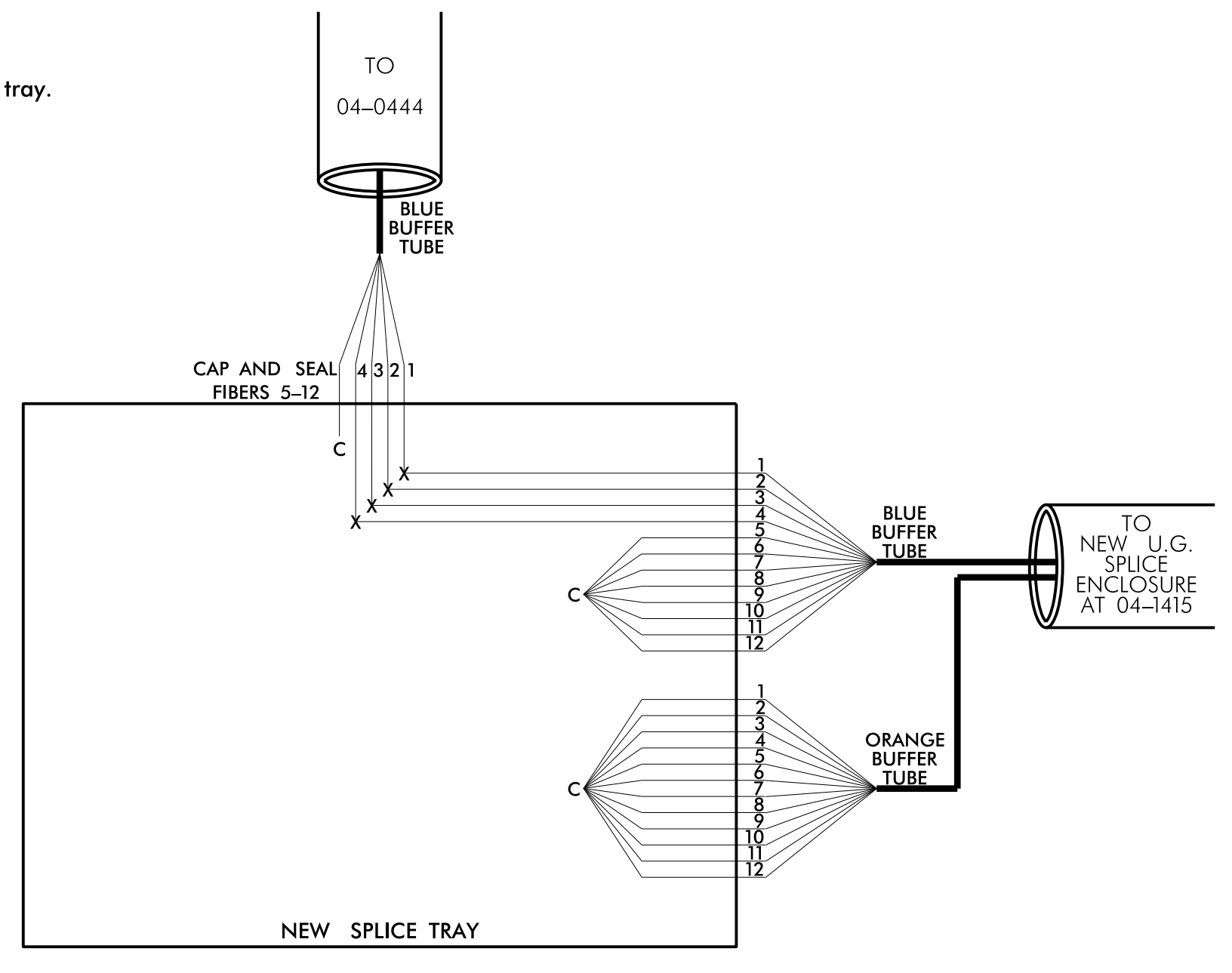
**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

 <small>750 N. Greenfield Pkwy., Garner, NC 27529</small>	COMMUNICATIONS CABLE AND CONDUIT ROUTING PLANS		SEAL
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD PLAN DATE: AUGUST 2017 REVIEWED BY: <i>Neil Avery</i> PREPARED BY: A. J. SKUCE		SEAL 032108 ENGINEER MOHD A. ASLAMI 9/27/2017 DATE
SCALE 70' 1" = 70'			

**NEW UNDERGROUND SPLICE ENCLOSURE
BOOKER DAIRY RD. AT BUFFALO RD.
SIG. INV. # 04-0444**

Notes:
Unused fibers left coiled and stored in splice tray.
Unused Buffer Tubes left coiled and stored in splice tray.

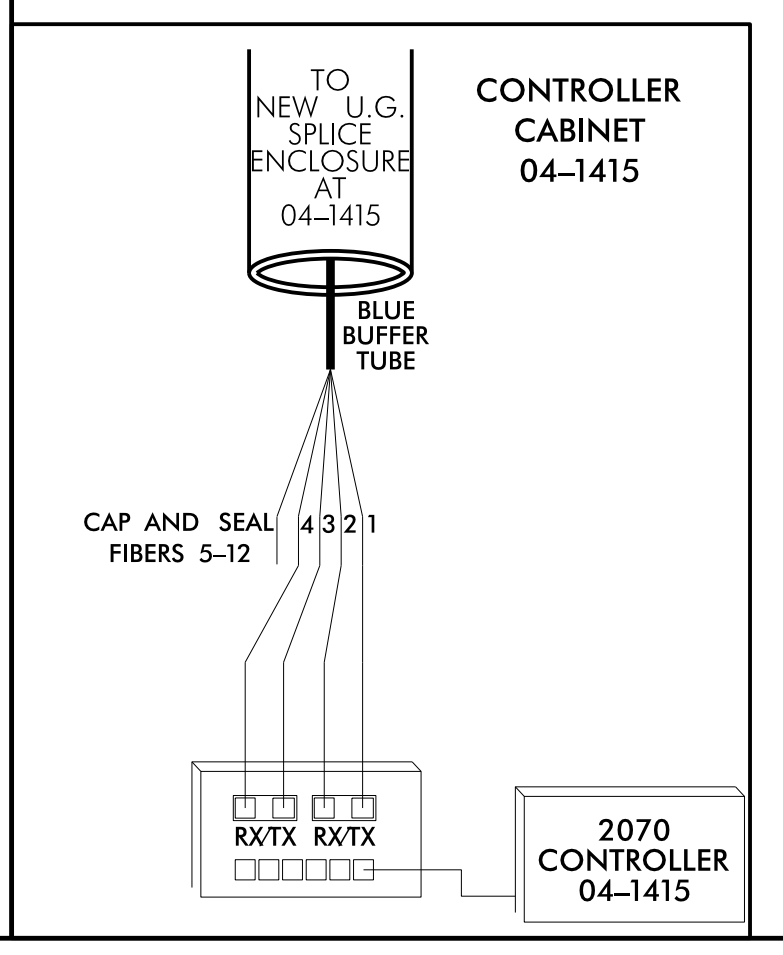
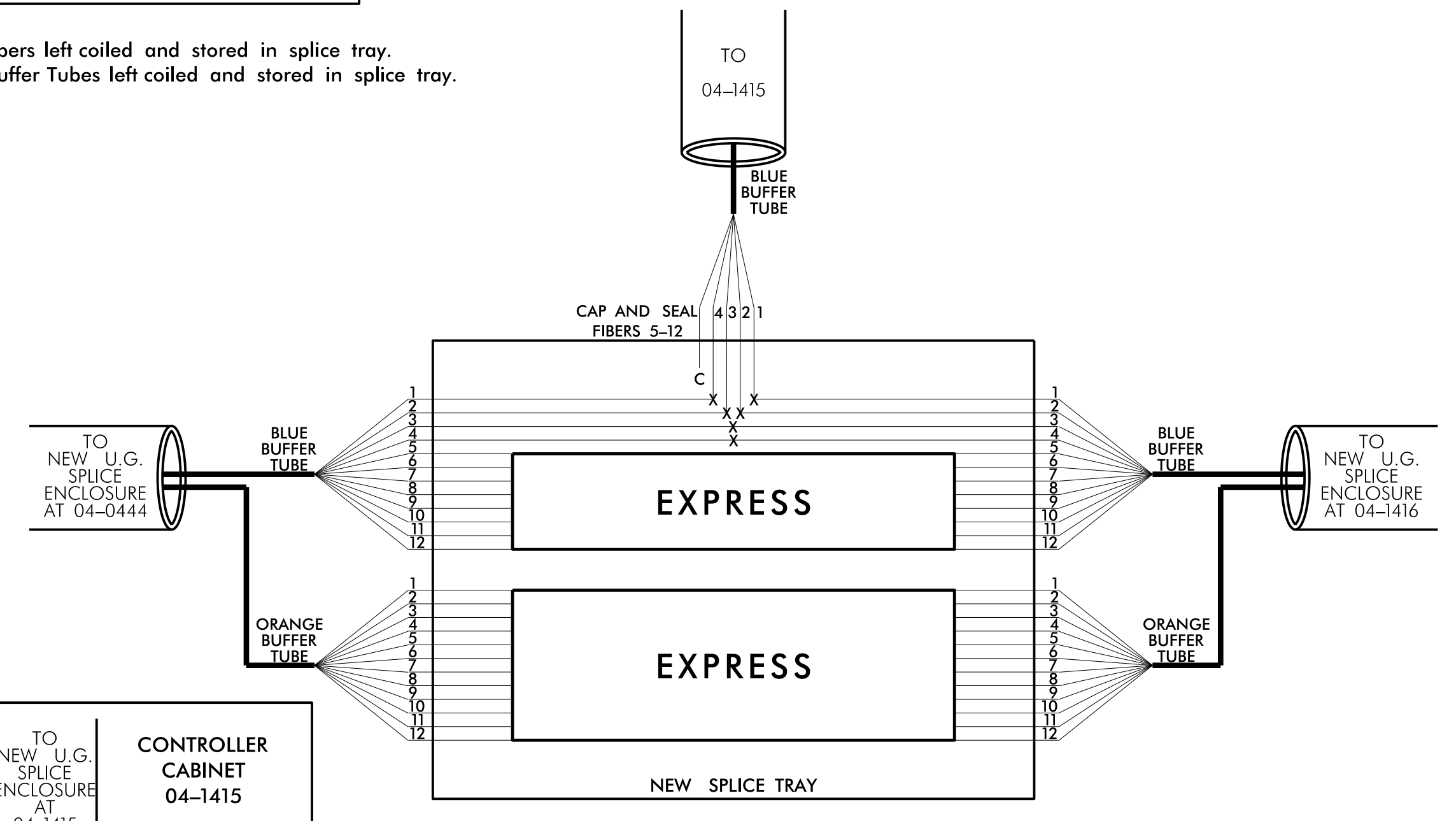
- LEGEND**
X = FUSION SPLICE
C = CAP IN TRAY
- COLOR CODE**
TIA/EIA 598-A
- (1) BLUE
 - (2) ORANGE
 - (3) GREEN
 - (4) BROWN
 - (5) SLATE
 - (6) WHITE
 - (7) RED
 - (8) BLACK
 - (9) YELLOW
 - (10) PURPLE
 - (11) ROSE
 - (12) AQUA



**NEW UNDERGROUND SPLICE ENCLOSURE
BOOKER DAIRY RD. AT KELLIE DR.
SIG. INV. # 04-1415**

Notes:
Unused fibers left coiled and stored in splice tray.
Unused Buffer Tubes left coiled and stored in splice tray.

- LEGEND**
X = FUSION SPLICE
C = CAP IN TRAY
- COLOR CODE**
TIA/EIA 598-A
- (1) BLUE
 - (2) ORANGE
 - (3) GREEN
 - (4) BROWN
 - (5) SLATE
 - (6) WHITE
 - (7) RED
 - (8) BLACK
 - (9) YELLOW
 - (10) PURPLE
 - (11) ROSE
 - (12) AQUA



- 1) FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 04 DEPUTY TRAFFIC ENGINEER, RUSSELL BROADWELL, AT (252) 640-6507 TO ARRANGE FOR THE DIVISION TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DEPUTY TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.
 - 2) CONTRACTOR TO RECORD FINAL SPLICE ARRANGEMENT FOR COMPARISON TO THE SUPPLIED SPLICE DETAILS. IF DISCREPANCIES EXIST, PROVIDE AS-BUILT PLANS TO THE ENGINEER.
 - 3) ETHERNET SWITCH TERMINATION CONFIGURATIONS ARE GENERIC. CONTRACTOR IS RESPONSIBLE FOR DETERMINING \ ENSURING PROPER TERMINATIONS.
 - 4) INCLUDE ON THE COVER OF EACH SPLICE TRAY THE FOLLOWING:
REFERENCE SECTION 1731 "FIBER OPTIC SPLICE ENCLOSURE"
- 1) SPLICE LOCATION
 - 2) DATE
 - 3) COMPANY NAME
 - 4) NAME OF INDIVIDUAL PERFORMING THE SPLICING

PRIOR TO INSTALLING THE COVER ON THE SPLICE TRAY TAKE A DIGITAL PHOTOGRAPH SHOWING THE SPLICE TRAY AND INFORMATION SHOWN ABOVE (1-4) AND SUBMIT PHOTOGRAPH ALONG WITH OTDR TEST RESULTS.

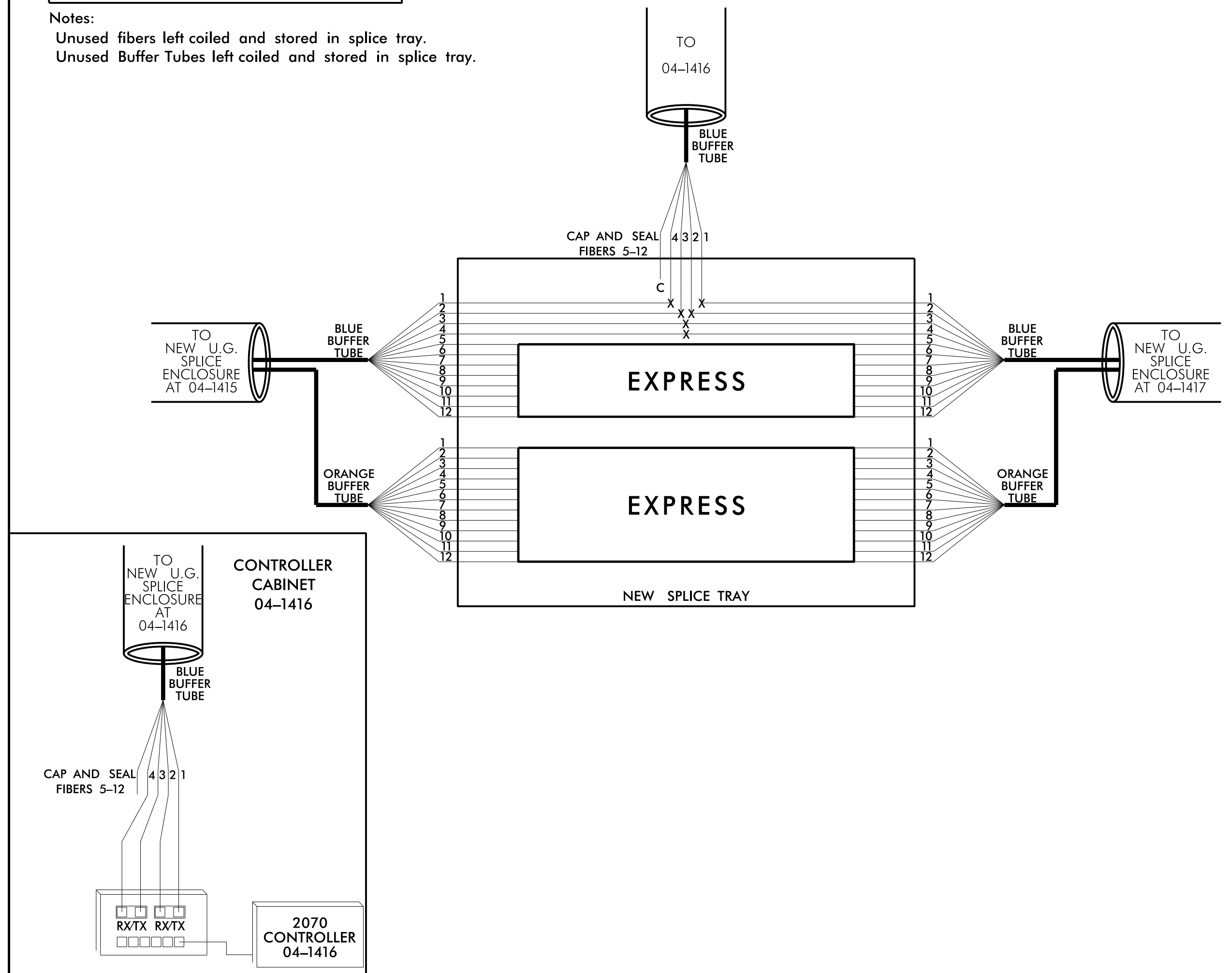
**DOCUMENT NOT CONSIDERED FINAL
UNLESS ALL SIGNATURES COMPLETED**

 <small>750 N. Greenfield Pkwy., Garner, NC 27529</small>	SPLICE DETAIL		SEAL
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD PLAN DATE: AUGUST 2017 REVIEWED BY: <i>Neil Avery</i> PREPARED BY: A. J. SKUCE	ENGINEER <i>Mohd A. Aslami</i> DATE: 9/27/2017	
REVISIONS: _____ INIT. DATE _____		DATE: _____	

NEW UNDERGROUND SPlice ENCLOSURE
BOOKER DAIRY RD. EXT. AT BRADFORD ST.
SIG. INV. # 04-1416

Notes:
 Unused fibers left coiled and stored in splice tray.
 Unused Buffer Tubes left coiled and stored in splice tray.

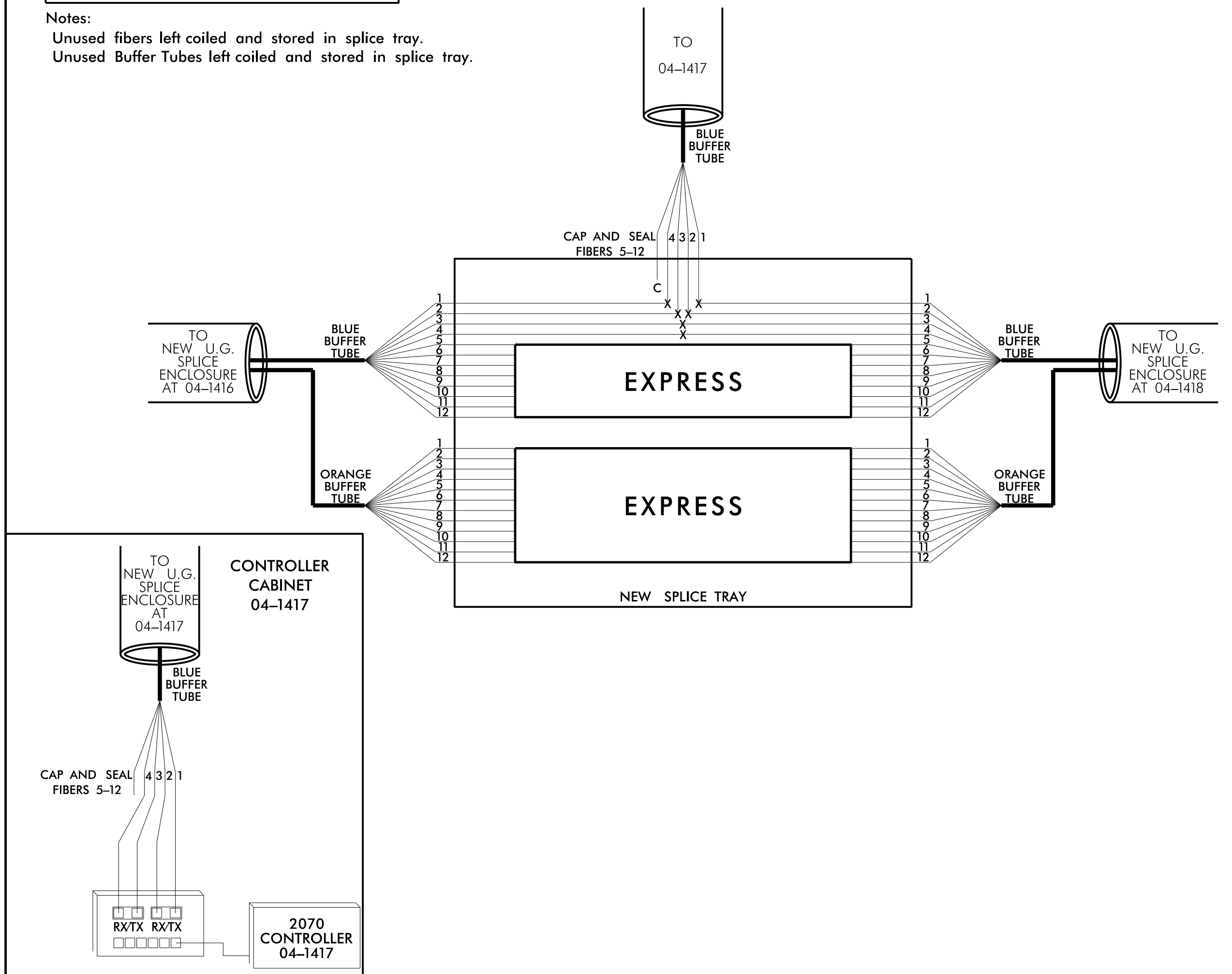
- LEGEND**
 X = FUSION SPlice
 C = CAP IN TRAY
- COLOR CODE**
 TIA/EIA 598-A
- (1) BLUE
 - (2) ORANGE
 - (3) GREEN
 - (4) BROWN
 - (5) SLATE
 - (6) WHITE
 - (7) RED
 - (8) BLACK
 - (9) YELLOW
 - (10) PURPLE
 - (11) ROSE
 - (12) AQUA



NEW UNDERGROUND SPlice ENCLOSURE
BOOKER DAIRY RD. EXT. AT
EXISTING BOOKER DAIRY RD.
SIG. INV. # 04-1417

Notes:
 Unused fibers left coiled and stored in splice tray.
 Unused Buffer Tubes left coiled and stored in splice tray.

- LEGEND**
 X = FUSION SPlice
 C = CAP IN TRAY
- COLOR CODE**
 TIA/EIA 598-A
- (1) BLUE
 - (2) ORANGE
 - (3) GREEN
 - (4) BROWN
 - (5) SLATE
 - (6) WHITE
 - (7) RED
 - (8) BLACK
 - (9) YELLOW
 - (10) PURPLE
 - (11) ROSE
 - (12) AQUA

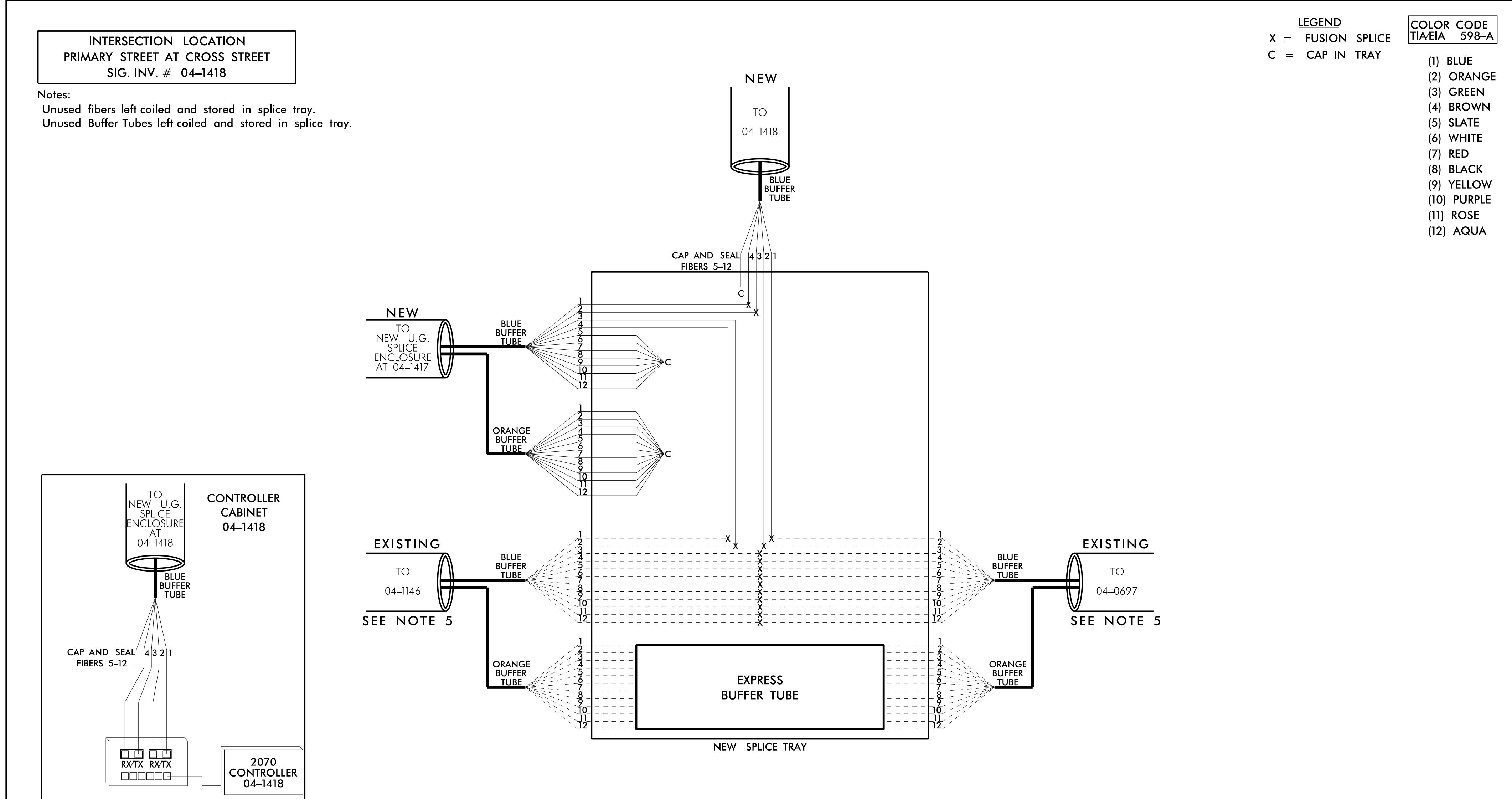


- 1) FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 04 DEPUTY TRAFFIC ENGINEER, RUSSELL BROADWELL, AT (252) 640-6507 TO ARRANGE FOR THE DIVISION TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DEPUTY TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.
- 2) CONTRACTOR TO RECORD FINAL SPlice ARRANGEMENT FOR COMPARISON TO THE SUPPLIED SPlice DETAILS. IF DISCREPANCIES EXIST, PROVIDE AS-BUILT PLANS TO THE ENGINEER.
- 3) ETHERNET SWITCH TERMINATION CONFIGURATIONS ARE GENERIC. CONTRACTOR IS RESPONSIBLE FOR DETERMINING \ ENSURING PROPER TERMINATIONS.
- 4) INCLUDE ON THE COVER OF EACH SPlice TRAY THE FOLLOWING:
 REFERENCE SECTION 1731 "FIBER OPTIC SPlice ENCLOSURE"
 1) SPlice LOCATION
 2) DATE
 3) COMPANY NAME
 4) NAME OF INDIVIDUAL PERFORMING THE SPlicing

PRIOR TO INSTALLING THE COVER ON THE SPlice TRAY TAKE A DIGITAL PHOTOGRAPH SHOWING THE SPlice TRAY AND INFORMATION SHOWN ABOVE (1-4) AND SUBMIT PHOTOGRAPH ALONG WITH OTRD TEST RESULTS.

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

 Prepared in the Offices of: 750 N. Greenfield Pkwy., Garner, NC 27529	SPLICE DETAIL		SEAL
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD PLAN DATE: AUGUST 2017 REVIEWED BY: <i>Neil Avery</i> PREPARED BY: A. J. SKUCE		NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 032108 MOHD A. ASLAMI 9/27/2017
REVISIONS: _____ INIT. DATE _____		DATE: _____	

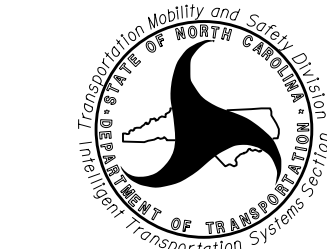



- 1) FIVE (5) DAYS PRIOR TO BEGINNING WORK ON THE SIGNAL SYSTEM, CONTACT THE DIVISION 04 DEPUTY TRAFFIC ENGINEER, RUSSELL BROADWELL, AT (252) 640-6507 TO ARRANGE FOR THE DIVISION TO PROGRAM THE NEW FIELD ETHERNET SWITCHES WITH THE NECESSARY NETWORK CONFIGURATION DATA, INCLUDING BUT NOT LIMITED TO: THE PROJECT IP ADDRESS, DEFAULT GATEWAY, SUBNET MASK AND VLAN ID INFORMATION. NOTIFY THE DEPUTY TRAFFIC ENGINEER AFTER ALL WORK IS PERFORMED TO ENSURE THAT ALL FIBER CIRCUITS ARE FUNCTIONING PROPERLY. WORK IS NOT COMPLETE UNTIL THE SIGNAL SYSTEM IS BACK UP AND OPERATIONAL.
- 2) CONTRACTOR TO RECORD FINAL SPLICE ARRANGEMENT FOR COMPARISON TO THE SUPPLIED SPLICE DETAILS. IF DISCREPANCIES EXIST, PROVIDE AS-BUILT PLANS TO THE ENGINEER.
- 3) ETHERNET SWITCH TERMINATION CONFIGURATIONS ARE GENERIC. CONTRACTOR IS RESPONSIBLE FOR DETERMINING \ ENSURING PROPER TERMINATIONS.
- 4) INCLUDE ON THE COVER OF EACH SPLICE TRAY THE FOLLOWING: REFERENCE SECTION 1731 "FIBER OPTIC SPLICE ENCLOSURE"
 - 1) SPLICE LOCATION
 - 2) DATE
 - 3) COMPANY NAME
 - 4) NAME OF INDIVIDUAL PERFORMING THE SPLICING

5) EXISTING CONDUIT AND FIBER INSTALLED UNDER W-5601DO. IF THIS EXISTING CABLE IS NOT INSTALLED CONTACT THE DEPUTY TRAFFIC ENGINEER TO DETERMINE HOW TO PROCEED.

PRIOR TO INSTALLING THE COVER ON THE SPLICE TRAY TAKE A DIGITAL PHOTOGRAPH SHOWING THE SPLICE TRAY AND INFORMATION SHOWN ABOVE (1-4) AND SUBMIT PHOTOGRAPH ALONG WITH OTRD TEST RESULTS.

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

 <small>750 N. Greenfield Pkwy., Garner, NC 27529</small>	SPLICE DETAIL		SEAL
	DIVISION 04 JOHNSTON COUNTY SMITHFIELD PLAN DATE: AUGUST 2017 REVIEWED BY: <i>Neil Avery</i> PREPARED BY: A. J. SKUCE		 ENGINEER MOHD A. ASLAM
REVISIONS _____		INIT. DATE _____	DATE 9/27/2017