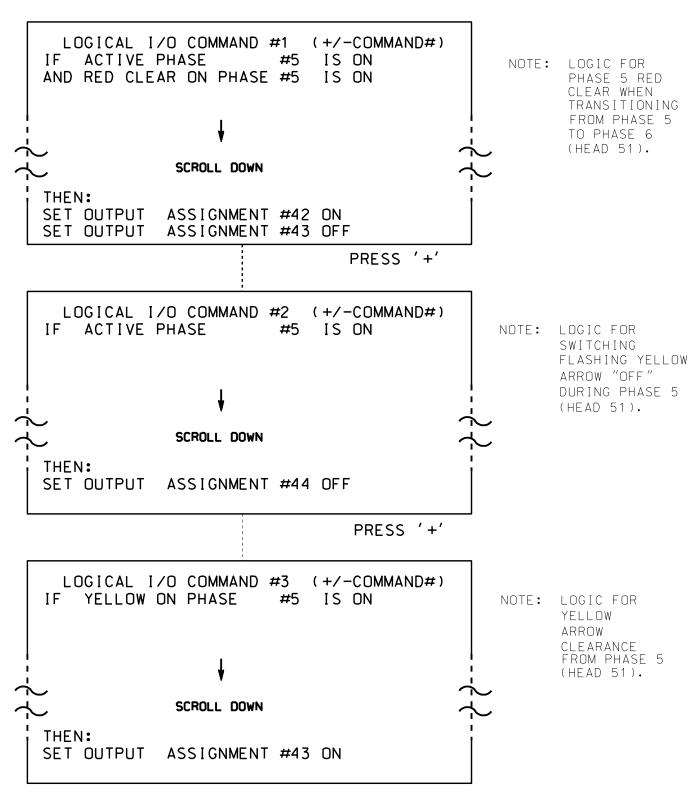
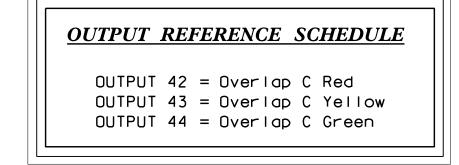
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



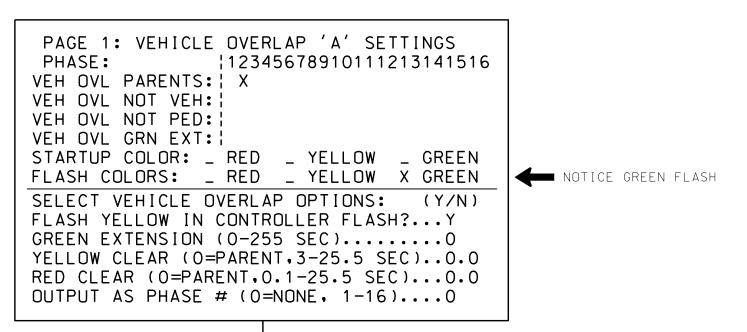
LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

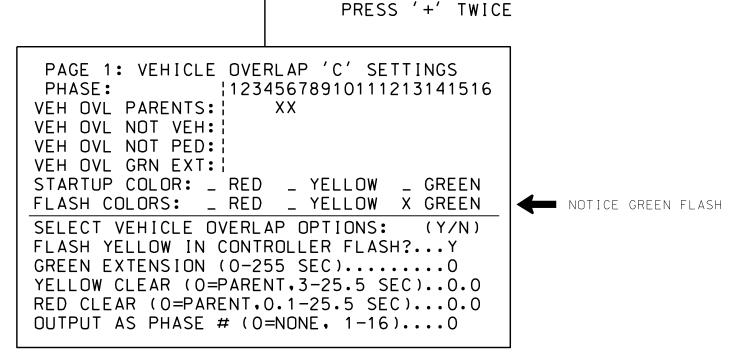


OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).





OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-1418 DESIGNED: June 2017 SEALED: 8/30/2017 REVISED:

Electrical Detail - Sheet 2 of 2 ELECTRICAL AND PROGRAMMING

DETAILS FOR: Prepared in the Offices of:

750 N.Greenfield Pkwy, Garner, NC 27529

US 301 (Brightleaf Boulevard) Ava Gardner Street

ivision 4 Johnston County Smithfield PLAN DATE: August 2017 REVIEWED BY: T. Joyce PREPARED BY: C. Strickland REVIEWED BY:

REVISIONS INIT. DATE

SIGNATURES COMPLETED 031001 SIG. INVENTORY NO. 04-1418

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