LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, AND 12.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE        #1  IS ON
AND RED CLEAR ON PHASE #1  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #50 ON
SET OUTPUT  ASSIGNMENT #51 OFF
```
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE        #1  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #52 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 1 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE     #1  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #51 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #4  (+/-COMMAND#)
IF  ACTIVE PHASE        #5  IS ON
AND RED CLEAR ON PHASE #5  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #42 ON
SET OUTPUT  ASSIGNMENT #43 OFF
```
NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #5  (+/-COMMAND#)
IF  ACTIVE PHASE        #5  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #44 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 5 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #6  (+/-COMMAND#)
IF  YELLOW ON PHASE     #5  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #43 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #7  (+/-COMMAND#)
IF  ACTIVE PHASE        #3  IS ON
AND RED CLEAR ON PHASE #3  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #47 ON
SET OUTPUT  ASSIGNMENT #48 OFF
```
NOTE: LOGIC FOR PHASE 3 RED CLEAR WHEN TRANSITIONING FROM PHASE 3 TO PHASE 4 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #8  (+/-COMMAND#)
IF  ACTIVE PHASE        #3  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #49 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 3 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #9  (+/-COMMAND#)
IF  YELLOW ON PHASE     #3  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #48 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 3 (HEAD 31).

PRESS '+'

```
LOGICAL I/O COMMAND #10 (+/-COMMAND#)
IF  ACTIVE PHASE        #7  IS ON
AND RED CLEAR ON PHASE #7  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #39 ON
SET OUTPUT  ASSIGNMENT #40 OFF
```
NOTE: LOGIC FOR PHASE 7 RED CLEAR WHEN TRANSITIONING FROM PHASE 7 TO PHASE 8 (HEAD 71).

PRESS '+'

```
LOGICAL I/O COMMAND #11 (+/-COMMAND#)
IF  ACTIVE PHASE        #7  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #41 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW OFF DURING PHASE 7 (HEAD 71).

PRESS '+'

```
LOGICAL I/O COMMAND #12 (+/-COMMAND#)
IF  YELLOW ON PHASE     #7  IS ON
        SCROLL DOWN
THEN:
SET OUTPUT  ASSIGNMENT #40 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 7 (HEAD 71).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS:|XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:    (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```
← NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS:|  XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:    (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```
← NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS:|    XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:    (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```
← NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE:          |12345678910111213141516
VEH OVL PARENTS:|      XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:    (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```
← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green
OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green
OUTPUT 47 = Overlap B Red
OUTPUT 48 = Overlap B Yellow
OUTPUT 49 = Overlap B Green
OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 03-0824
DESIGNED: March 2017
SEALED: 5-12-17 (Revision 1)
REVISED: N/A

Electrical Detail - Sheet 2 of 2

REVISION 1 SEAL

NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 036880 KEITH M. MIMS

DocuSigned by: Keith M. Mims 2F80786E8CD34A6... SIGNATURE 5/22/2017 DATE

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of: Transportation Mobility and Safety Division, State of North Carolina Department of Transportation, Signals Management Section

750 N.Greenfield Pkwy,Garner,NC 27529

NC 904/NC 179 - 904 (Seaside Rd)
at
NC 179/SR 1163 (Old Georgetown Rd)

Division 3 | Brunswick County | Sunset Beach

PLAN DATE:	June 2009	REVIEWED BY:	R. Hinshaw
PREPARED BY:	K. Moore	REVIEWED BY:	

REVISIONS	INIT.	DATE
1 Changed head 42 to a 3-section head, added Note 8 and eliminated loop 4C. (JP)	KMM	5/22/2017

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

Not a certified document as to the Original Document but Only as to the Revisions - This document originally issued and sealed by F. Royal Hinshaw, PE #032117, on 7-27-09. This document is only certified as to the revisions.

DATE

SIG. INVENTORY NO. 03-0824