ECONOLITE ASC/3-2070 OVERLAP PROGRAMMING DETAIL

(program controller as shown)

- 1. From Main Menu select | 2. CONTROLLER
- 2. From CONTROLLER Submenu select | 2. VEHICLE OVERLAPS |

OVERLAP A

Select TMG VEH OVLP [A] and 'PPLT FYA'

TMG VEH OVLP[A]	TYPE:PPLT	FYA
PROTECTED LEFT TURN. OPPOSING THROUGH	- -	
FLASHING ARROW OUTPU DELAY START OF: FYA. ACTION PLAN SF BIT D	.0.0 CLEARANCE	0.0
	Toggl	e Once

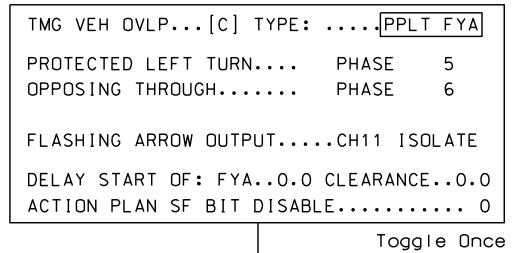
OVERLAP B

Select TMG VEH OVLP [B] and 'PPLT FYA'

TMG VEH OVLP[B]	TYPE:PPLT FYA	
PROTECTED LEFT TURN. OPPOSING THROUGH		
FLASHING ARROW OUTPU	JTCH10 ISOLATE	
DELAY START OF: FYAO.O CLEARANCEO.O ACTION PLAN SF BIT DISABLE		
	Toggle Once	

OVERLAP C

Select TMG VEH OVLP [C] and 'PPLT FYA'



Select TMG VEH OVLP $[\,D\,]$ and $'\,PPLT\,FYA'$

OVERLAP D

TMG VEH OVLP...[D] TYPE: PPLT FYA PROTECTED LEFT TURN.... PHASE 7 OPPOSING THROUGH..... PHASE 8 FLASHING ARROW OUTPUT....CH12 ISOLATE DELAY START OF: FYA..O.O CLEARANCE..O.O ACTION PLAN SF BIT DISABLE..... 0

END PROGRAMMING

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO ENSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-1733T1 DESIGNED: August 2017 SEALED: 9/25/2017 REVISED: N/A

