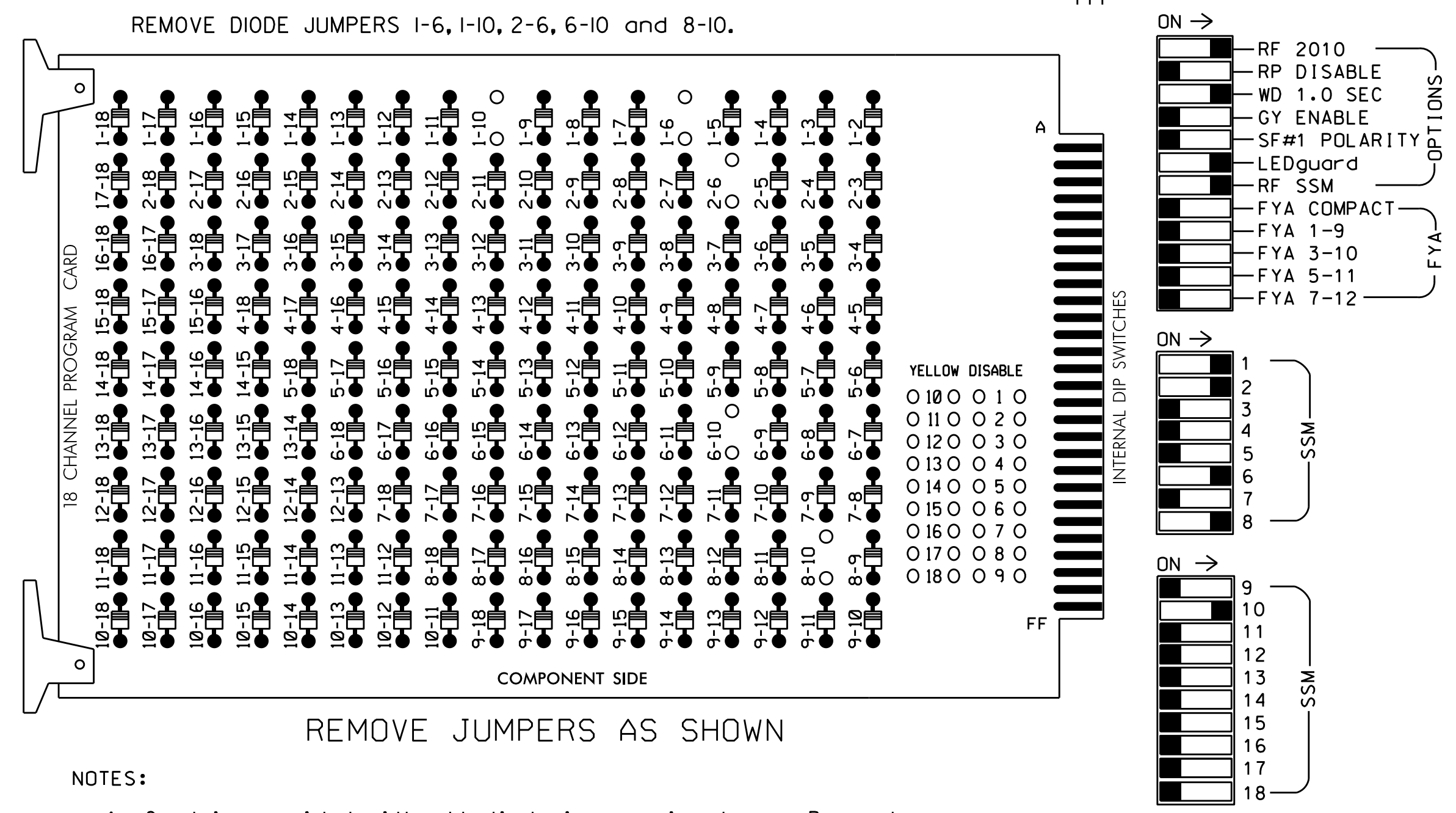


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash, and overlap 2 as Wag Overlaps.
- The cabinet and controller are part of the US 158 (Reidsville Road) CLS.

EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 (12-STD, 6-AUX)
 LOAD SWITCHES USED.....S1,S2,S8,S11,AUX S2
 PHASES USED.....1,2,6,8
 OVERLAP A.....NOT USED
 OVERLAP B.....1+8
 OVERLAP C.....NOT USED
 OVERLAP D.....NOT USED

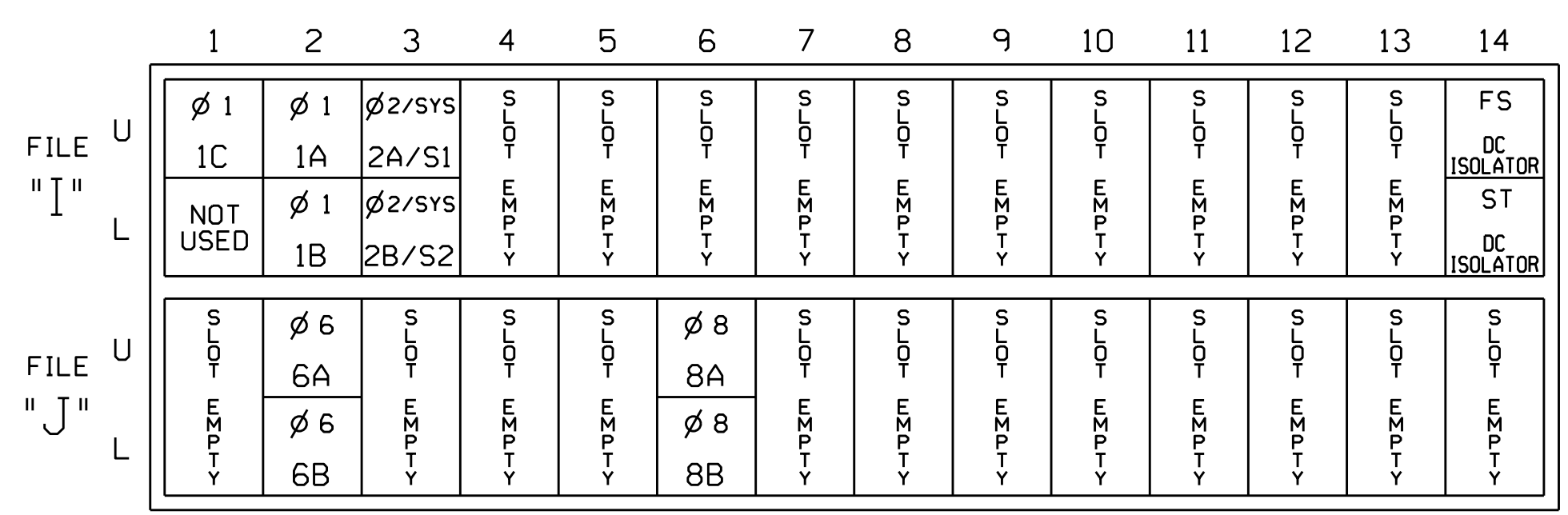
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11,12	21,22	NU	NU	NU	NU	NU	61,62	NU	NU	81,82	NU	NU	13	NU	NU	NU	NU
RED		128						134					A124					
YELLOW		129						135										
GREEN		130						136										
RED ARROW	125										107							
YELLOW ARROW	126										108		A125					
GREEN ARROW	127										109		A126					

NU = Not Used

INPUT FILE POSITION LAYOUT

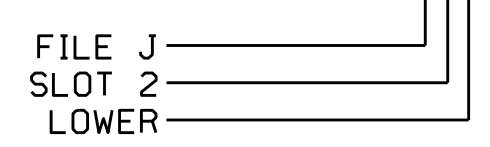
(front view)



INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-5,6	I2U	39	1	2	1	Y	Y			
1B	TB2-7,8	I2L	43	5	12	1	Y	Y			
1C	TB2-1,2	I1U	56	18	1	1	Y	Y			15
2A/S1	TB2-9,10	I3U	63	25	32	2/SYS	Y	Y			
2B/S2	TB2-11,12	I3L	76	38	42	2/SYS	Y	Y			
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			

INPUT FILE POSITION LEGEND: J2L



OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS
 PHASE: :12345678910111213141516
 VEH OVL PARENTS: X X
 VEH OVL NOT VEH: :
 VEH OVL NOT PED: :
 VEH OVL GRN EXT: :
 STARTUP COLOR: - RED - YELLOW - GREEN
 FLASH COLORS: - RED - YELLOW - GREEN
 SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
 FLASH YELLOW IN CONTROLLER FLASH?...N
 GREEN EXTENSION (0-255 SEC)...0
 YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
 OUTPUT AS PHASE # (0=NONE, 1-16)...0

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
- ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

Electrical Detail

Electrical and Programming Details For:

Prepared In the Offices of:

750 N. Greenfield Pkwy, Garner, NC 27529

US 158 (Reidsville Road)
at
I-74 EB Ramps

Division 9 Forsyth County Winston-Salem
 PLAN DATE: May 2017 REVIEWED BY: T. Joyce
 PREPARED BY: C. Strickland REVIEWED BY:

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0510
 DESIGNED: April 2017
 SEALED: 5/16/2017
 REVISED:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

CARY M. LITTLE
ENGINEER
030530

DocuSigned by:
Cary M. Little 5/23/2017
DATE

SIG. INVENTORY NO. 09-0510

23-May-2017 11:59
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