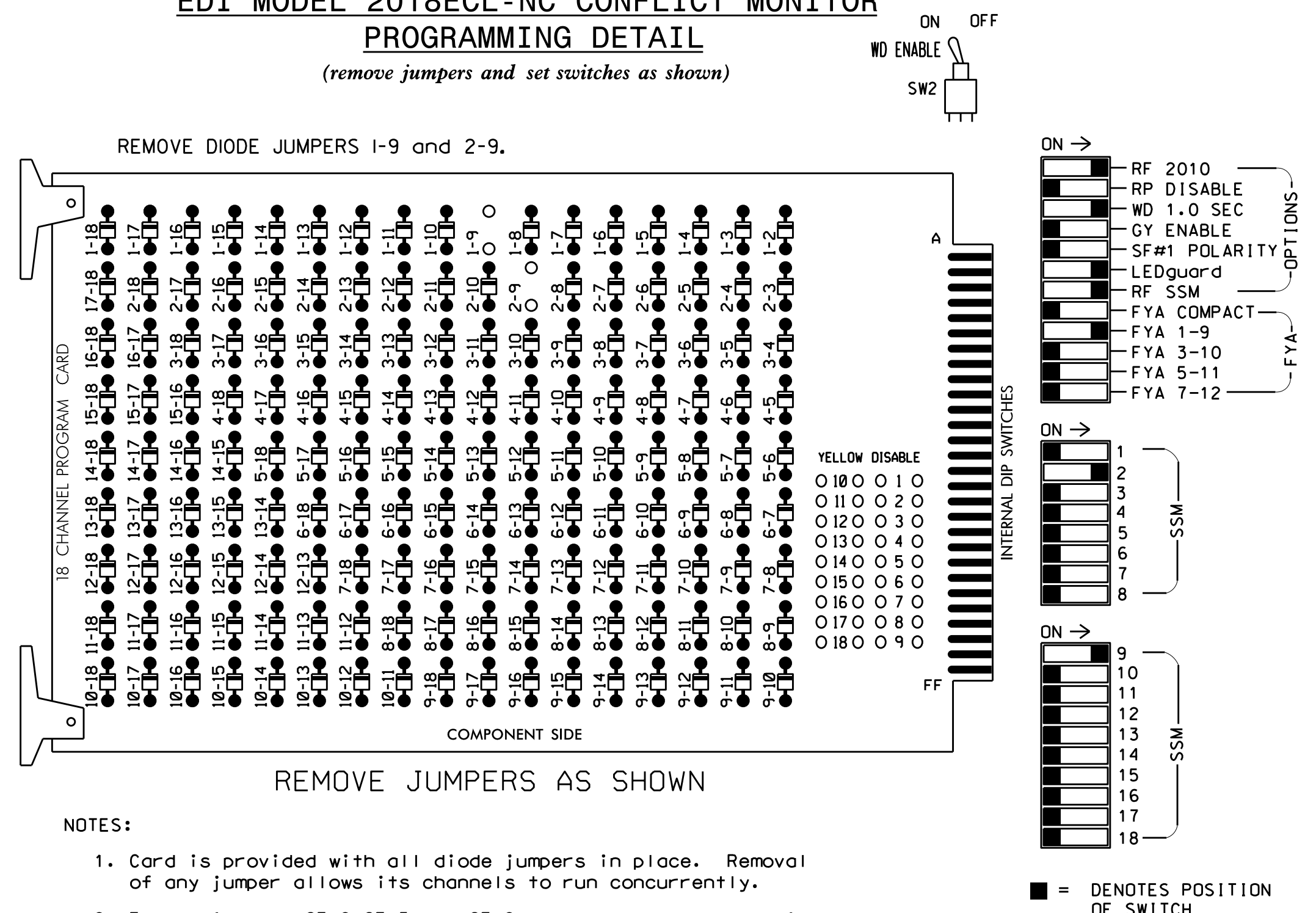


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all Phases.
- Program phase 2 for Variable Initial and Gap Reduction.
- Program phase 2 for Startup In Green.
- Program phase 2 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the US 311 (New Walkertown Road) Closed Loop System.

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	21,22	NU	NU	NU	NU	NU	NU	NU	NU	NU	NU	11	NU	NU	NU	NU	NU
RED		128																
YELLOW	*	129																
GREEN		130																
RED ARROW													A121					
YELLOW ARROW													A122					
FLASHING YELLOW ARROW													A123					
GREEN ARROW	127																	

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 * See pictorial of head wiring in detail this sheet.

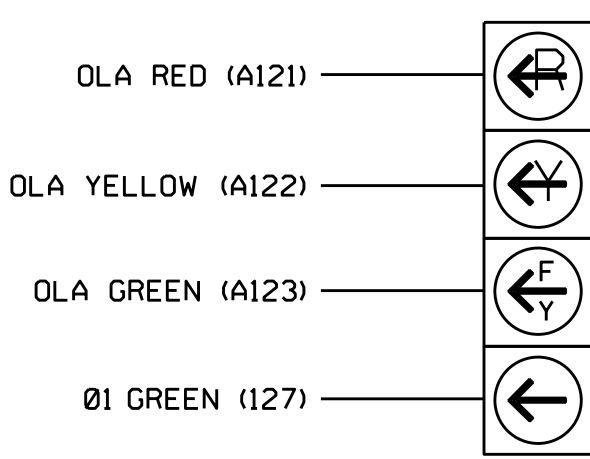
EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S1,S2,AUX S1
 PHASES USED.....1,2,9*
 OVERLAP "A".....1+2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

* Phase used for timing only

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal head as shown)



NOTE
 The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 2/SYS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	FS
L	NOT USED	NOT USED	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	DC ISOLATOR
U	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS	-OFS
L	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	←-VZMF	DC ISOLATOR

EX. : 1A, 2A, ETC. = LOOP NO.'S
 FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A	TB2-1,2	11U	56	18	1	1	Y	Y			15
2A/S1	TB2-5,6	12U	39	1	2	2/SYS	Y	Y			

* See Input Page Assignment programming detail on sheet 3.
 INPUT FILE POSITION LEGEND: J2L
 FILE J
 SLOT 2
 LOWER

PHASE SEQUENCE PROGRAMMING DETAIL

(program controller as shown below)

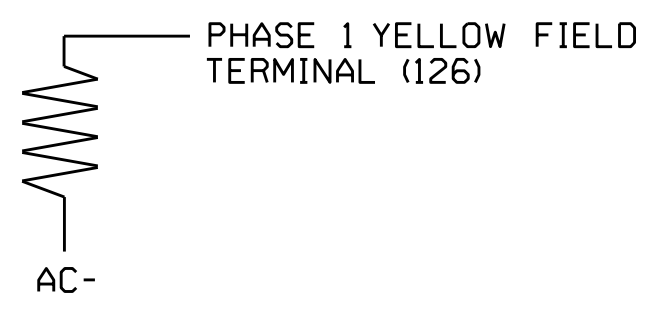
FROM OASIS LOCAL CONTROLLER MAIN MENU
 SELECT: 4 PHASE SEQUENCE

PHASE	RNG	LEAD	BARRIER 1	X-LAG	LEAD	BARRIER 2	X-LAG
1	1	2	0	0	9	0	0
2	0	0	0	0	0	0	0
3	0	0	0	0	0	0	0
4	0	0	0	0	0	0	0

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 09-0521
 DESIGNED: April 2017
 SEALED: 5/16/2017
 REVISED:

Electrical Detail - Sheet 1 of 4

US 311 (New Walkertown Road) at I-74 EB Ramps

Division 9 Forsyth County Walkertown

PLAN DATE: May 2017 REVIEWED BY: T. Joyce

PREPARED BY: C. Strickland REVIEWED BY:

750 N. Greenfield Pkwy, Garner, NC 27529

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL
 NORTH CAROLINA PROFESSIONAL ENGINEER
 SEAL 030530
 JACOBARY M. LITTLE

DocuSigned by:
 Jacobary M. Little 5/23/2017
 0021EFD04F5341F DATE

SIG. INVENTORY NO. 09-0521

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