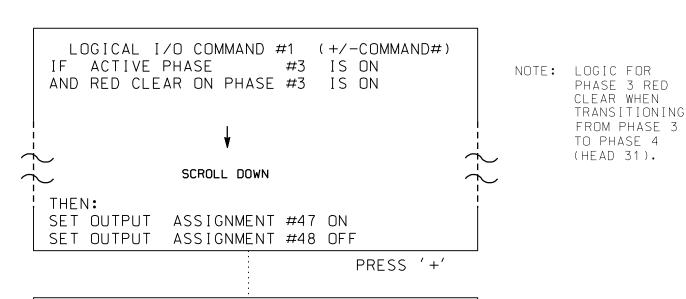
PROJECT REFERENCE NO. SHEET NO. U-5935

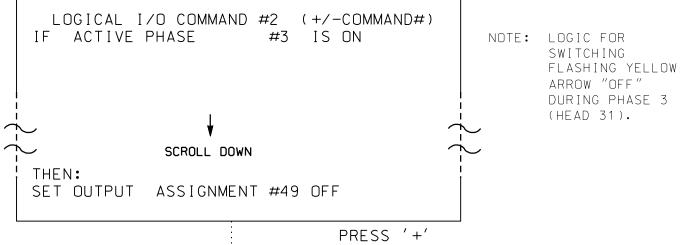
SIG-7.2

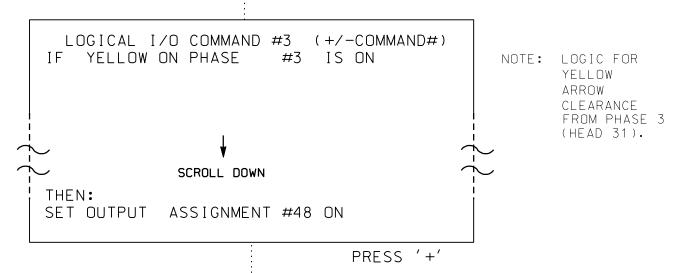
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

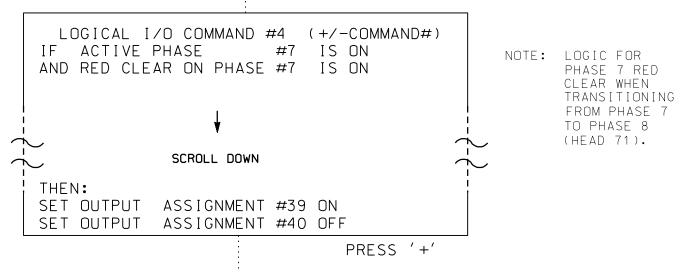
(program controller as shown below)

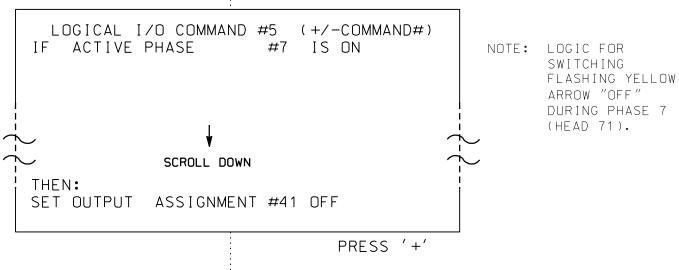
- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

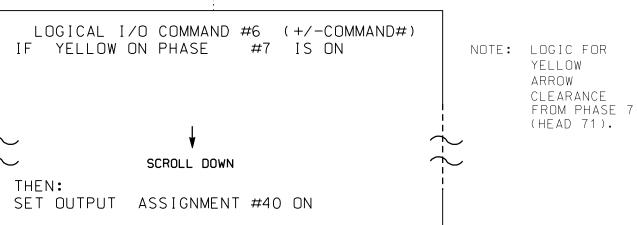












LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0161 DESIGNED: June 2017 SEALED: 08/04/2017 REVISED: N/A

OUTPUT REFERENCE SCHEDULE USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 39 = Overlap D Red

OUTPUT 40 = Overlap D Yellow

OUTPUT 41 = Overlap D Green

OUTPUT 48 = Overlap B Yellow

OUTPUT 49 = Overlap B Green

OUTPUT 47 = Overlap B Red

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

PRESS '+'

PAGE 1: VEHICLE OVERLAP 'B' SETTINGS PHASE: ¦12345678910111213141516 VEH OVL PARENTS: | XX VEH OVL NOT VEH: | VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN **←** NOTICE FLASH COLORS: _ RED _ YELLOW X GREEN GREEN FLASH SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)...... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

PRESS '+' TWICE

PAGE 1: VEHICLE OVERLAP 'D' SETTINGS \\ 12345678910111213141516 PHASE: VEH OVL PARENTS: | VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN **←** NOTICE SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH FLASH YELLOW IN CONTROLLER FLASH?...N GREEN EXTENSION (0-255 SEC)..... YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

- 1. ON REAR OF PDA REMOVE WIRE FROM TERM, T2-4 AND TERMINATE ON T2-2.
- 2. ON REAR OF PDA REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
- 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

Electrical Detail - Sheet 2 of 2 - Signal Upgrade

ELECTRICAL AND PROGRAMMING DETAILS FOR: US 301 (Ward Boulevard) Prepared in the Offices of: Division 4

NC 58 (Lipscomb Road) PLAN DATE:

June 2017 REVIEWED BY: W M Ruhsam PREPARED BY: K M Cory REVIEWED BY: REVISIONS INIT. DATE

SEAL 038970

Kelly M Cory

DOCUMENT NOT CONSIDERED

FINAL UNLESS ALL

SIGNATURES COMPLETED

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SIG. INVENTORY NO. 04-0161