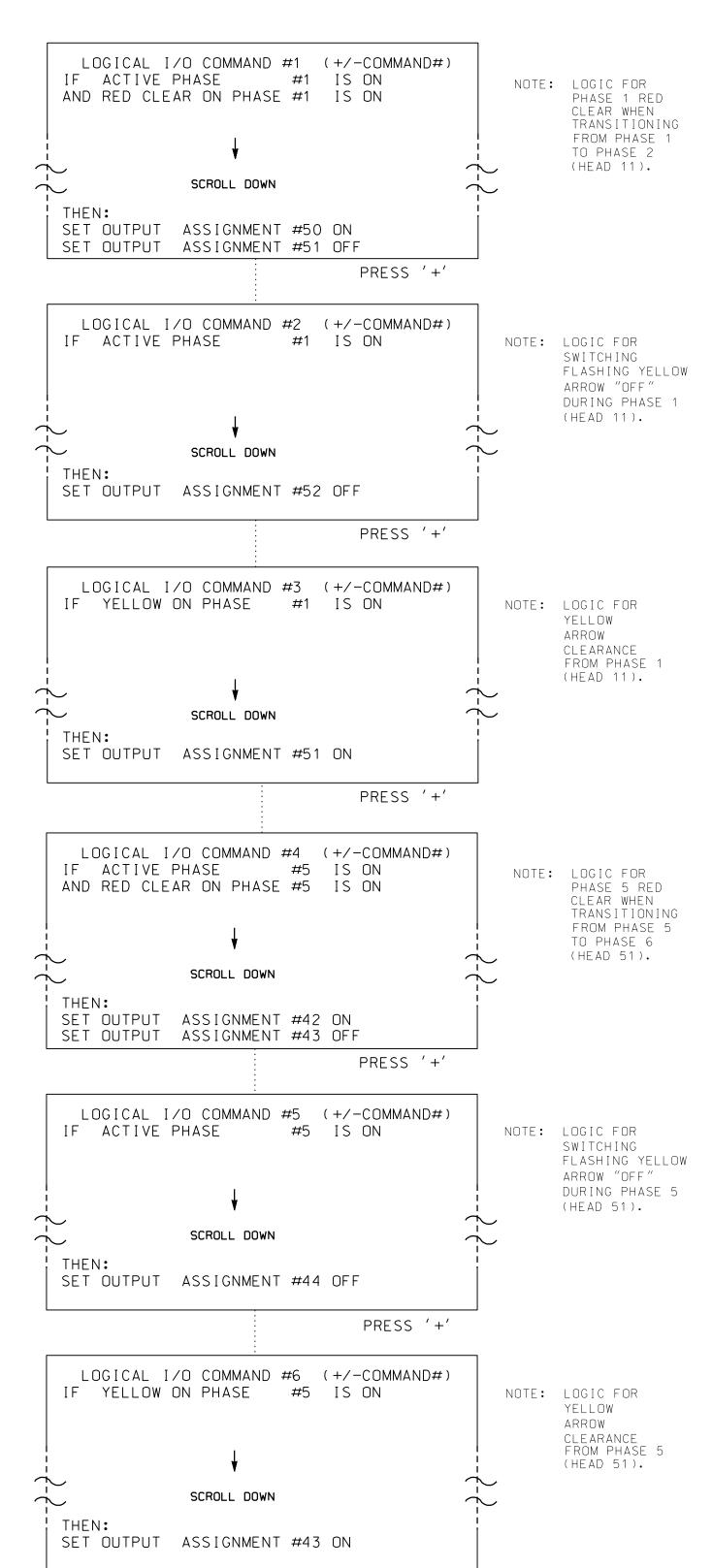
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OUTPUT REFERENCE SCHEDULE

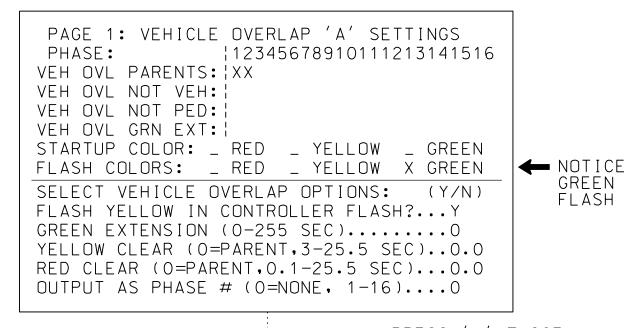
USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).



PRESS '+' TWICE

<u> </u>	
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS PHASE: 12345678910111213141516 VEH OVL PARENTS: XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: _ RED _ YELLOW _ GREEN FLASH COLORS: _ RED _ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?Y GREEN EXTENSION (0-255 SEC)0 YELLOW CLEAR (0=PARENT, 3-25.5 SEC)0.0 OUTPUT AS PHASE # (0=NONE, 1-16)0	← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: Ø4-Ø454 DESIGNED: June 2017 SEALED: 08/04/2017 REVISED: N/A

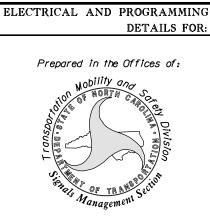
Electrical Detail - Sheet 2 of 2 - Signal Upgrade

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL



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US 301 (Ward Boulevard) Lane Street

Division 4 REVIEWED BY: W M Ruhsam PLAN DATE: June 2017 PREPARED BY: K M Corv REVIEWED BY:

038970 INIT. DATE

Kelly M Cory 8/4/2017

750 N.Greenfield Pkwy, Garner, NC 27529

REVISIONS

SIG. INVENTORY NO. 04-0454