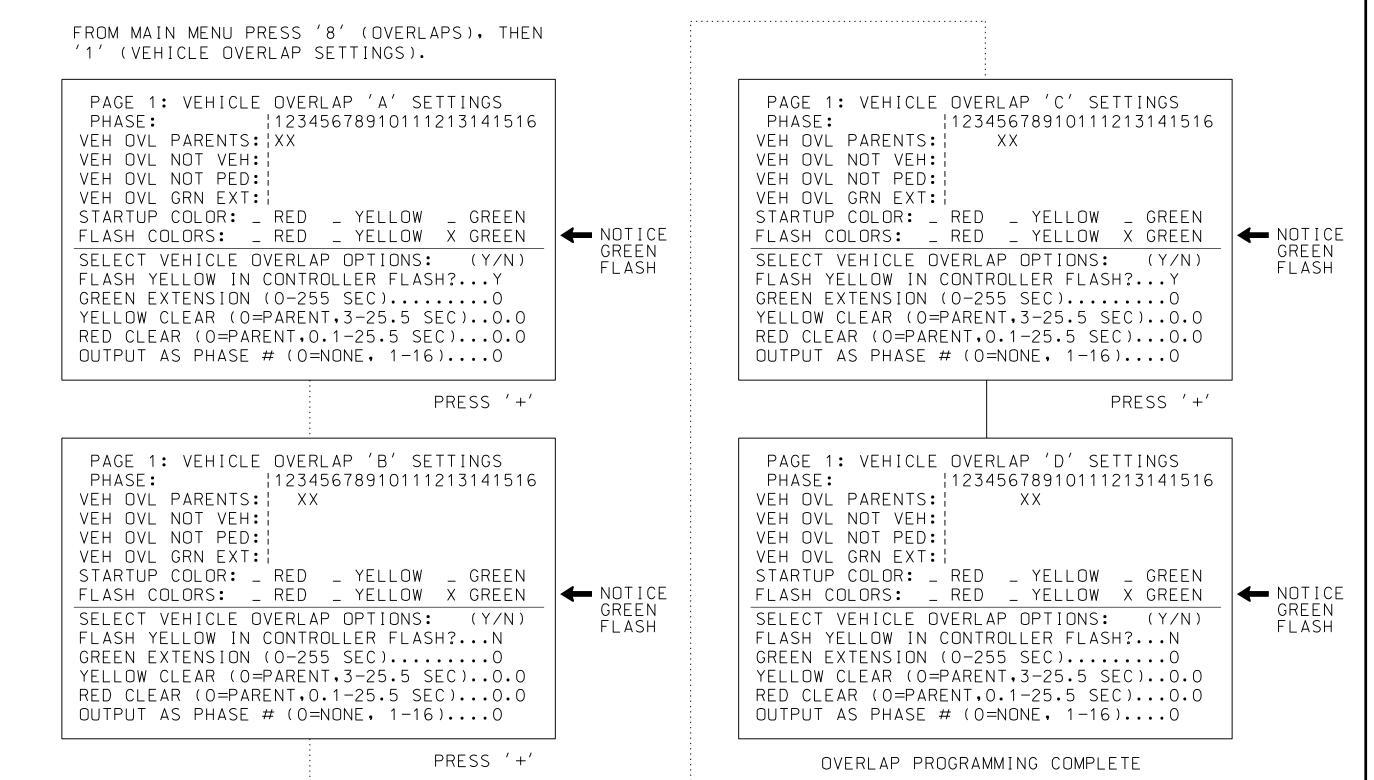
(program controller as shown below)



LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE (program controller as shown below) 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11 AND 12. 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR). LOGICAL I/O COMMAND #7 (+/-COMMAND#) LOGICAL I/O COMMAND #1 (+/-COMMAND#) IF ACTIVE PHASE IF ACTIVE PHASE #1 IS ON #3 IS ON NOTE: LOGIC FOR NOTE: LOGIC FOR AND RED CLEAR ON PHASE #3 IS ON AND RED CLEAR ON PHASE #1 IS ON PHASE 1 RED PHASE 3 RED CLEAR WHEN CLEAR WHEN TRANSITIONING TRANSITIONING FROM PHASE 1 FROM PHASE 3 TO PHASE 2 TO PHASE 4 (HEAD 11). (HEAD 31). SCROLL DOWN SCROLL DOWN THEN: THEN: SET OUTPUT ASSIGNMENT #47 ON SET OUTPUT ASSIGNMENT #50 ON SET OUTPUT ASSIGNMENT #51 OFF SET OUTPUT ASSIGNMENT #48 OFF PRESS '+' PRESS '+' LOGICAL I/O COMMAND #2 (+/-COMMAND#) LOGICAL I/O COMMAND #8 (+/-COMMAND#) IF ACTIVE PHASE #3 IS ON IF ACTIVE PHASE #1 IS ON NOTE: LOGIC FOR NOTE: LOGIC FOR SWITCHING SWITCHING FLASHING YELLOW FLASHING YELLOW ARROW "OFF" ARROW "OFF" DURING PHASE 1 DURING PHASE 3 (HEAD 11). (HEAD 31).

NOTE: LOGIC FOR

YELLOW

ARROW

NOTE: LOGIC FOR

NOTE: LOGIC FOR

NOTE: LOGIC FOR

YELLOW

ARROW

CLEARANCE FROM PHASE 5

(HEAD 51).

SWITCHING

ARROW "OFF"

(HEAD 51).

FLASHING YELLOW

DURING PHASE 5

PHASE 5 RED

TRANSITIONING

FROM PHASE 5

CLEAR WHEN

TO PHASE 6

(HEAD 51).

CLEARANCE

FROM PHASE 1

(HEAD 11).

SCROLL DOWN

LOGICAL I/O COMMAND #3 (+/-COMMAND#)

LOGICAL I/O COMMAND #4 (+/-COMMAND#)

LOGICAL I/O COMMAND #5 (+/-COMMAND#)

LOGICAL I/O COMMAND #6 (+/-COMMAND#)

#5 IS ON

#5 IS ON

PRESS '+'

PRESS '+'

PRESS '+'

PRESS '+'

PRESS '+'

SET OUTPUT ASSIGNMENT #52 OFF

IF YELLOW ON PHASE #1 IS ON

SCROLL DOWN

SET OUTPUT ASSIGNMENT #51 ON

AND RED CLEAR ON PHASE #5 IS ON

SCROLL DOWN

SCROLL DOWN

SET OUTPUT ASSIGNMENT #44 OFF

IF YELLOW ON PHASE #5 IS ON

SCROLL DOWN

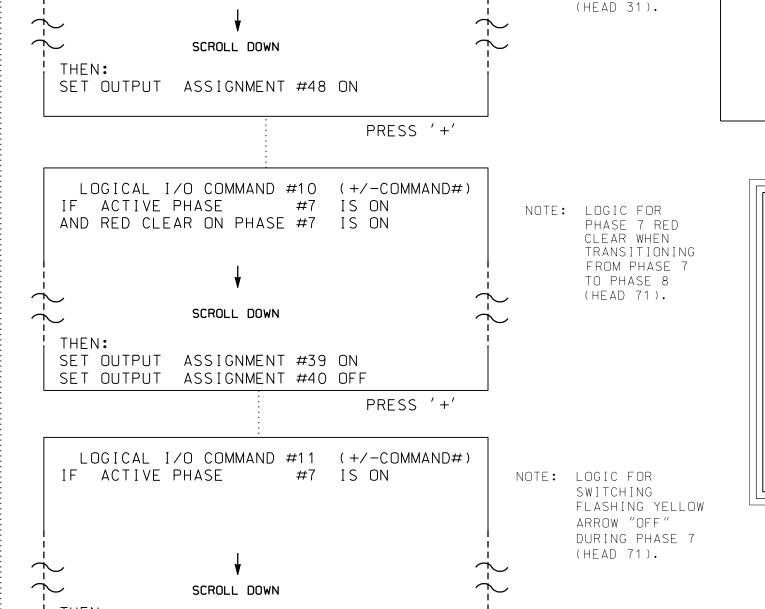
SET OUTPUT ASSIGNMENT #43 ON

SET OUTPUT ASSIGNMENT #42 ON

SET OUTPUT ASSIGNMENT #43 OFF

IF ACTIVE PHASE

IF ACTIVE PHASE



PRESS '+'

PRESS '+'

NOTE: LOGIC FOR

NOTE: LOGIC FOR

YELLOW

CLEARANCE

FROM PHASE 7

(HEAD 71).

ARROW

YELLOW

CLEARANCE

FROM PHASE 3

ARROW

SCROLL DOWN

LOGICAL I/O COMMAND #9 (+/-COMMAND#)

SET OUTPUT ASSIGNMENT #49 OFF

IF YELLOW ON PHASE #3 IS ON

SET OUTPUT ASSIGNMENT #41 OFF

IF YELLOW ON PHASE #7 IS ON

SCROLL DOWN

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

SET OUTPUT ASSIGNMENT #40 ON

LOGICAL I/O COMMAND #12 (+/-COMMAND#)

OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR OUTPUT 39 = Overlap D Red OUTPUT 40 = Overlap D Yellow OUTPUT 41 = Overlap D Green OUTPUT 42 = Overlap C Red OUTPUT 43 = Overlap C Yellow OUTPUT 44 = Overlap C Green OUTPUT 47 = Overlap B Red OUTPUT 48 = Overlap B Yellow OUTPUT 49 = Overlap B Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 04-0319 DESIGNED: June 2017 SEALED: 08/04/2017 REVISED: N/A

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM, T2-4 AND TERMINATE ON T2-2.

2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3. 3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

Electrical Detail - Sheet 2 of 2 - Signal Upgrade

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED SEAL

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ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of: 750 N.Greenfield Pkwy, Garner, NC 27529

US 301 (Ward Boulevard) SR 1606 (Black Creek Road)

Division 4 June 2017 REVIEWED BY: W M Ruhsam PLAN DATE: PREPARED BY: K M Cory REVIEWED BY: REVISIONS INIT. DATE

SEAL 038970 Kelly M Cory

8/4/2017 SIG. INVENTORY NO. 04-0319