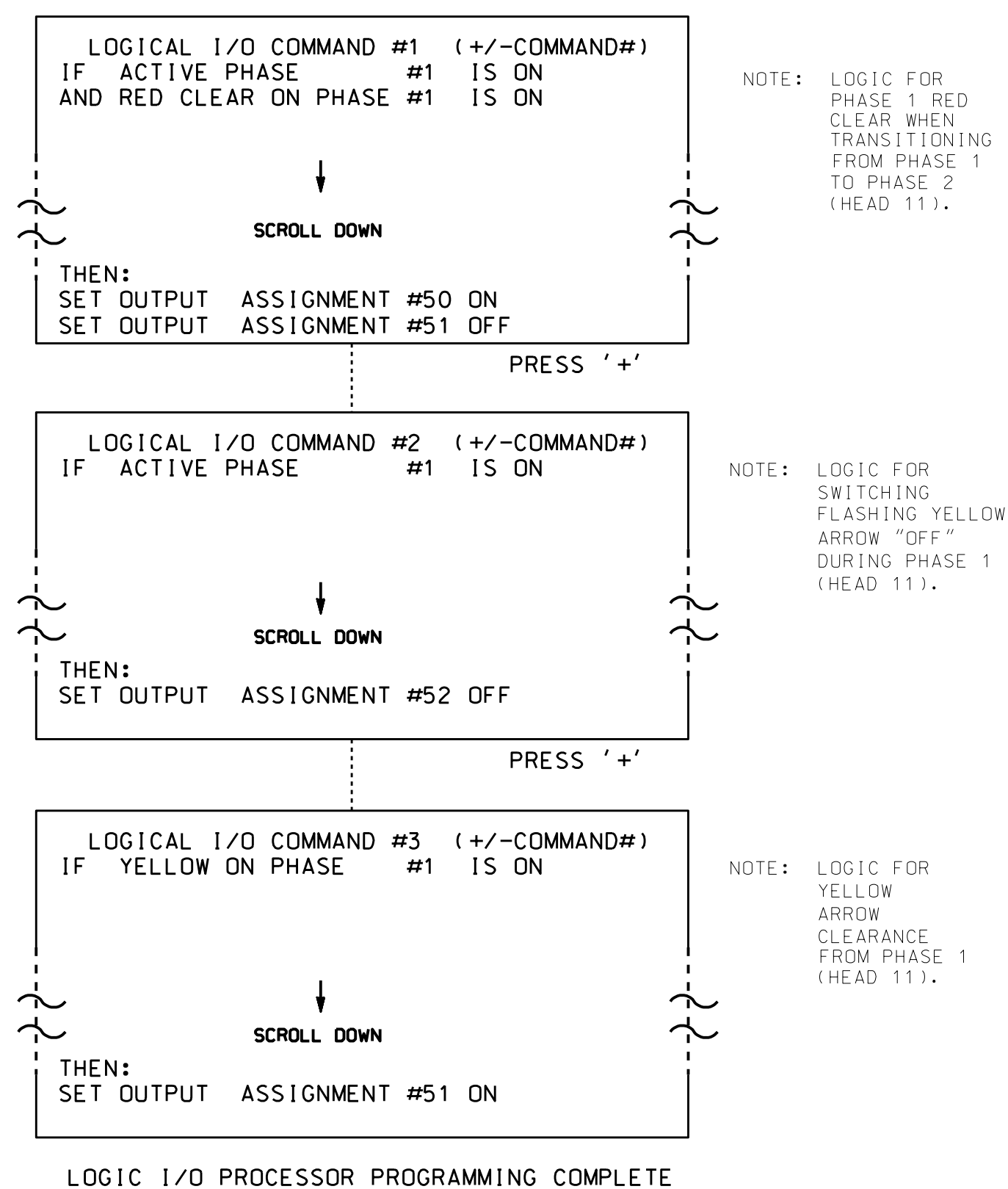


### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



#### OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red  
OUTPUT 51 = Overlap A Yellow  
OUTPUT 52 = Overlap A Green

### OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```

PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      :12345678910111213141516
VEH OVL PARENTS: :XX
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW X GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

### OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING - PAGE 2

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

→ NOTICE PAGE 2

```

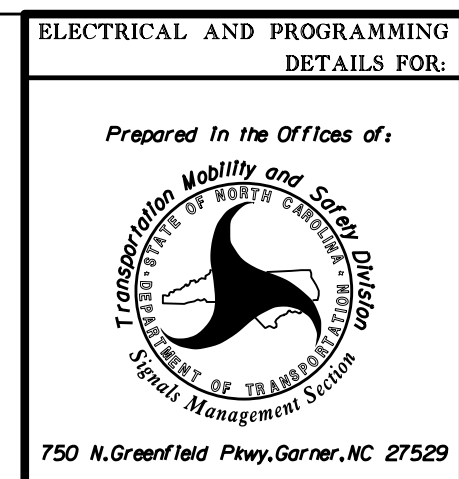
PAGE 2: VEHICLE OVERLAP 'A' SETTINGS
PHASE:      :12345678910111213141516
VEH OVL PARENTS: :X
VEH OVL NOT VEH: :
VEH OVL NOT PED: :
VEH OVL GRN EXT: :
STARTUP COLOR: - RED - YELLOW - GREEN
FLASH COLORS:  - RED - YELLOW - GREEN
SELECT VEHICLE OVERLAP OPTIONS: (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC)...0.0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)...0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)...0
    
```

OVERLAP PROGRAMMING COMPLETE

18-MAY-2017 13:57  
 S:\MITS\0115\Sig\101\work\hgr\oups\Sig\_Maps\F11\ck\lanc\00521\_sml.e\_xxv.dgn  
 ceastf\ckland

Electrical Detail - Sheet 2 of 4

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 09-0521  
DESIGNED: April 2017  
SEALED: 5/16/2017  
REVISED:



|   |                            |   |            |
|---|----------------------------|---|------------|
| ELECTRICAL AND PROGRAMMING DETAILS FOR: |                            | US 311 (New Walkertown Road)<br>at<br>I-74 EB Ramps |            |
| Prepared In the Offices of:             | Division 9                 | Forsyth County                                      | Walkertown |
|   | PLAN DATE: May 2017        | REVIEWED BY: T. Joyce                               |            |
|   | PREPARED BY: C. Strickland | REVIEWED BY:  |            |
| REVISIONS                               | INIT.                      | DATE  |            |
|   |                            |   |            |

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

DocuSigned by:  
Zachary M. Little  
5/23/2017  
001EFD4F5341F  
DATE

SIG. INVENTORY NO. 09-0521