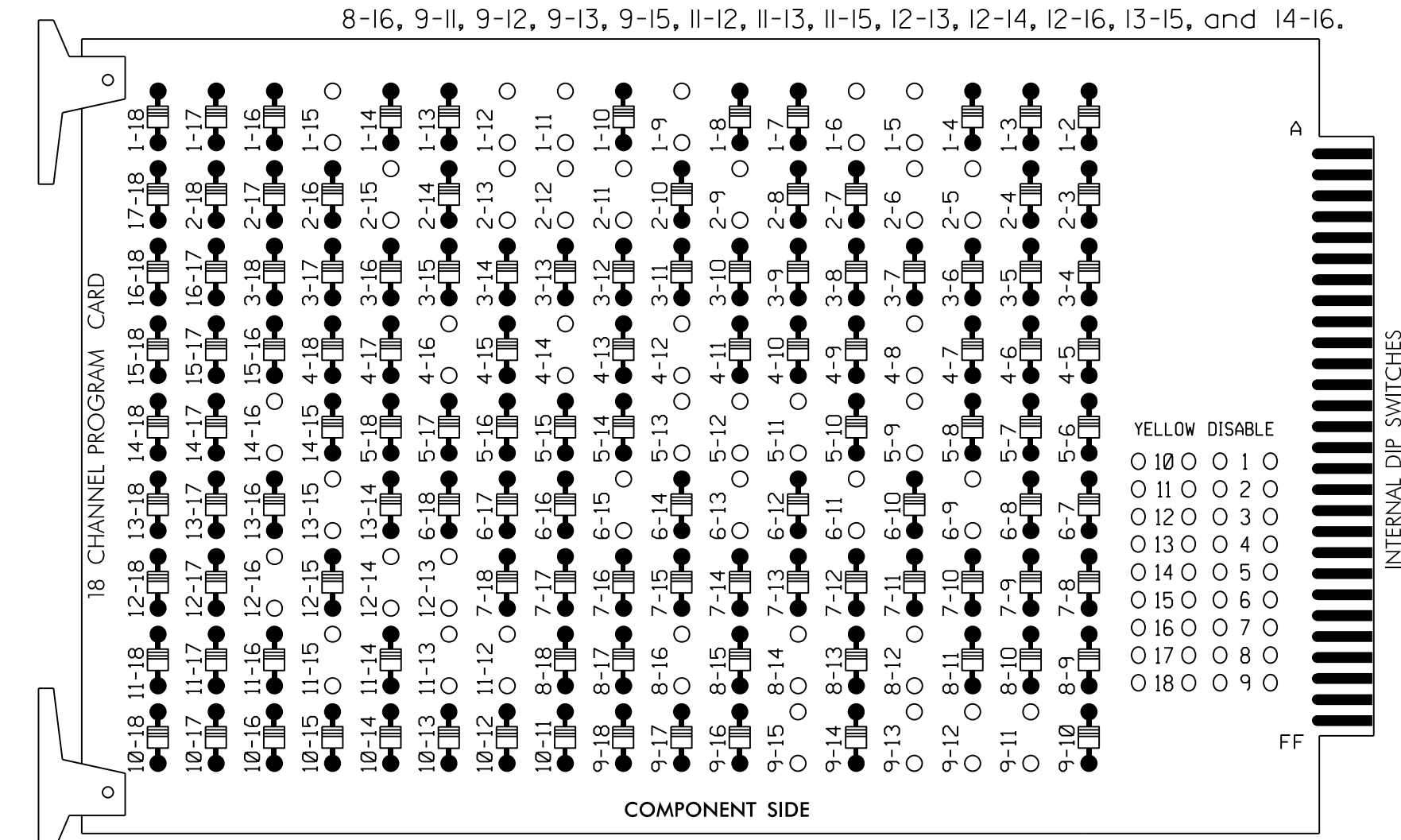


### EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-5, 1-6, 1-9, 1-11, 1-12, 1-15, 2-5, 2-6, 2-9, 2-11, 2-12, 2-13, 2-15, 4-8, 4-12, 4-14, 4-16, 5-9, 5-11, 5-12, 5-13, 6-9, 6-11, 6-13, 6-15, 8-12, 8-14, 8-16, 9-11, 9-12, 9-13, 9-15, 11-12, 11-13, 11-15, 12-13, 12-14, 12-16, 13-15, and 14-16.



REMOVE JUMPERS AS SHOWN

**NOTES:**

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

■ = DENOTES POSITION OF SWITCH

### NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, 4, 6 and 8 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the NC 273 (Highland Street) Closed Loop System.

### EQUIPMENT INFORMATION

CONTROLLER.....2070  
 CABINET.....332 /W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S5,S6,S7,S8,S9,S11,S12,  
 AUX S1,AUX S4,AUX S5.  
 PHASES USED.....1,2,4,5,6,8.  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....5+6  
 OVERLAP "D".....4+5

PROJECT REFERENCE NO.	SHEET NO.
U-3633	SIG-24

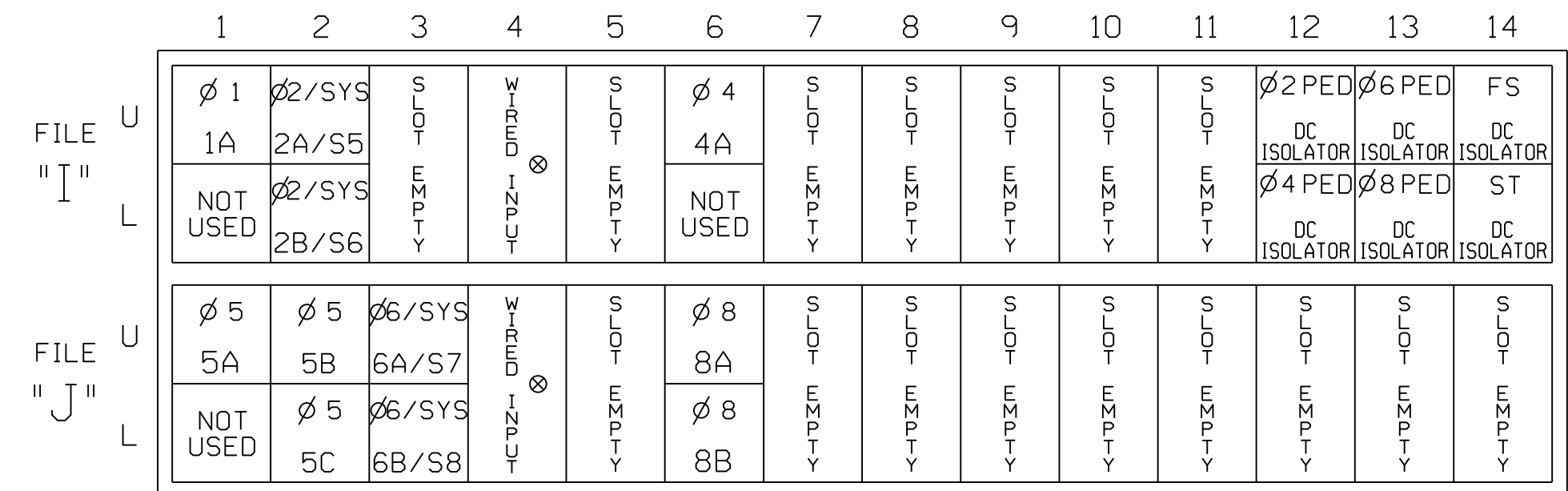
### SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6				
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18				
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE				
SIGNAL HEAD NO.	11★	21,22	P21 P22	NU	41,42	P41 P42	51★	61,62	P61 P62	NU	81,82	P81 P82	11★	NU	NU	51★	43,44★	NU				
RED		128			101			134			107							A101				
YELLOW	*	129			102		*	135			108											
GREEN		130			103			136			109											
RED ARROW																		A121	A114			
YELLOW ARROW																			A122	A115	A102	
FLASHING YELLOW ARROW																			A123		A116	A103
GREEN ARROW	127							133														
Hand				113			104			119			110									
Walking				115			106			121			112									

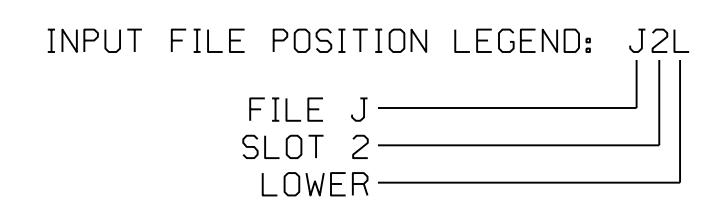
NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.  
 ★ See pictorial of head wiring in detail below.

### INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S  
 FS = FLASH SENSE  
 ST = STOP TIME



### INPUT FILE CONNECTION & PROGRAMMING CHART

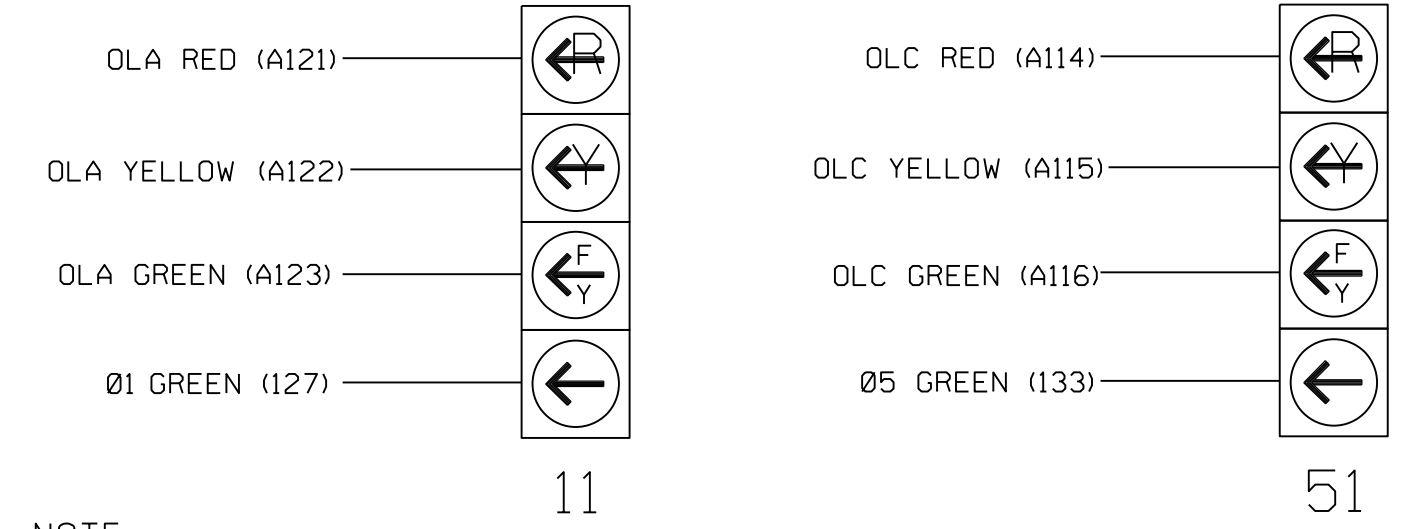
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
	-	J4U	48	10★	26	6	Y	Y	Y		3
	-	I1U	56	18★	51	1	Y	Y			
2A/S5	TB2-5,6	I2U	39	1	2	2/SYS	Y	Y			
2B/S6	TB2-7,8	I2L	43	5	12	2/SYS	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			
5A <sup>2</sup>	TB3-1,2	J1U	55	17	5	5	Y	Y			15
	-	I4U	47	9★	22	2	Y	Y	Y		3
5B	TB3-5,6	J2U	40	2	6	5	Y	Y			15
5C	TB3-7,8	J2L	44	6	16	5	Y	Y			15
6A/S7	TB3-9,10	J3U	64	26	36	6/SYS	Y	Y			
6B/S8	TB3-11,12	J3L	77	39	46	6/SYS	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P41,P42	TB8-5,6	I12L	69	31	PED 4	4 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:  
 INSTALL DC ISOLATORS IN INPUT FILE SLOTS 112 AND 113.

<sup>1</sup>Add jumper from I1-W to J4-W on rear of input file  
<sup>2</sup>Add jumper from J1-W to I4-W on rear of input file  
 ★ See Input Page Assignment programming details on sheets 3 and 4.

### 4 SECTION FYA PPLT SIGNAL WIRING DETAIL

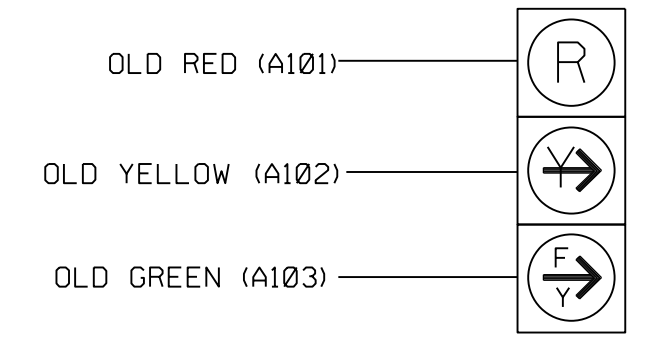
(wire signal heads as shown)



NOTE  
 1. The sequence display for signals #11 & #51 require special logic programming. See sheet 2 for programming instructions.

### SIGNAL WIRING DETAIL

(wire signal heads as shown)

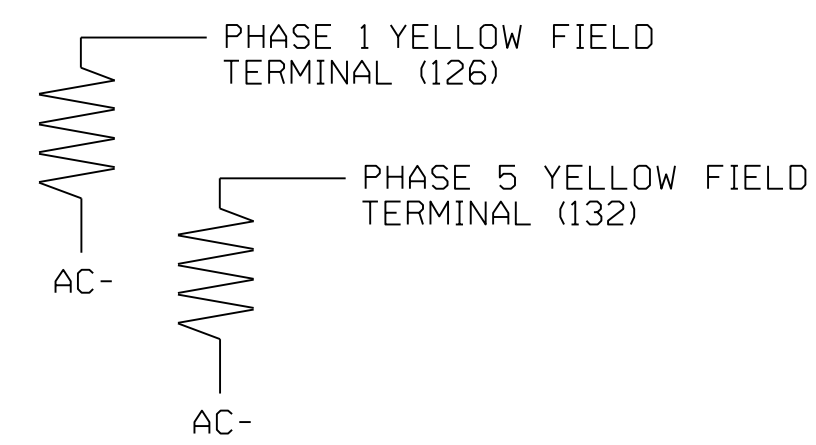


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### LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



### COUNTDOWN PEDESTRIAN SIGNAL OPERATION

Countdown Ped Signals are required to display timing only during Ped Clearance Interval. Consult Ped Signal Module user's manual for instructions on selecting this feature.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 12-0981  
 DESIGNED: July 2016  
 SEALED: 10/4/2016  
 REVISED:



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Signal Upgrade - Final Design  
 ELECTRICAL DETAIL SHEET 1 OF 5

**DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED**

NC 273 (Highland St/S. Main St) at S. Main St/ Shopping Center Entrance

Division 12 Gaston County Mount Holly

PLAN DATE: JULY 2016 REVIEWED BY: D. HARRIS  
 PREPARED BY: J. HAMBRIGHT REVIEWED BY: B. WATSON

750 N. Greenfield Pkwy, Garner, NC 27529

SEAL NORTH CAROLINA PROFESSIONAL ENGINEER SEAL 29449 BETTY L. WATSON

DocuSigned by: Betty L. Watson 10/4/2016

SIG. INVENTORY NO. 12-0981