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PROJECT REFERENCE NO.	SHEET NO.
U-3633	SIG-22

LOGICAL I/O PROCESSOR PROGRAMMING DETAIL
TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5, AND 6.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE       #1  IS ON
AND RED CLEAR ON PHASE #1  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #50 ON
SET OUTPUT  ASSIGNMENT #51 OFF
```

NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE       #1  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #52 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE    #1  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #51 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #4  (+/-COMMAND#)
IF  ACTIVE PHASE       #5  IS ON
AND RED CLEAR ON PHASE #5  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #42 ON
SET OUTPUT  ASSIGNMENT #43 OFF
```

NOTE: LOGIC FOR PHASE 5 RED CLEAR WHEN TRANSITIONING FROM PHASE 5 TO PHASE 6 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #5  (+/-COMMAND#)
IF  ACTIVE PHASE       #5  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #44 OFF
```

NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 5 (HEAD 51).

PRESS '+'

```
LOGICAL I/O COMMAND #6  (+/-COMMAND#)
IF  YELLOW ON PHASE    #5  IS ON

SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #43 ON
```

NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 5 (HEAD 51).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:          |123456789101112131415 16
VEH OVL PARENTS:|XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+' TWICE

```
PAGE 1: VEHICLE OVERLAP 'C' SETTINGS
PHASE:          |123456789101112131415 16
VEH OVL PARENTS:|   XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

PRESS '+'

```
PAGE 1: VEHICLE OVERLAP 'D' SETTINGS
PHASE:          |123456789101112131415 16
VEH OVL PARENTS:|   XX
VEH OVL NOT VEH:|
VEH OVL NOT PED:|
VEH OVL GRN EXT:|
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN
SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...N
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

← NOTICE GREEN FLASH

OVERLAP PROGRAMMING COMPLETE

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

OUTPUT REFERENCE SCHEDULE

USE TO INTERPRET LOGIC PROCESSOR

OUTPUT 42 = Overlap C Red
OUTPUT 43 = Overlap C Yellow
OUTPUT 44 = Overlap C Green

OUTPUT 50 = Overlap A Red
OUTPUT 51 = Overlap A Yellow
OUTPUT 52 = Overlap A Green

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 12-0981-T2
DESIGNED: July 2016
SEALED: 10/4/2016
REVISED:

Stantec
Stantec Consulting Services Inc.
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Signal Upgrade
Temporary Detail 2 - TMP Phases 2 & 3
ELECTRICAL DETAIL SHEET 2 OF 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

NC 273 (Highland St/S.Main St)
at
S.Main St/
Shopping Center Entrance

Division 12 Gaston County Mount Holly

PLAN DATE:	JULY 2016	REVIEWED BY:	D. HARRIS
PREPARED BY:	J. HAMBRIGHT	REVIEWED BY:	B. WATSON

REVISIONS	INIT.	DATE

Prepared for the Offices of:
Transportation Mobility and Safety Division
Signal Management Section
DEPARTMENT OF TRANSPORTATION STATE OF NORTH CAROLINA
750 N.Greenfield Pkwy,Garner,NC 27529

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 29449 ENGINEER BETSY L. WATSON

DocuSigned by: Betsy L. Watson 10/4/2016
USER SIGNATURE DATE

SIG. INVENTORY NO. 12-0981-T2

10/4/2016
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jhambright