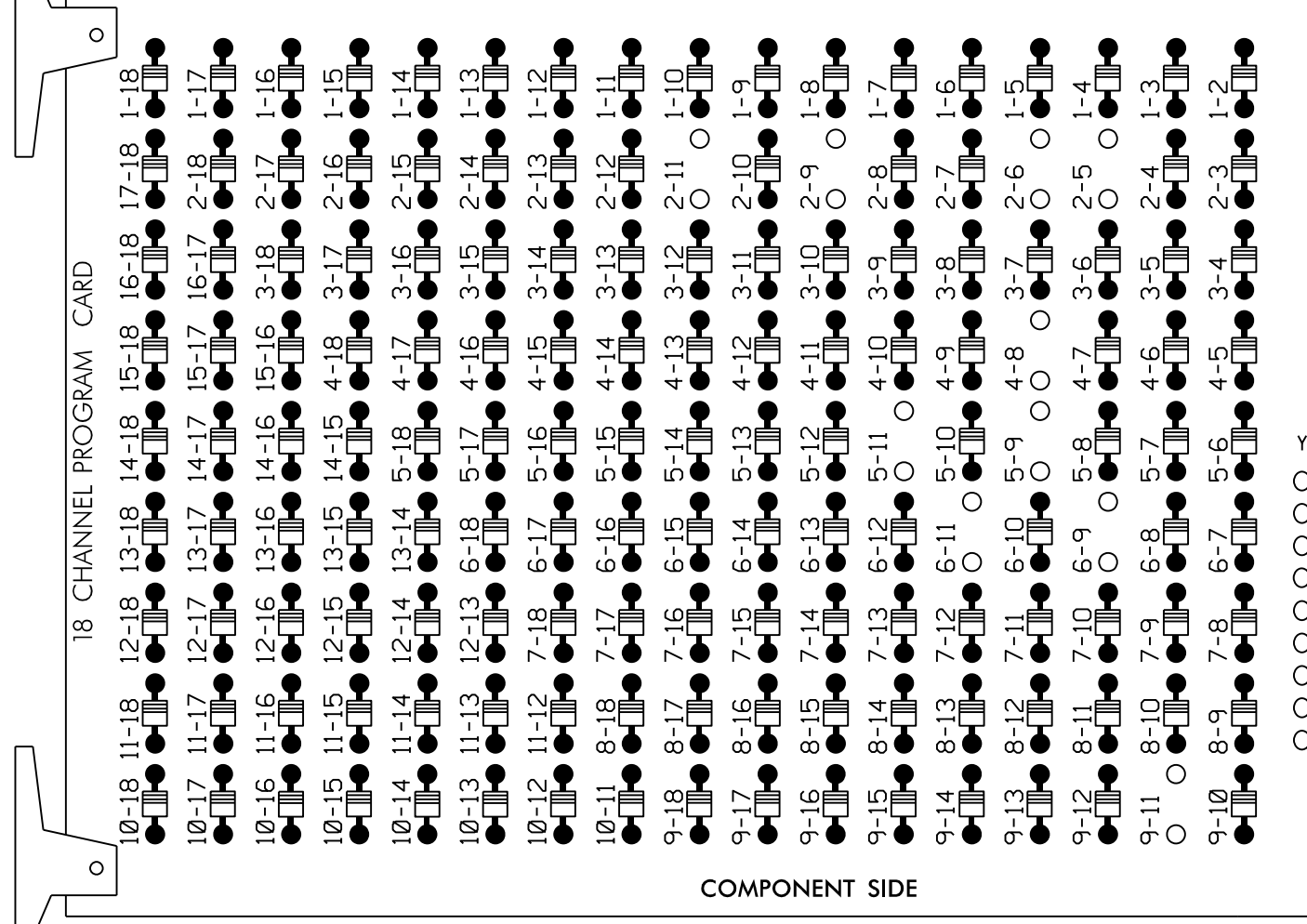


EDI MODEL 2018ECL-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

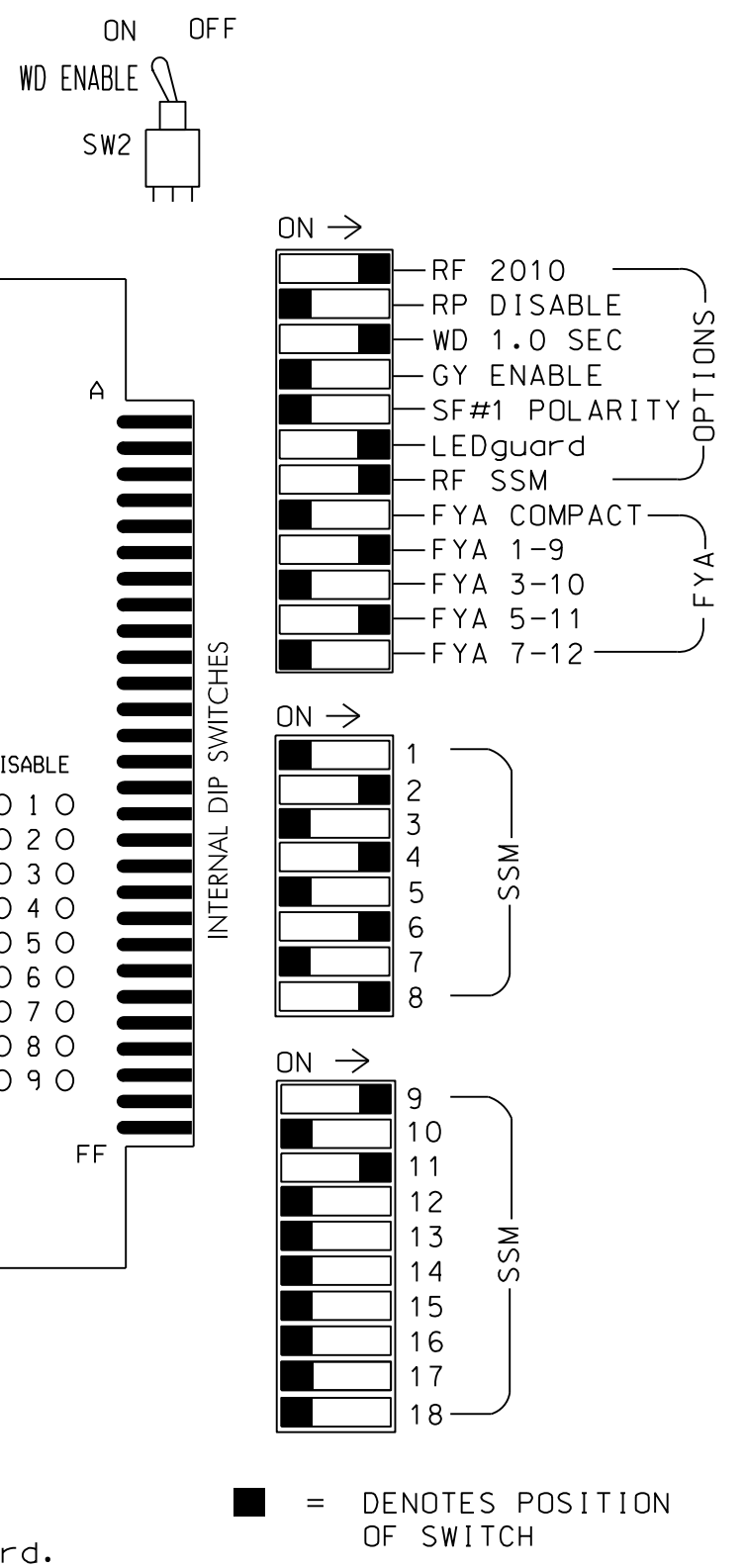
REMOVE DIODE JUMPERS 2-5, 2-6, 2-9, 2-11, 4-8, 5-9, 5-11, 6-9, 6-11, and 9-11



REMOVE JUMPERS AS SHOWN

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.



NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Program phases 4 and 8 for Dual Entry.
3. Enable Simultaneous Gap-Out for all phases.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash and overlap 1 as Wag Overlaps.

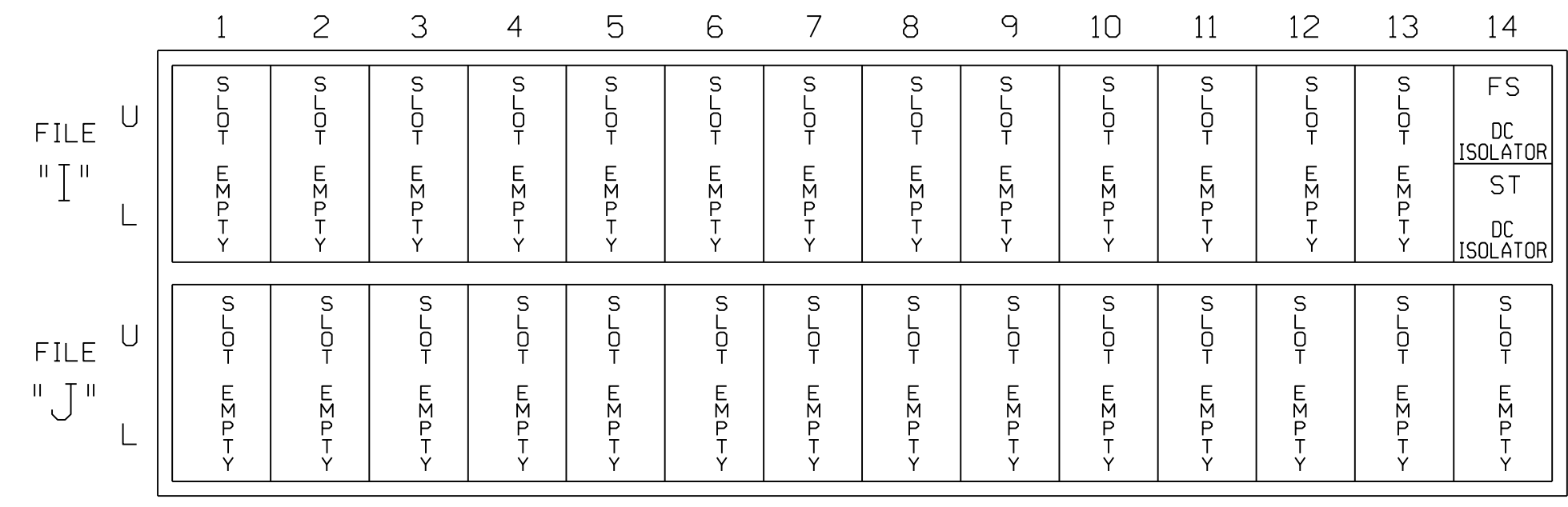
EQUIPMENT INFORMATION

CONTROLLER.....2070
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE

LOAD SWITCHES USED.....S2,S5,S7,S8,S11,AUXS1,AUXS4.
 PHASES USED.....2,4,5,6,8
 OVERLAP "A".....2
 OVERLAP "B".....NOT USED
 OVERLAP "C".....5+6
 OVERLAP "D".....NOT USED

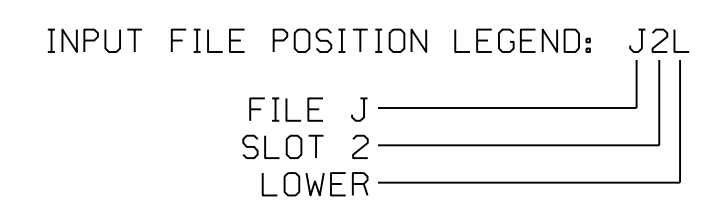
INPUT FILE POSITION LAYOUT

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME



SPECIAL DETECTOR NOTE

Install a loop emulation detection system for vehicle detection. Perform installation according to manufacturer's directions and NCDOT engineer-approved mounting locations to accomplish the detection schemes shown on the Signal Design Plans.

FLASHER CIRCUIT MODIFICATION DETAIL

IN ORDER TO INSURE THAT SIGNALS FLASH CONCURRENTLY ON THE SAME APPROACH, MAKE THE FOLLOWING FLASHER CIRCUIT CHANGES:

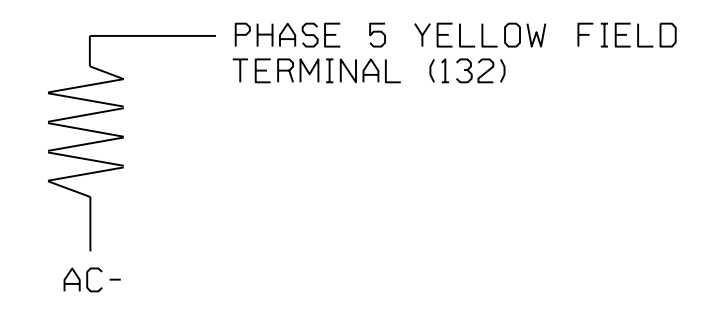
1. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-4 AND TERMINATE ON T2-2.
2. ON REAR OF PDA - REMOVE WIRE FROM TERM. T2-5 AND TERMINATE ON T2-3.
3. REMOVE FLASHER UNIT 2.

THE CHANGES LISTED ABOVE TIES ALL PHASES AND OVERLAPS TO FLASHER UNIT 1.

LOAD RESISTOR INSTALLATION DETAIL

(install resistors as shown below)

ACCEPTABLE VALUES	
VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



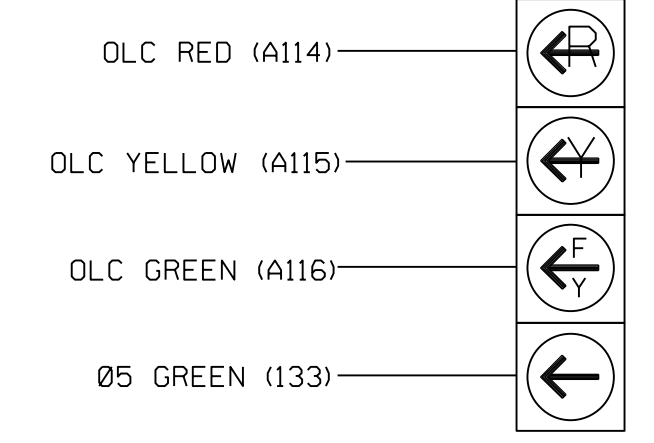
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42, 43	NU	51	62,63	NU	NU	81,82	NU	61	NU	NU	51	NU	NU
RED		128			101			134			107							
YELLOW		129			102		*	135			108							
GREEN		130			103			136			109							
RED ARROW													A121			A114		
YELLOW ARROW													A122			A115		
FLASHING YELLOW ARROW													A123			A116		
GREEN ARROW							133											

NU = Not Used
 * Denotes install load resistor. See load resistor installation detail this sheet.
 ★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal heads as shown)

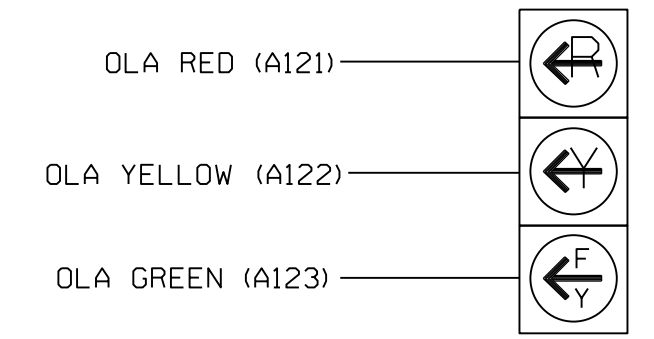


NOTE

1. The sequence display for signal #51 requires special logic programming. See sheet 2 for programming instructions.

SIGNAL WIRING DETAIL

(wire signal heads as shown)



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 12-0538-T1
 DESIGNED: July 2016
 SEALED: 10/4/2016
 REVISED:

Signal Upgrade
 Temporary Design 1 - TMP Phases 1 & 01A
 ELECTRICAL DETAIL SHEET 1 OF 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

	NC 273 (South Main Street) at SR 2044 (Tuckaseegee Road)/ Rankin Avenue		
	Division 12 Gaston County Mount Holly		
PLAN DATE: JULY 2016	REVIEWED BY: D. HARRIS		
PREPARED BY: J. HAMBRIGHT	REVIEWED BY: B. WATSON		
REVISIONS	INIT. DATE		
Documented by: <i>Betty L. Watson</i> 10/4/2016			
750 N. Greenfield Pkwy, Garner, NC 27529			