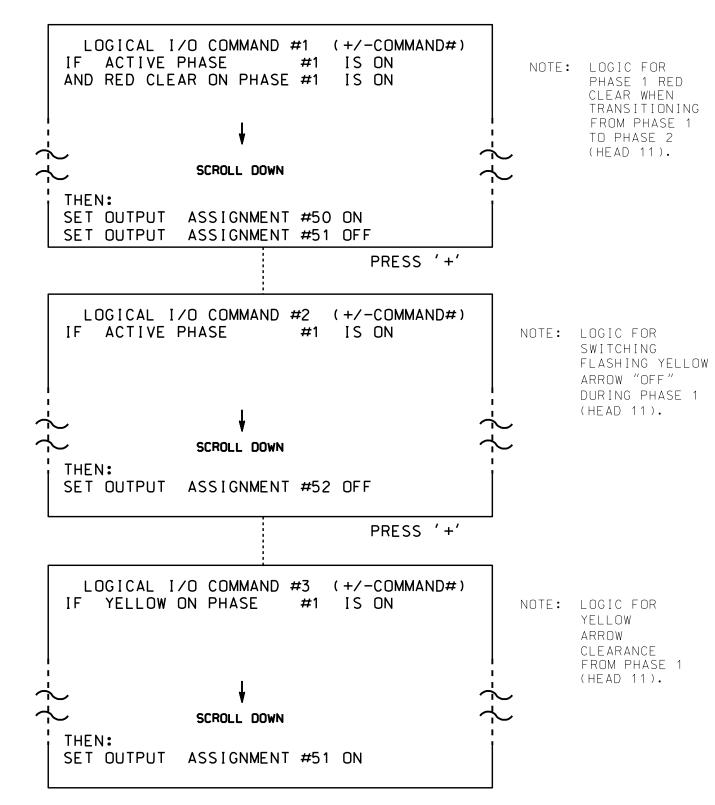
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

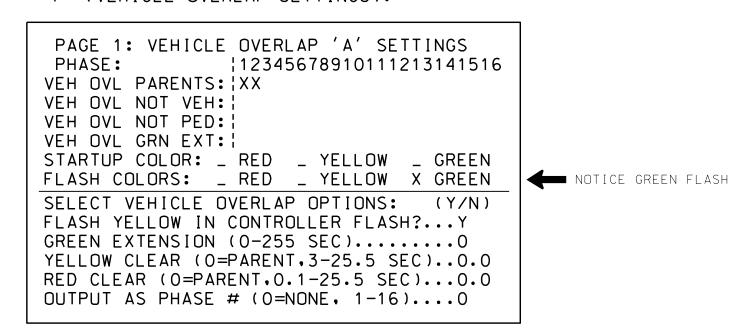
OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 07-2132T2 DESIGNED: November 2016 SEALED: 1/24/2017 REVISED: N/A

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED Electrical Detail - Temp 2 (TMP Phase IIB) - Sheet 2 of 2 ELECTRICAL AND PROGRAMMING SR 1962 (S. Third Street) DETAILS FOR: SR 1980 (Holmes Road) ivision 7 Alamance County Mebane PLAN DATE: January 2017 REVIEWED BY: BAS PREPARED BY: S. Armstrong REVIEWED BY:

750 N.Greenfield Pkwy, Garner, NC 27529

REVISIONS INIT. DATE SIG. INVENTORY NO. 07-2132T2