LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE
AND TO INVERT INPUT FROM MICROWAVE DETECTOR

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 and 3.

2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).

```
LOGICAL I/O COMMAND #1  (+/-COMMAND#)
IF  ACTIVE PHASE        #1  IS ON
AND RED CLEAR ON PHASE #1  IS ON

         ↓
    SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #14 ON
SET OUTPUT  ASSIGNMENT #15 OFF
```
NOTE: LOGIC FOR PHASE 1 RED CLEAR WHEN TRANSITIONING FROM PHASE 1 TO PHASE 2 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #2  (+/-COMMAND#)
IF  ACTIVE PHASE        #1  IS ON

         ↓
    SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #16 OFF
```
NOTE: LOGIC FOR SWITCHING FLASHING YELLOW ARROW "OFF" DURING PHASE 1 (HEAD 11).

PRESS '+'

```
LOGICAL I/O COMMAND #3  (+/-COMMAND#)
IF  YELLOW ON PHASE     #1  IS ON

         ↓
    SCROLL DOWN

THEN:
SET OUTPUT  ASSIGNMENT #15 ON
```
NOTE: LOGIC FOR YELLOW ARROW CLEARANCE FROM PHASE 1 (HEAD 11).

LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

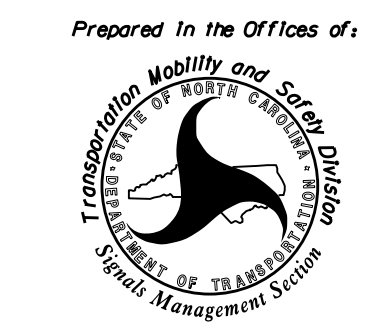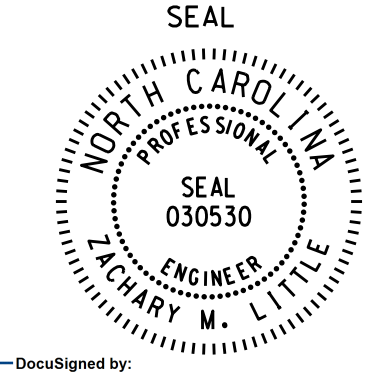FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

```
PAGE 1: VEHICLE OVERLAP 'A' SETTINGS
PHASE:           12345678910111213141516
VEH OVL PARENTS: XX
VEH OVL NOT VEH:
VEH OVL NOT PED:
VEH OVL GRN EXT:
STARTUP COLOR: _ RED  _ YELLOW  _ GREEN
FLASH COLORS:  _ RED  _ YELLOW  X GREEN     ← NOTICE GREEN FLASH

SELECT VEHICLE OVERLAP OPTIONS:   (Y/N)
FLASH YELLOW IN CONTROLLER FLASH?...Y
GREEN EXTENSION (0-255 SEC).........0
YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0
RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0
OUTPUT AS PHASE # (0=NONE, 1-16)....0
```

OVERLAP PROGRAMMING COMPLETE

```
THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 07-0440T2
DESIGNED: September 2016
SEALED: 1/25/17
REVISED: N/A
```

OUTPUT REFERENCE SCHEDULE

```
OUTPUT 14 = Overlap A Red
OUTPUT 15 = Overlap A Yellow
OUTPUT 16 = Overlap A Green
OUTPUT 33 = Phase 1 Green
```

Note: All Inputs and Outputs shown above have been remapped. See sheet 3 of this electrical detail.

Electrical Detail - Temp 2 (TMP Phase II & III) - Sheet 2 of 3

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared In the Offices of:

Transportation Mobility and Safety Division
SEAL OF TRANSPORTATION
Signal Management Section

750 N.Greenfield Pkwy,Garner,NC 27529

NC 119
at
I-40 EB/I-85 NB Ramps

Division 7	Alamance County	Mebane
PLAN DATE: January 2017	REVIEWED BY: BAS	
PREPARED BY: B. Simmons	REVIEWED BY:	

REVISIONS

	INIT.	DATE

SEAL

NORTH CAROLINA
SEAL
030530
PROFESSIONAL ENGINEER
ZACHARY M. LITTLE

DocuSigned by:
Zachary M. Little   1/30/2017
9C21EFD94F5341F
DATE

SIG. INVENTORY NO. 07-0440T2