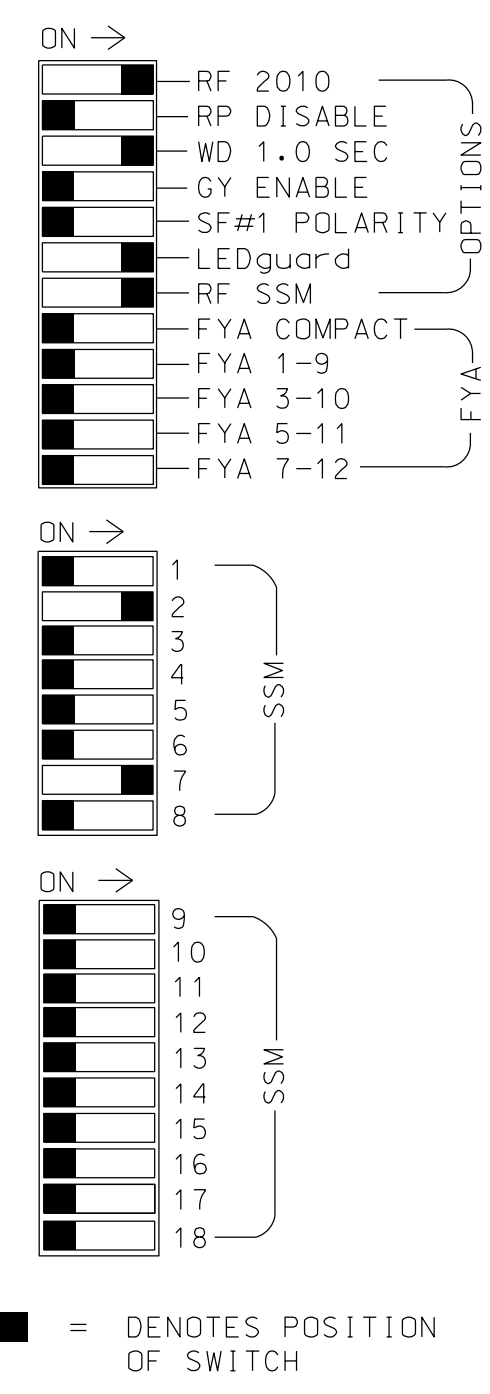
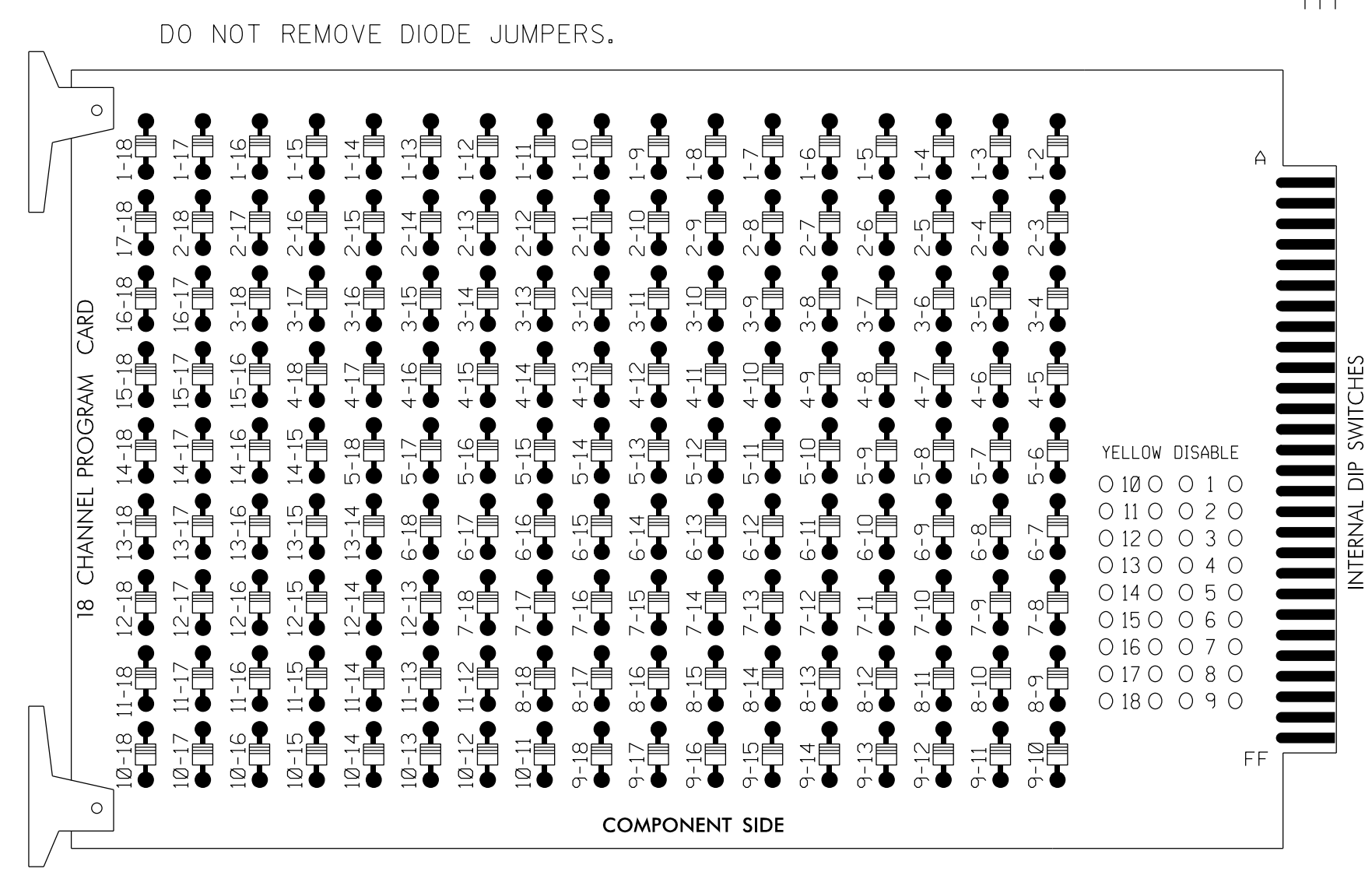


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phase 2 for Variable Initial and Gap Reduction.
- Program phase 2 for Start Up In Green.
- Program phase 2 for Yellow Flash.
- The cabinet and controller are part of the US 74-Indian Trail Closed Loop System #2.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE*
 LOAD SWITCHES USED.....S2,S10.
 PHASES USED.....2,7.
 OVERLAP "A".....NOT USED
 OVERLAP "B".....NOT USED
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED
 *INSTALL AUX. OUTPUT FILE FOR FUTURE USE

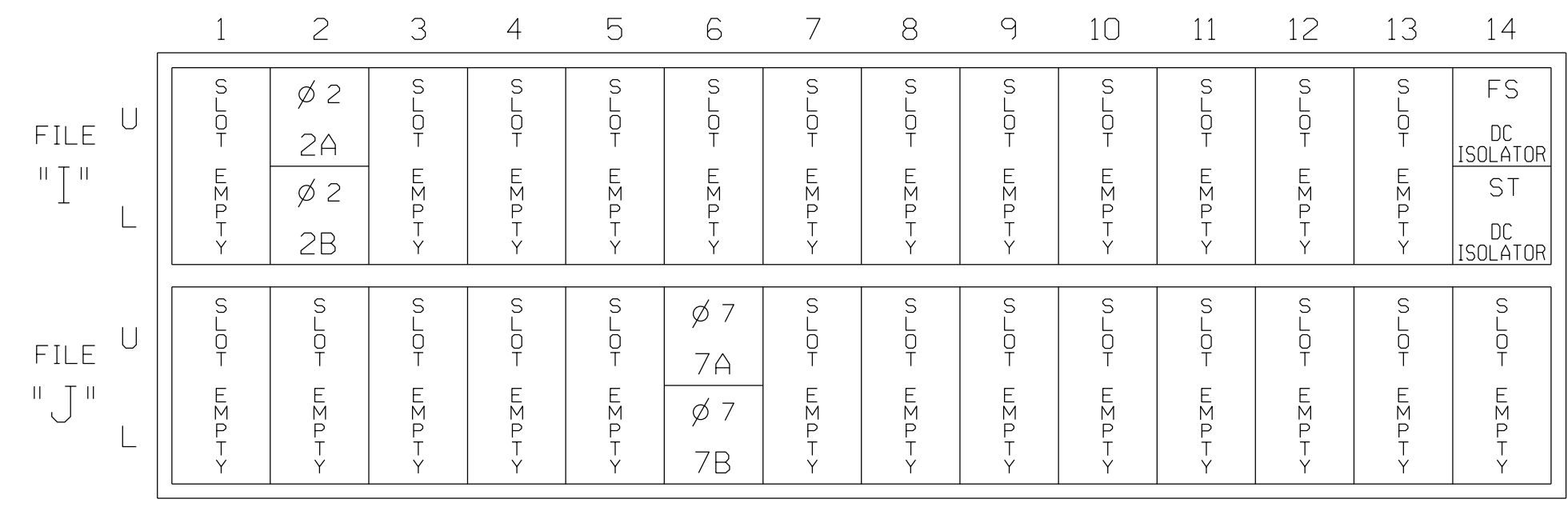
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	DLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	NU	NU	NU	NU	NU	71,72	NU	NU	NU	NU	NU	NU	NU	NU
RED		128								122								
YELLOW		129																
GREEN		130																
RED ARROW																		
YELLOW ARROW										123								
FLASHING YELLOW ARROW																		
GREEN ARROW										124								

NU = Not Used

INPUT FILE POSITION LAYOUT

(front view)



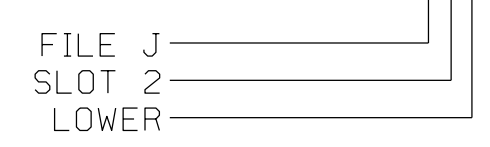
EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
7A	TB5-9,10	J6U	42	4	8	7	Y	Y			10
7B	TB5-11,12	J6L	46	8	18	7	Y	Y			10

INPUT FILE POSITION LEGEND: J2L



THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-2196T1
 DESIGNED: June 2015
 SEALED: August 30, 2016
 REVISED:

ELECTRICAL DETAIL

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

PLANS PREPARED BY:

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 ENGINEERS • PLANNERS • SCIENTISTS

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Prepared for the Offices of:

US 74 (Andrew Jackson Highway)
 Eastbound
 at
 SR 1377 (Wesley Chapel Stouts Rd)

Division 10 Union County Indian Trail

PLAN DATE: June 2015 REVIEWED BY: LM Moon
 PREPARED BY: K Smith REVIEWED BY: B Humfleet

REVISIONS	INIT.	DATE

12/12/2016
 Lisa M. Moon

SIG. INVENTORY NO. 10-2196T1

09-DEC-2016 14:16
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