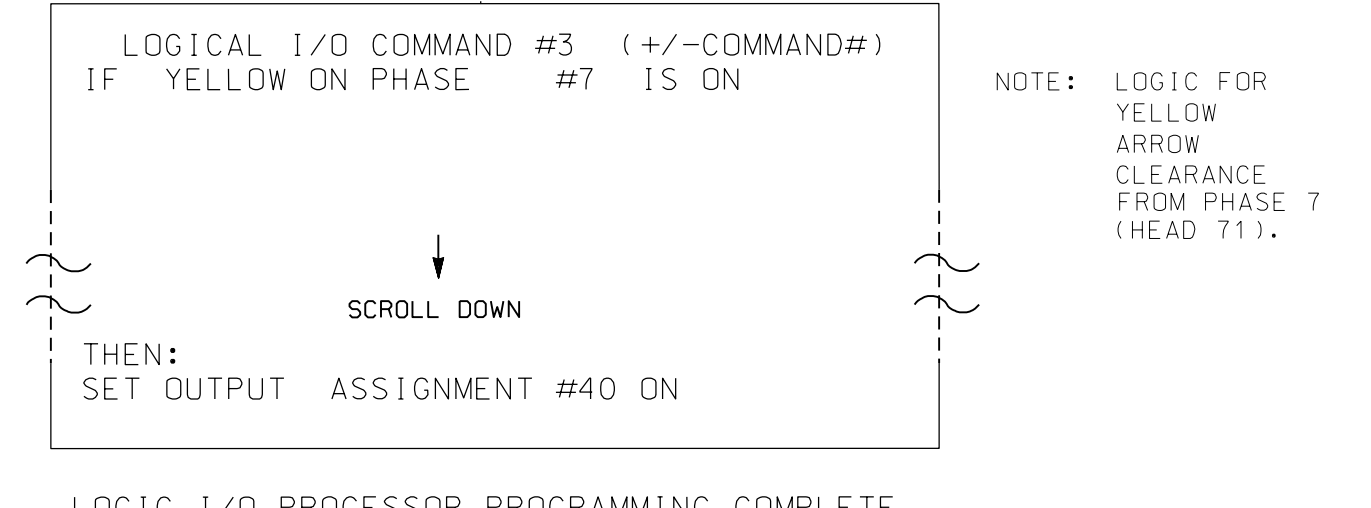
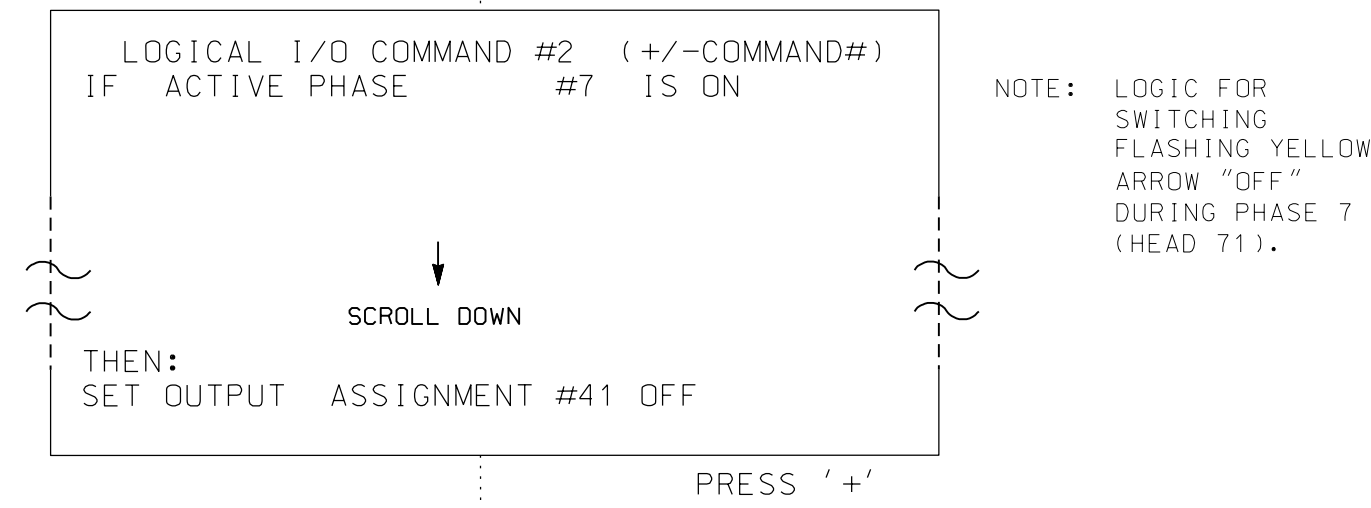
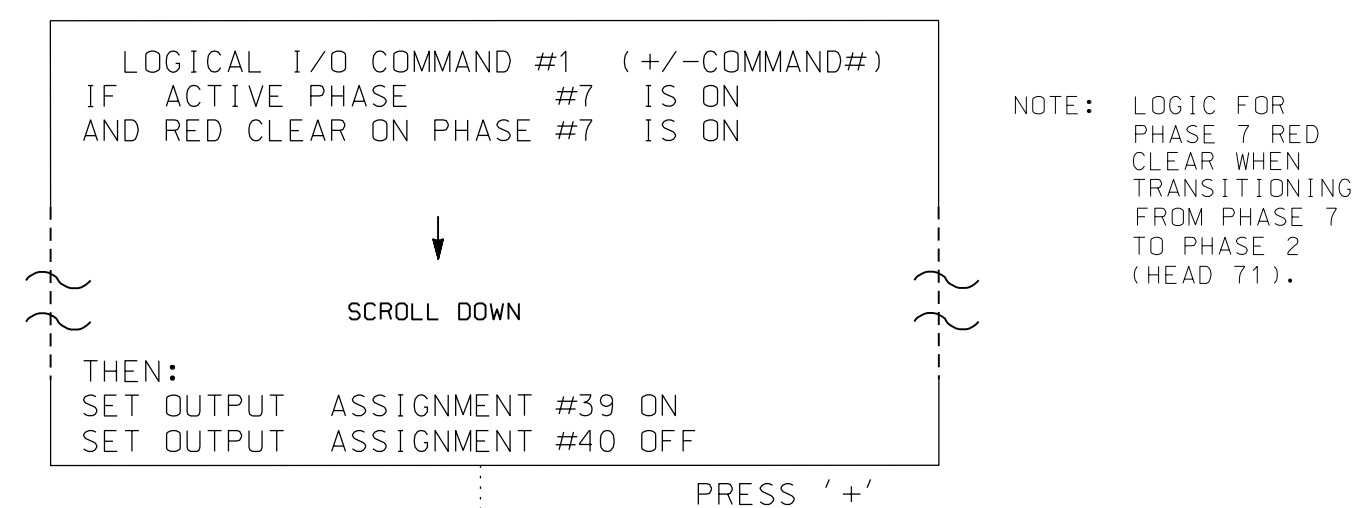


### LOGICAL I/O PROCESSOR PROGRAMMING DETAIL TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2 AND 3.
2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



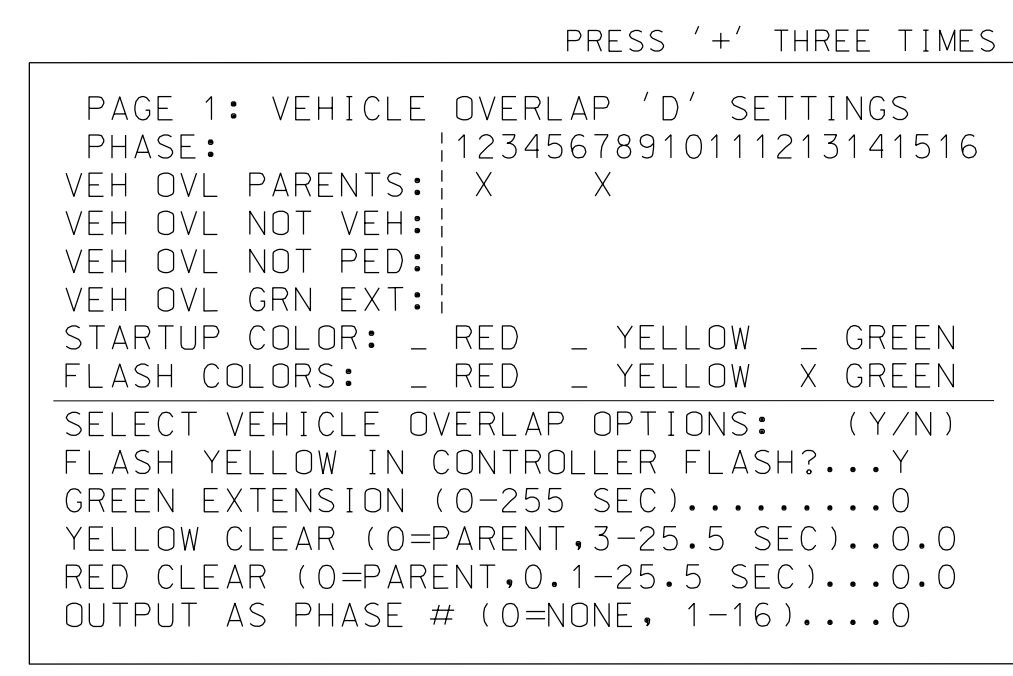
**OUTPUT REFERENCE SCHEDULE**

OUTPUT 39 = Overlap D Red
OUTPUT 40 = Overlap D Yellow
OUTPUT 41 = Overlap D Green

### OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

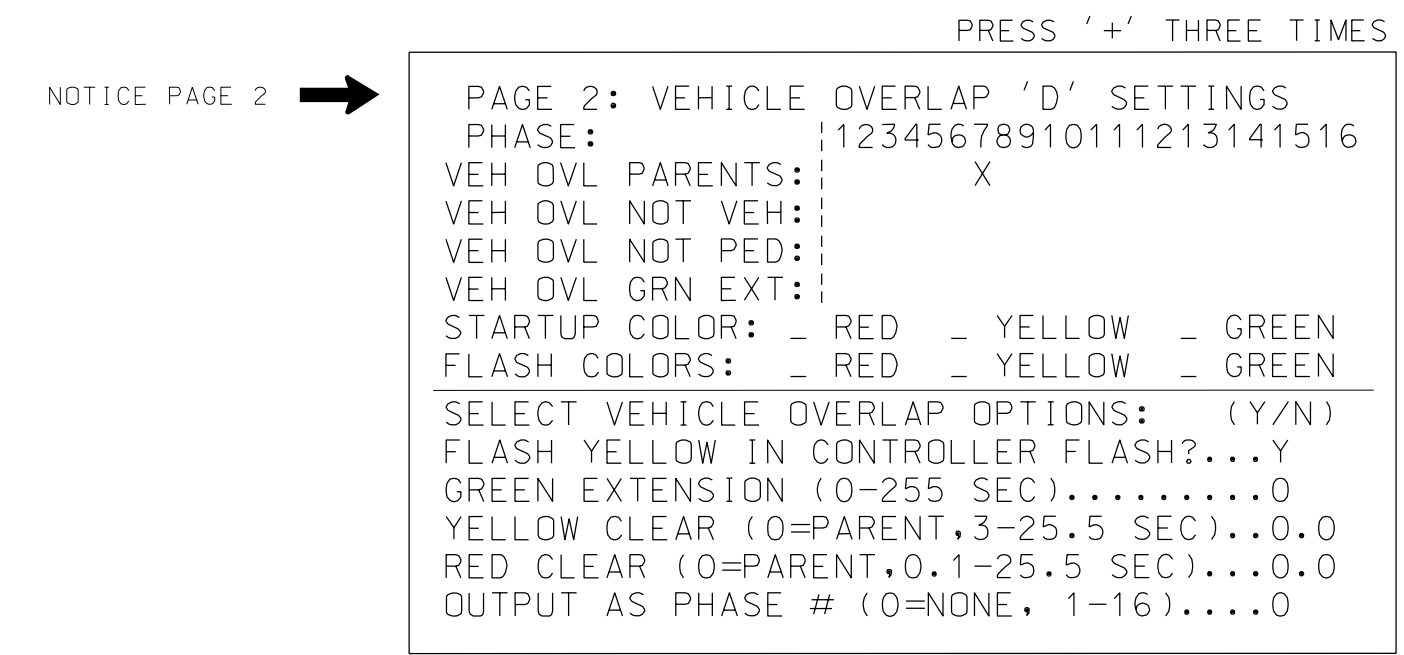


OVERLAP PROGRAMMING COMPLETE

### OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS NEXT TO ADVANCE TO PAGE 2.



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR  
THE SIGNAL DESIGN: 10-2192  
DESIGNED: June 2015  
SEALED: August 30, 2016  
REVISED:

ELECTRICAL DETAIL SHEET 2 OF 4

DOCUMENT NOT CONSIDERED FINAL  
UNLESS ALL SIGNATURES COMPLETED

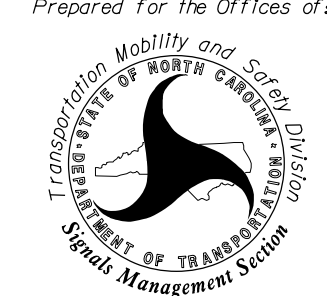
PLANS PREPARED BY:



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Prepared for the Offices of:




US 74 (Andrew Jackson Highway)  
Eastbound  
at  
Harris Teeter Distribution Cntr  
Division 10 Union County Indian Trail

PLAN DATE: June 2015 REVIEWED BY: B Humfleet  
PREPARED BY: LM Moon REVIEWED BY:

REVISIONS	INIT.	DATE

SEAL



Lisa M. Moon 12/12/2016  
DATE

SIG. INVENTORY NO. 10-2192