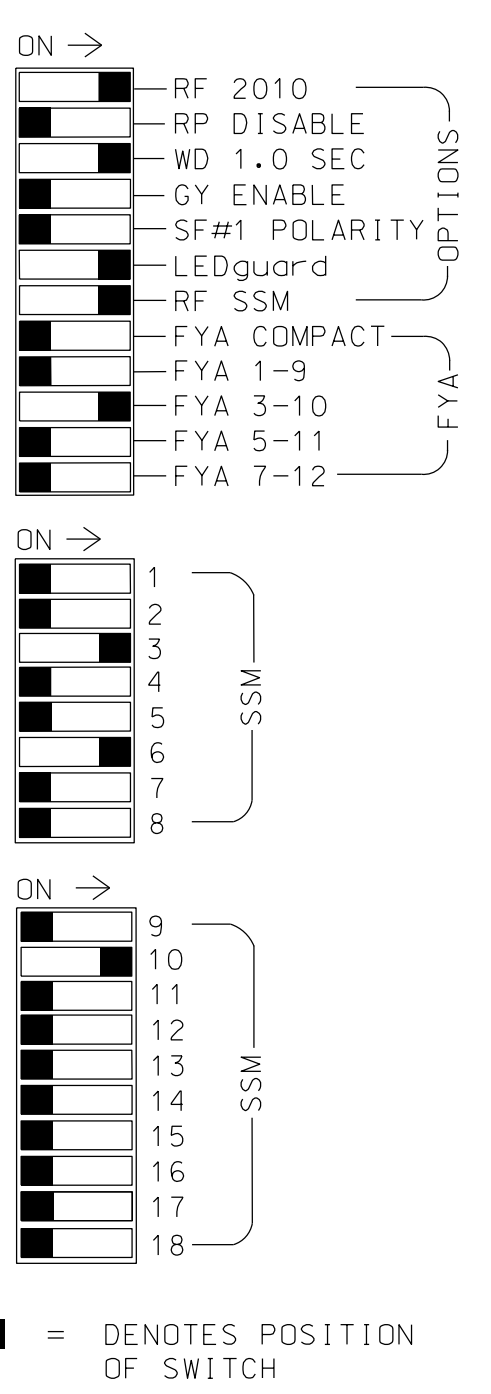
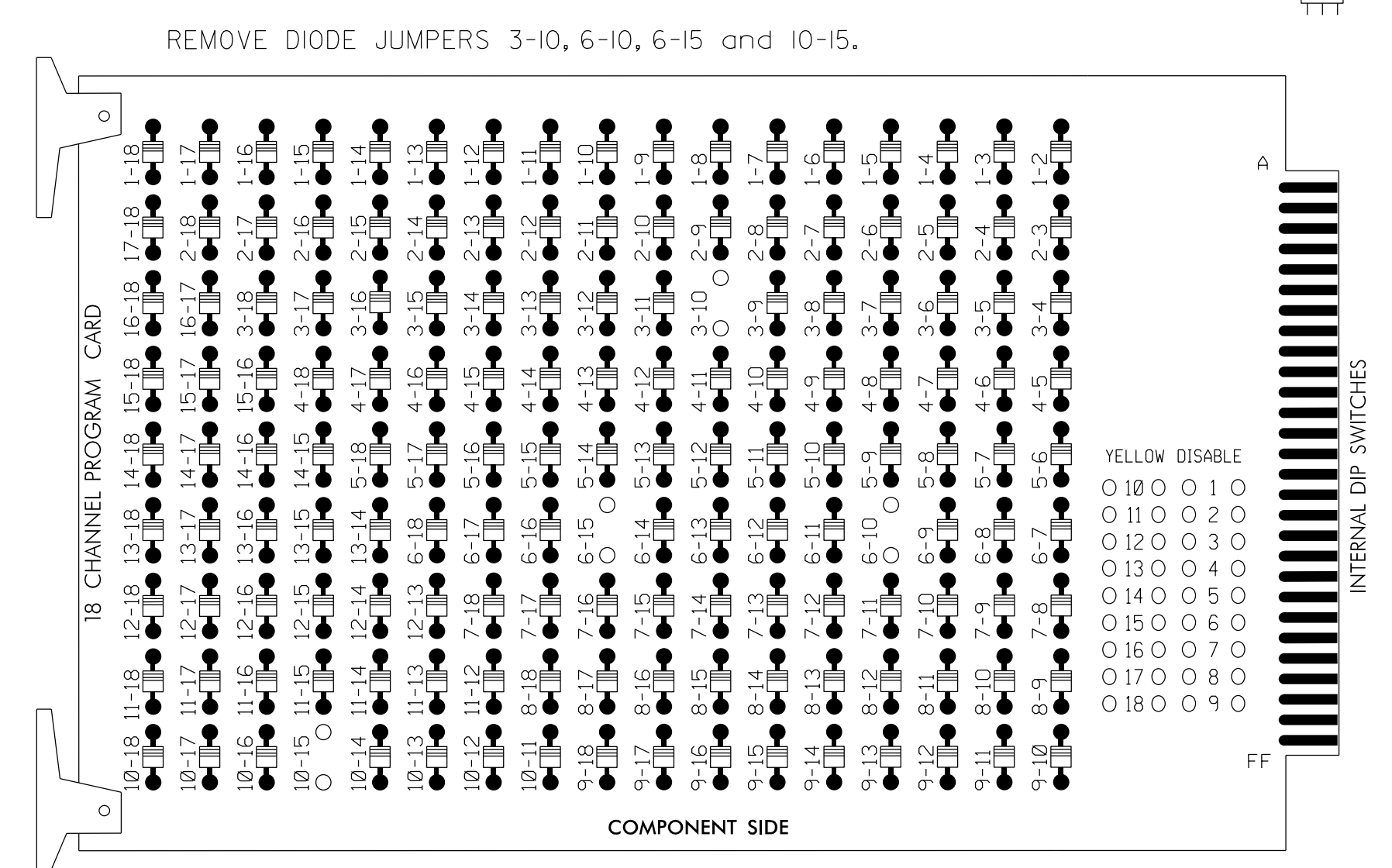


EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Connect serial cable from conflict monitor to comm. port 1 of 2070 controller. Ensure conflict monitor communicates with 2070.

NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phase 6 for Variable Initial and Gap Reduction.
- Program phase 6 for Start Up In Green.
- Program phase 6 for 'STARTUP PED CALL'.
- Program phase 6 for Yellow Flash and overlap 2 as Wag Overlaps.
- The cabinet and controller are part of the US 74 - Indian Trail Closed Loop System #2.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
 CABINET.....332 /W/ AUX
 SOFTWARE.....ECONOLITE OASIS
 CABINET MOUNT.....BASE
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
 LOAD SWITCHES USED.....S4,S8,S9,AUX S2.
 PHASES USED.....3,6,6PED.
 OVERLAP "A".....NOT USED
 OVERLAP "B".....3+6
 OVERLAP "C".....NOT USED
 OVERLAP "D".....NOT USED

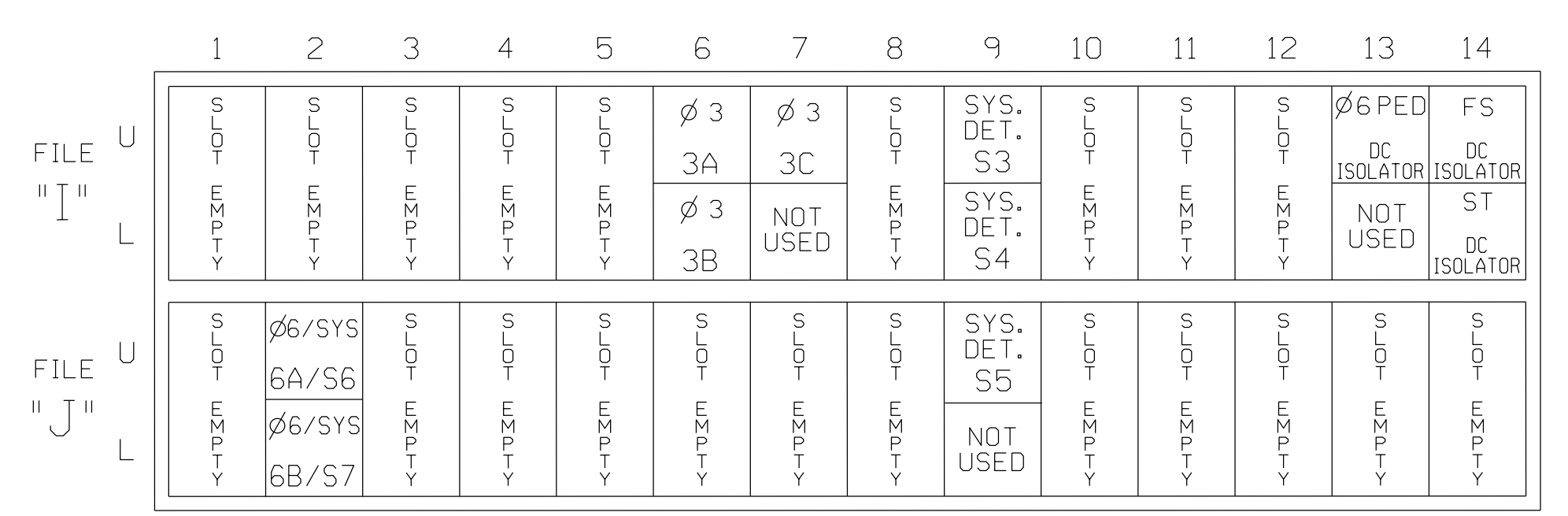
SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6	
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18	
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	3 PED	OLA	OLB	SPARE	OLC	OLD	SPARE	
SIGNAL HEAD NO.	NU	NU	NU	31	32,33	NU	NU	NU	61,62	P61 P62	NU	NU	NU	31	NU	NU	NU	NU	
RED				116					134										
YELLOW									135										
GREEN									136										
RED ARROW																		A124	
YELLOW ARROW									117										A125
FLASHING YELLOW ARROW																			A126
GREEN ARROW				118	118														
Hand icon																			119
Walking person icon																			121

NU = Not Used

★ See pictorial of head wiring in detail below.

INPUT FILE POSITION LAYOUT (front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

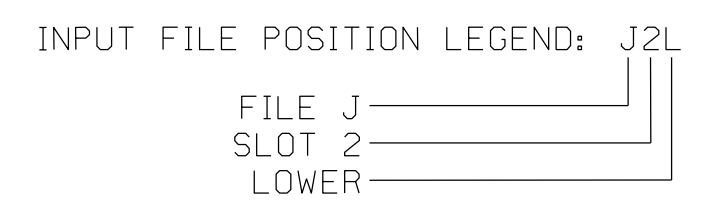
FS = FLASH SENSE
 ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

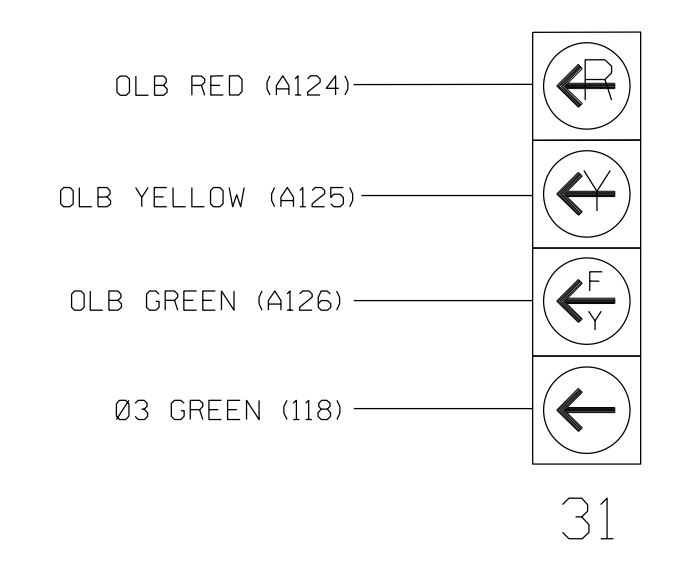
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
3A	TB4-9,10	16U	41	3	4	3	Y	Y			15
	-	16U	41	3★	53	3	Y	Y			
3B	TB4-11,12	16L	45	7	14	3	Y	Y			10
3C	TB6-1,2	17U	65	27	34	3	Y	Y			10
* S3	TB6-9,10	19U	60	22	11	SYS					
* S4	TB6-11,12	19L	62	24	13	SYS					
6A/S6	TB3-5,6	J2U	40	2	6	6/SYS	Y	Y			
6B/S7	TB3-7,8	J2L	44	6	16	6/SYS	Y	Y			
* S5	TB7-9,10	J9U	59	21	15	SYS					
PED PUSH BUTTONS											
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					

NOTE:
 INSTALL DC ISOLATORS IN INPUT FILE SLOT 113.

- * SYSTEM DETECTOR ONLY. REMOVE THE VEHICLE PHASE ASSIGNED TO THIS DETECTOR IN THE DEFAULT PROGRAMMING.
- ★ INPUT PAGE 2. SEE INPUT PAGE ASSIGNMENT PROGRAMMING DETAIL ON SHEET 3.



4 SECTION FYA PPLT SIGNAL WIRING DETAIL (wire signal heads as shown)



NOTE

- The sequence display for this signal requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-2191
 DESIGNED: June 2015
 SEALED: August 30, 2016
 REVISED:

ELECTRICAL DETAIL SHEET 1 OF 4

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared for the Offices of: 	US 74 (Andrew Jackson Highway) Westbound at SR 1518 (Faith Church Road)		SEAL
	Division 10 PLAN DATE: June 2015 PREPARED BY: LM Moon	Union County Indian Trail REVIEWED BY: B Humfleet REVIEWED BY:	
REVISIONS:			SIG. INVENTORY NO. 10-2191

PLANS PREPARED BY:

DRMP
 ENGINEERS • PLANNERS • SCIENTISTS

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