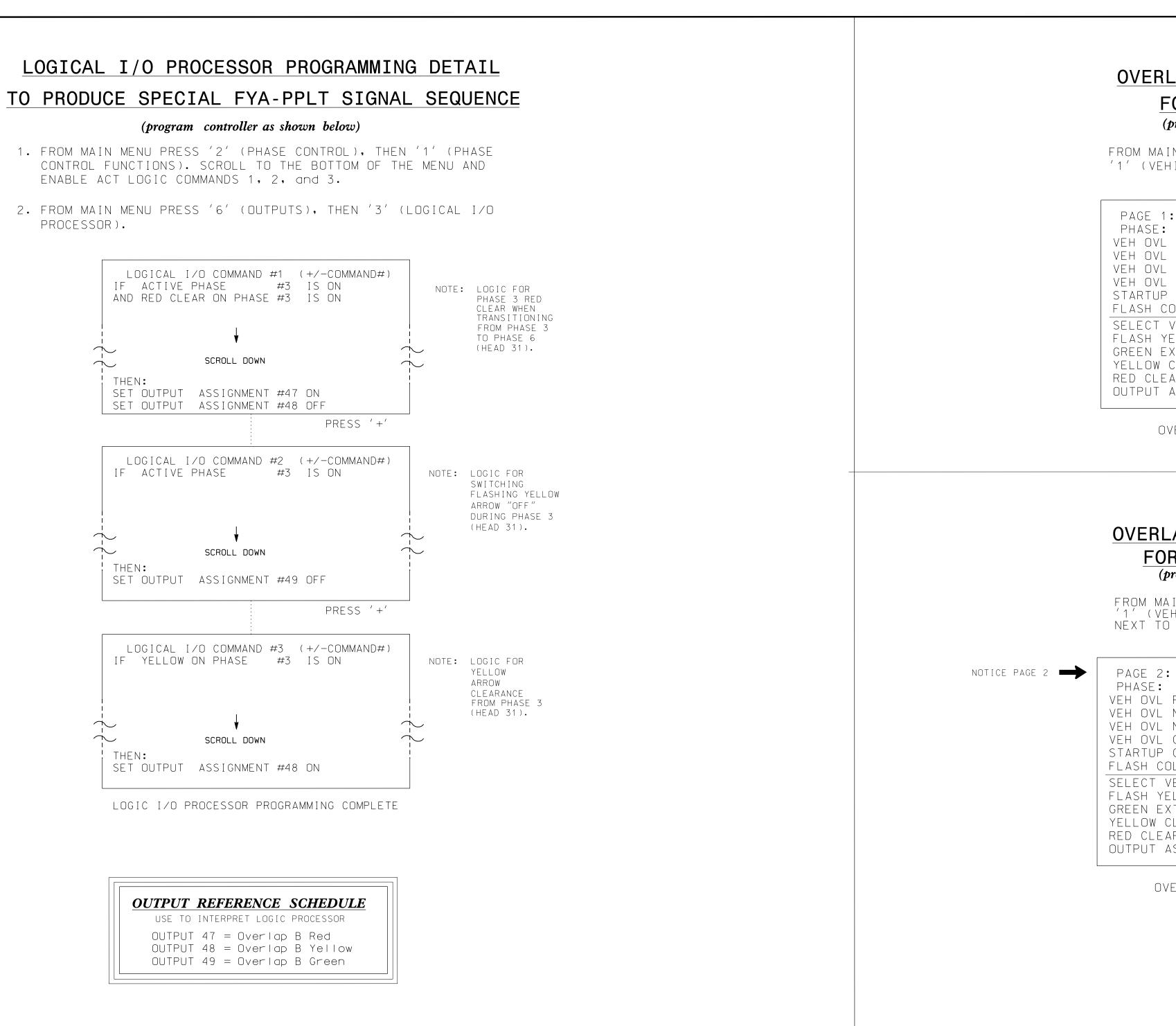
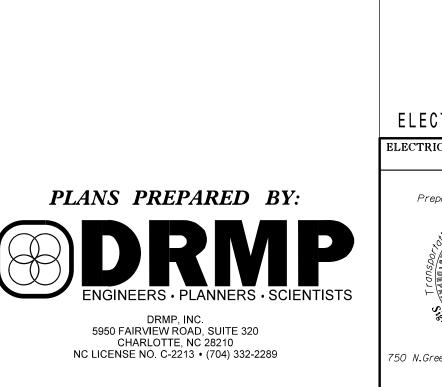
- ENABLE ACT LOGIC COMMANDS 1, 2, and 3.
- PROCESSOR).



OUTPUT	REFE	RENCE	SC	HEDU
USE TO	INTERP	RET LOGIC	PRC	CESSOR
OUTPUT	48 =	Overlap Overlap Overlap	В	Yello



	PROJECT REFERENCE NO.	SHEET NO.	
	W-5520	Sig. 14.2	
RLAP PROGRAMMING DETAIL			
FOR DEFAULT PHASING			
(program controller as shown below)			
AIN MENU PRESS '8' (OVERLAPS), THEN EHICLE OVERLAP SETTINGS).			
PRESS '+'			
1: VEHICLE OVERLAP 'B' SETTINGS : 12345678910111213141516 L PARENTS: X X L NOT VEH: L NOT PED: L GRN EXT: P COLOR: _ RED _ YELLOW _ GREEN COLORS: _ RED _ YELLOW X GREEN VEHICLE OVERLAP OPTIONS: (Y/N) YELLOW IN CONTROLLER FLASH?Y EXTENSION (0-255 SEC)O CLEAR (0=PARENT,3-25.5 SEC)O.O CAS PHASE # (0=NONE, 1-16)O OVERLAP PROGRAMMING COMPLETE	N FLASH		

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OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING (program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS NEXT TO ADVANCE TO PAGE 2.

PRESS '+'

: VEHICLE OVERLAP 'B' SETTINGS {12345678910111213141516
PARENTS: X Not veh:
NOT PED:¦ GRN EXT:¦ Color: _ RFD _ Yellow _ Green
DLORS: _ RED _ YELLOW _ GREEN
/EHICLE OVERLAP OPTIONS: (Y/N) ELLOW IN CONTROLLER FLASH?Y (TENSION (0-255 SEC)0) CLEAR (0=PARENT,3-25.5 SEC)0.0 AR (0=PARENT,0.1-25.5 SEC)0.0 AS PHASE # (0=NONE, 1-16)0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 10-2189 DESIGNED: June 2015 SEALED: August 30,2016 REVISED:

CTRICAL DETAIL SHEET 2 OF 4				DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED			
RICAL AND PROGRAMMING DETAILS FOR:	US 7	4 (Indepe	ndence	Blvd)	WB	SEAL	
Nobility and Solution	at SR 1367 (Unionvil Trail Road) Eastbo Division 10 Union County PLAN DATE: June 2015 REVIEWED		astbour	Dund U-turn Indian Trail		SEAL 022516	
SET OF TRANSPORT	PREPARED BY:	K Smith REVISIONS	REVIEWED BY:	B Humf]	DATE	MA M. M	
Greenfield Pkwy,Garner,NC 27529						Lisa M. Moon	12/12/2016
						SIG. INVENTORY NO.	10-2189