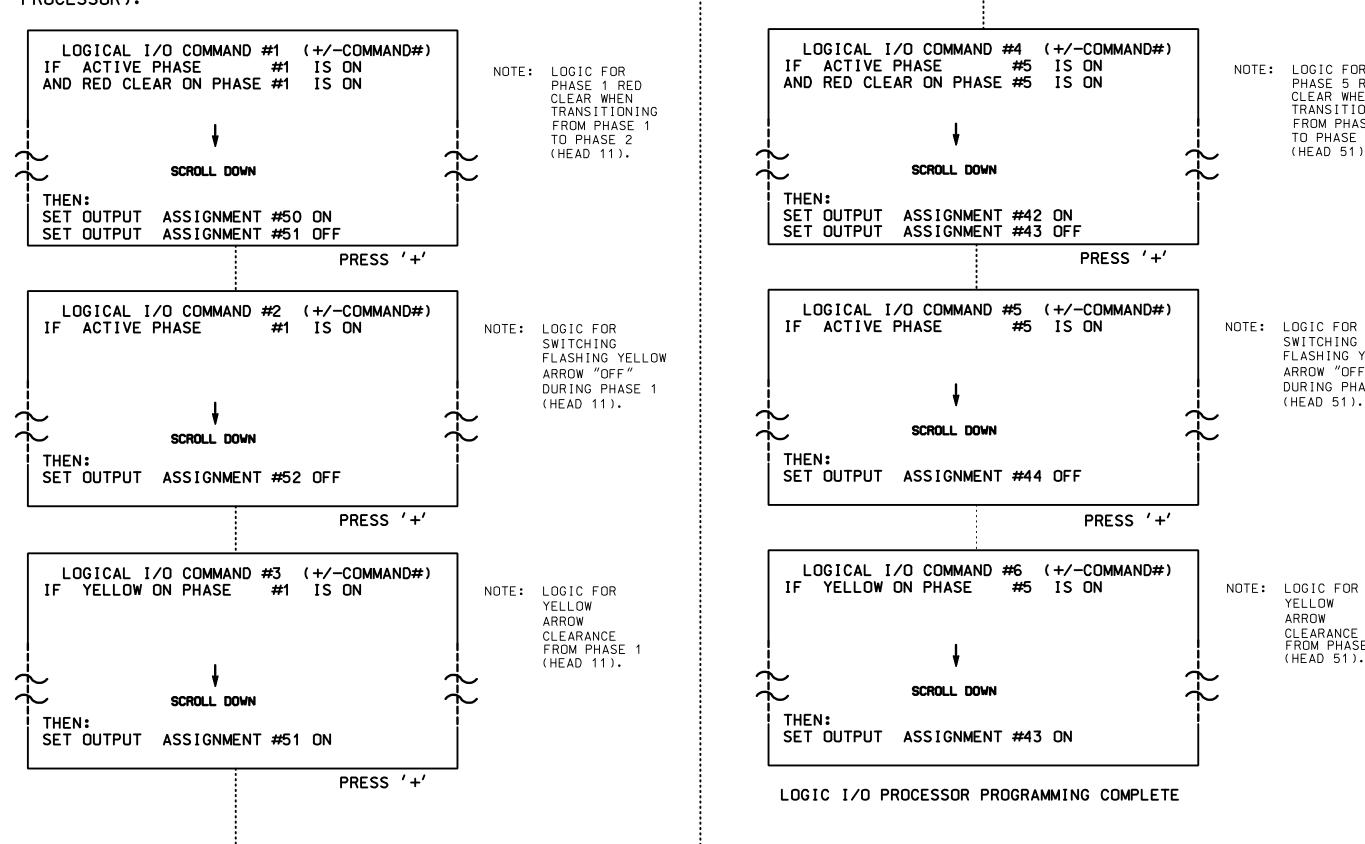
#### PROJECT REFERENCE NO. SHEET NO. Sig. 24. I-5000

# LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

### TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, 3, 4, 5 AND 6.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



**OUTPUT REFERENCE SCHEDULE** 

OUTPUT 43 = Overlap C Yellow

OUTPUT 44 = Overlap C Green OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

OUTPUT 42 = Overlap C Red

## OVERLAP PROGRAMMING DETAIL FOR DEFAULT PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS).

NOTE: LOGIC FOR

PHASE 5 RED

TRANSITIONING

FROM PHASE 5

CLEAR WHEN

TO PHASE 6

(HEAD 51).

SWITCHING

(HEAD 51).

YELLOW

ARROW

CLEARANCE FROM PHASE 5

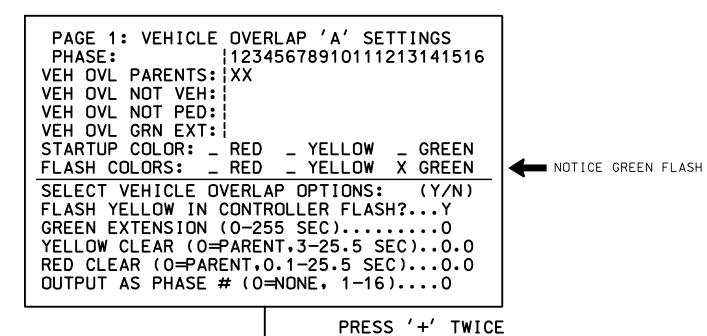
(HEAD 51).

FLASHING YELLOW

ARROW "OFF"

DURING PHASE 5

PHASE:



PAGE 1: VEHICLE OVERLAP 'C' SETTINGS 12345678910111213141516

VEH OVL PARENTS: | XX VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW X GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC).....0

YELLOW CLEAR (O=PARENT,3-25.5 SEC)..0.0

RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0

OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

# OVERLAP PROGRAMMING DETAIL FOR ALTERNATE PHASING

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS), THEN '1' (VEHICLE OVERLAP SETTINGS). PRESS 'NEXT' TO ADVANCE TO PAGE 2.

NOTICE -PAGE 2: VEHICLE OVERLAP 'A' SETTINGS 12345678910111213141516 VEH OVL PARENTS: | X VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW \_ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC).....0 YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT, 0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

> PRESS '+' TWICE PAGE 2: VEHICLE OVERLAP 'C' SETTINGS 12345678910111213141516

VEH OVL NOT VEH: VEH OVL NOT PED: VEH OVL GRN EXT: | STARTUP COLOR: \_ RED \_ YELLOW \_ GREEN FLASH COLORS: \_ RED \_ YELLOW \_ GREEN SELECT VEHICLE OVERLAP OPTIONS: (Y/N) FLASH YELLOW IN CONTROLLER FLASH?...Y GREEN EXTENSION (0-255 SEC)...... YELLOW CLEAR (0=PARENT,3-25.5 SEC)..0.0 RED CLEAR (0=PARENT,0.1-25.5 SEC)...0.0 OUTPUT AS PHASE # (0=NONE, 1-16)....0

OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 12-1623 DESIGNED: September 2016 SEALED: 12-16-2016 REVISED:

PAGE 2

NOTICE -

PHASE:

VEH OVL PARENTS: | X

PAGE 2

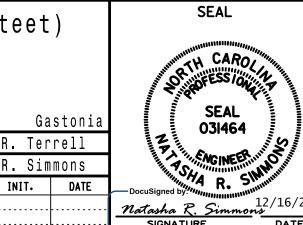
NOTICE GREEN FLASH

Signal Upgrade - Final Design (Sheet 2 of 5)

**DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED** 



US 321 (N. Chester Steet) Radio Street Division 12 Gaston Co. September 2016 REVIEWED BY: T.R. Terrell PLAN DATE: PREPARED BY: J.A. Wagner REVIEWED BY: N R Simmons REVISIONS



sig. inventory no. 12-1623

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