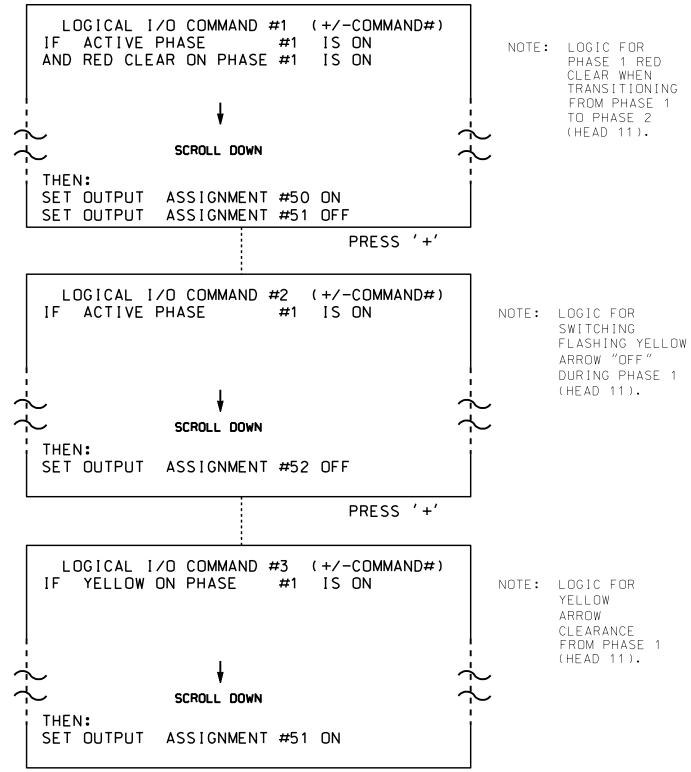
(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

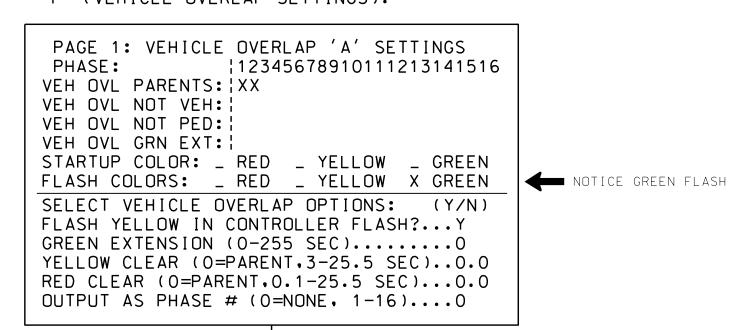
OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green U-4715B Sig. 230.2

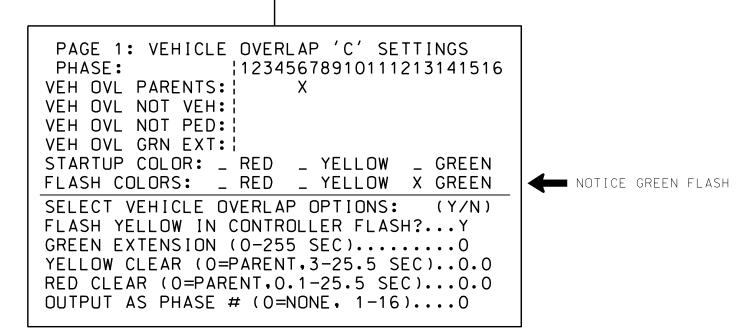
OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



PRESS '+' TWICE



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 14-1050
DESIGNED: January 2016
SEALED: 8/9/2016
REVISED: N/A

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared In the Offices of:

SR 1534 (Naples Road) / Skyland Drive

Division 14 Henderson County Hendersonville
PLAN DATE: May 2016 REVIEWED BY:

PREPARED BY: S. Armstrong REVIEWED BY:

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

SEAL

O36880

PREPARED BY: S. Armstrong REVIEWED BY:

BOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

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SEAL

SEAL

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PREPARED BY: S. Armstrong REVIEWED BY:

BAS

INIT. DATE

SIG. INVENTORY NO. 14-1050

REVISIONS

10-AUG-2016 11:54 S:*ITS&SU*ITS Signals*Workgroups sarmstrong