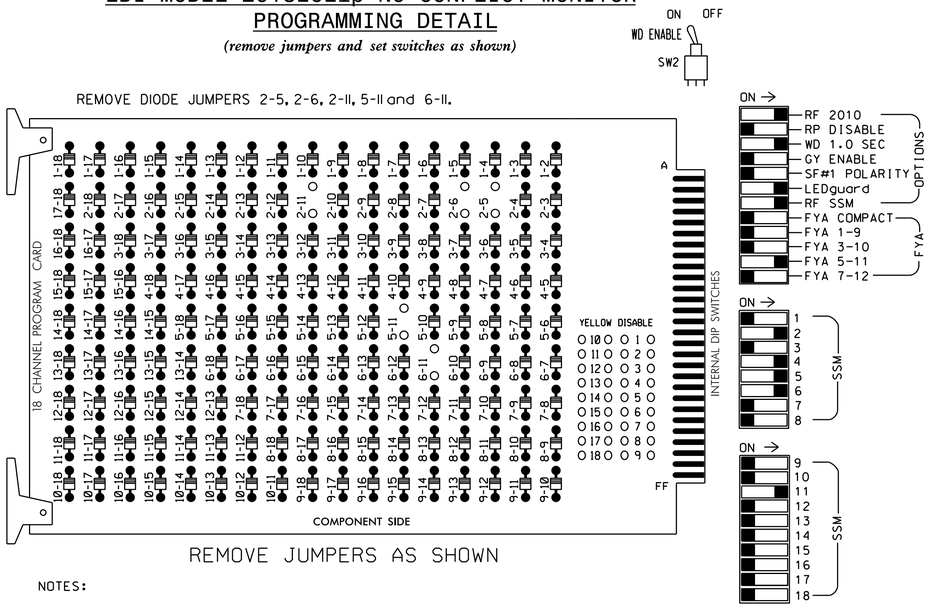
PROJECT REFERENCE NO.	SHEET NO.
U-4715 B	Sig.224.1

EDI MODEL 2018ECLip-NC CONFLICT MONITOR PROGRAMMING DETAIL

(remove jumpers and set switches as shown)



REMOVE JUMPERS AS SHOWN

■ = DENOTES POSITION OF SWITCH

NOTES:

1. Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
2. Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
3. Ensure that Red Enable is active at all times during normal operation.
4. Integrate monitor with Ethernet network in cabinet.

NOTES

1. To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
2. Enable Simultaneous Gap-Out for all phases.
3. Program phases 2 and 6 for Variable Initial and Gap Reduction.
4. Program phases 2 and 6 for Start Up In Green.
5. Program phases 2 and 6 for Yellow Flash.
6. The cabinet and controller are part of the Asheville Signal System.

EQUIPMENT INFORMATION

CONTROLLER.....2070E
CABINET.....332 /W/ AUX
SOFTWARE.....ECONOLITE OASIS
CABINET MOUNT.....BASE
OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE
LOAD SWITCHES USED.....S2,S5,S7,S8,AUX S4
PHASES USED.....2,4,5,6
OVERLAP "A".....NOT USED
OVERLAP "B".....NOT USED
OVERLAP "C".....5+6
OVERLAP "D".....NOT USED

SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7		S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5		6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5		6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	NU	21,22	NU	NU	41,42	NU	42	51★	61,62	NU	NU	NU	NU	NU	NU	NU	51★	NU	NU
RED		128			101		✳		134										
YELLOW		129			102				135										
GREEN		130			103				136										
RED ARROW																	A114		
YELLOW ARROW							132										A115		
FLASHING YELLOW ARROW																	A116		
GREEN ARROW							133	133											

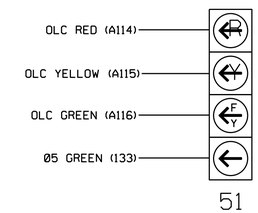
NU = Not Used

✳ Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

4 SECTION FYA PPLT SIGNAL WIRING DETAIL

(wire signal head as shown)



NOTE

1. The sequence display for this signal requires special logic programming. See sheet 2 of 2 for programming instructions.

INPUT FILE POSITION LAYOUT

(front view)

		1	2	3	4	5	6	7	8	9	10	11	12	13	14
FILE "I"	U	SLOT	Ø2	SLOT	SLOT	SLOT	Ø4	SLOT	SLOT	SYS. DET. S1	SLOT	SLOT	SLOT	SLOT	FS
			2A				4A								DC ISOLATOR
	L	EMPTY	Ø2	EMPTY	EMPTY	EMPTY	NOT USED	EMPTY	EMPTY	SYS. DET. S2	EMPTY	EMPTY	EMPTY	EMPTY	ST
			2B												DC ISOLATOR
FILE "J"	U	SLOT	Ø5	Ø6	SLOT	SLOT	SLOT	SLOT	SLOT	SYS. DET. S3	SLOT	SLOT	SLOT	SLOT	SLOT
			5A	6A											
	L	EMPTY	Ø5	Ø6	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY	SYS. DET. S4	EMPTY	EMPTY	EMPTY	EMPTY	EMPTY
			5B	6B											

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE
ST = STOP TIME

INPUT FILE CONNECTION & PROGRAMMING CHART

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			3
5A	TB3-5,6	J2U	40	2	6	5	Y	Y			10
5B	TB3-7,8	J2L	44	6	16	5	Y	Y			15
6A	TB3-9,10	J3U	64	26	36	6	Y	Y			
6B	TB3-11,12	J3L	77	39	46	6	Y	Y			
✳ S1	TB6-9,10	I9U	60	22	11	SYS					
✳ S2	TB6-11,12	I9L	62	24	13	SYS					
✳ S3	TB7-9,10	J9U	59	21	15	SYS					
✳ S4	TB7-11,12	J9L	61	23	17	SYS					

✳ System Detector only. Remove the vehicle phase assigned to this detector in the default programming.

INPUT FILE POSITION LEGEND: J2L

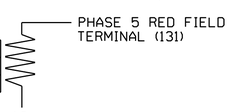
FILE J
SLOT 2
LOWER

LOAD RESISTOR INSTALLATION DETAIL

(install resistor as shown below)

ACCEPTABLE VALUES

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



NOTE: The purpose of this resistor is to load the channel red monitor input in order for the Signal Sequence Monitor to use the full signal sequence monitoring capability on channels that do not use the red display in the field.

THIS ELECTRICAL DETAIL IS FOR
THE SIGNAL DESIGN: 14-0742
DESIGNED: April 2016
SEALED: 8/9/2016
REVISED:

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:

Prepared in the Offices of:

Transportation Mobility and Safety Division
Signal Management Section

750 N.Greenfield Pkwy,Garner,NC 27529

US 25 (Hendersonville Road)
at
SR 1345 (Butler Bridge Road)

Division 14 | Henderson County | Hendersonville

PLAN DATE: June 2016	REVIEWED BY: BAS		
PREPARED BY: C. Strickland	REVIEWED BY:		
REVISIONS		INIT.	DATE

SEAL
NORTH CAROLINA PROFESSIONAL SEAL 030530 ENGINEER ZACHARY M. LITTLE

DocuSigned by: Zachary M. Little
0C21EFD94F5341F...
8/25/2016
DATE

SIG. INVENTORY NO. 14-0742

22-AUG-2016 07:17
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