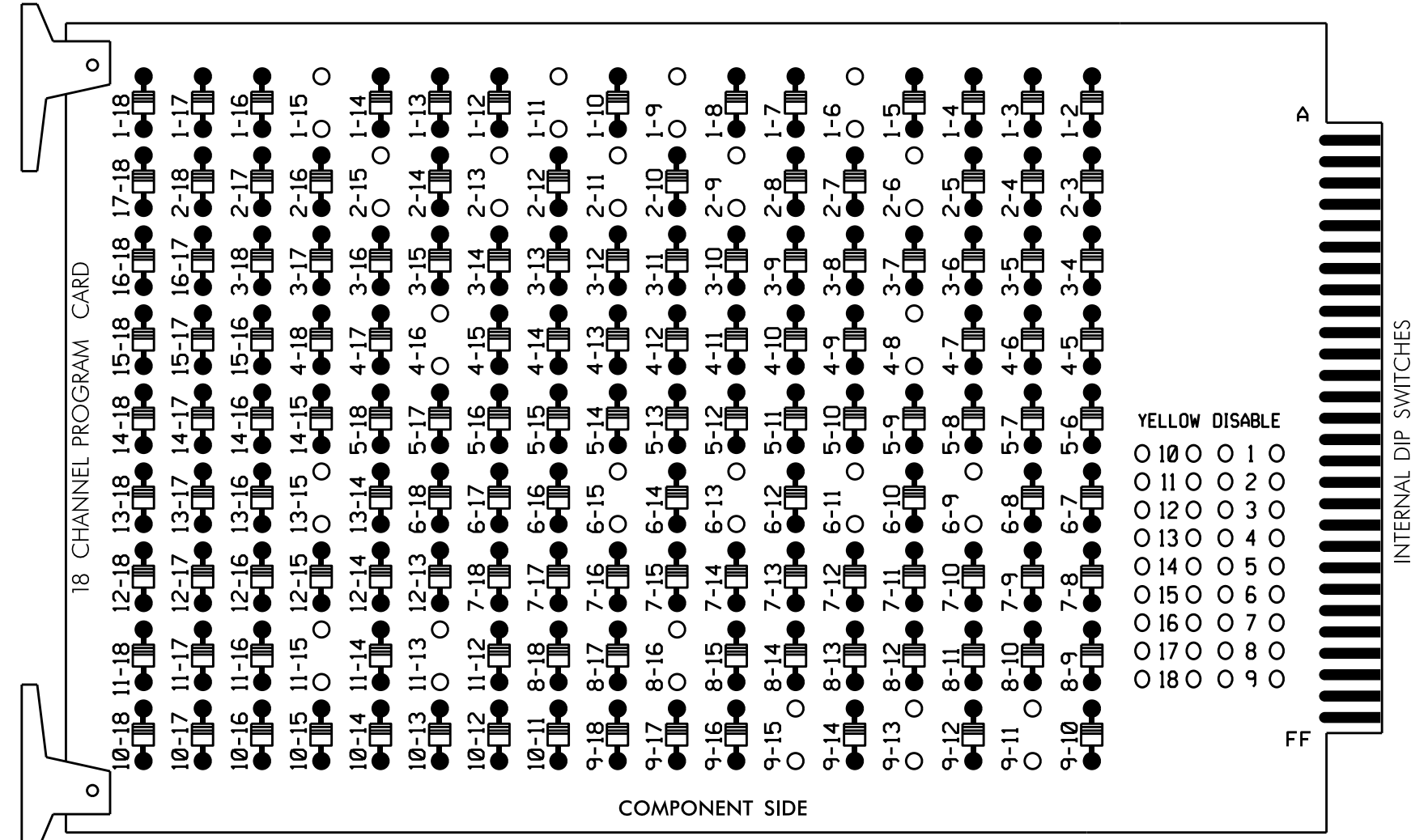


# EDI MODEL 2018ECLIP-NC CONFLICT MONITOR

## PROGRAMMING DETAIL

(remove jumpers and set switches as shown)

REMOVE DIODE JUMPERS 1-6, 1-9, 1-11, 1-15, 2-6, 2-9, 2-11, 2-13, 2-15, 4-8, 4-16, 6-9, 6-11, 6-13, 6-15, 8-16, 9-11, 9-13, 9-15, 11-13, 11-15, and 13-15.



REMOVE JUMPERS AS SHOWN

### NOTES:

- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
- Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
- Ensure that Red Enable is active at all times during normal operation.
- Integrate monitor with Ethernet network in cabinet.

■ = DENOTES POSITION OF SWITCH

## NOTES

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Program phases 4 and 8 for Dual Entry.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Variable Initial and Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2, 6 and 8 for 'STARTUP PED CALL'.
- Program phases 2 and 6 for Yellow Flash, and overlap 1 as Wag Overlaps.
- The cabinet and controller are part of the Asheville Signal System.

## EQUIPMENT INFORMATION

CONTROLLER.....2070E  
 CABINET.....332 W/ AUX  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....BASE  
 OUTPUT FILE POSITIONS...18 WITH AUX. OUTPUT FILE  
 LOAD SWITCHES USED.....S1,S2,S3,S5,S8,S9,S11,S12,  
 AUX S1,AUX S4  
 PHASES USED.....1,2,2PED,4,6,6PED,8,8PED  
 OVERLAP "A".....1+2  
 OVERLAP "B".....NOT USED  
 OVERLAP "C".....6  
 OVERLAP "D".....NOT USED

## SIGNAL HEAD HOOK-UP CHART

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12	AUX S1	AUX S2	AUX S3	AUX S4	AUX S5	AUX S6
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16	9	10	17	11	12	18
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED	OLA	OLB	SPARE	OLC	OLD	SPARE
SIGNAL HEAD NO.	11	22,23	P21, P22	NU	41,42	NU	NU	61,62	P61, P62	NU	81,82	P81, P82	11	NU	NU	21	NU	NU
RED		128			101			134			107							
YELLOW	*	129			102			135			108							
GREEN		130			103			136			109							
RED ARROW													A121				A114	
YELLOW ARROW													A122				A115	
FLASHING YELLOW ARROW													A123				A116	
GREEN ARROW	127																	
Hand icon									113				119					110
Person icon													121					112

NU = Not Used

\* Denotes install load resistor. See load resistor installation detail this sheet.

★ See pictorial of head wiring in detail below.

## INPUT FILE POSITION LAYOUT

(front view)

FILE	1	2	3	4	5	6	7	8	9	10	11	12	13	14
U	∅ 1	∅ 2	∅ 2	S	S	∅ 4	S	S	SYS. DET. S1	S	S	∅ 2 PED	∅ 6 PED	FS
L	NOT USED	∅ 2	NOT USED	Y	Y	NOT USED	Y	Y	SYS. DET. S2	Y	Y	DC ISOLATOR	DC ISOLATOR	DC ISOLATOR
U	S	∅ 6	S	S	S	∅ 8	S	S	SYS. DET. S3	S	S	S	S	S
L	Y	∅ 6	Y	Y	Y	∅ 8	Y	Y	SYS. DET. S4	Y	Y	Y	Y	Y

EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE  
 ST = STOP TIME

⊗ Wired Input - Do not populate slot with detector card

## INPUT FILE CONNECTION & PROGRAMMING CHART

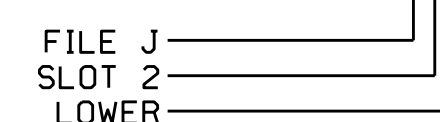
LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB2-1,2	I1U	56	18	1	1	Y	Y			15
		J4U	48	10	26	6	Y	Y	Y		3
2A	TB2-5,6	I2U	39	1	2	2	Y	Y			
2B	TB2-7,8	I2L	43	5	12	2	Y	Y			
2C	TB2-9,10	I3U	63	25	32	2	Y	Y	Y		3
4A	TB4-9,10	I6U	41	3	4	4	Y	Y			10
6A	TB3-5,6	J2U	40	2	6	6	Y	Y			
6B	TB3-7,8	J2L	44	6	16	6	Y	Y			
8A	TB5-9,10	J6U	42	4	8	8	Y	Y			3
8B	TB5-11,12	J6L	46	8	18	8	Y	Y			10
* S1	TB6-9,10	I9U	60	22	11	SYS					
* S2	TB6-11,12	I9L	62	24	13	SYS					
* S3	TB7-9,10	J9U	59	21	15	SYS					
* S4	TB7-11,12	J9L	61	23	17	SYS					
PED PUSH BUTTONS											
P21,P22	TB8-4,6	I12U	67	29	PED 2	2 PED					
P61,P62	TB8-7,9	I13U	68	30	PED 6	6 PED					
P81,P82	TB8-8,9	I13L	70	32	PED 8	8 PED					

NOTE:  
 INSTALL DC ISOLATORS  
 IN INPUT FILE SLOTS  
 112 AND 113.

<sup>1</sup>Add jumper from I1-W to J4-W, on rear of input file.

\* System detector only. Remove the vehicle phase assigned to this detector in the default programming.

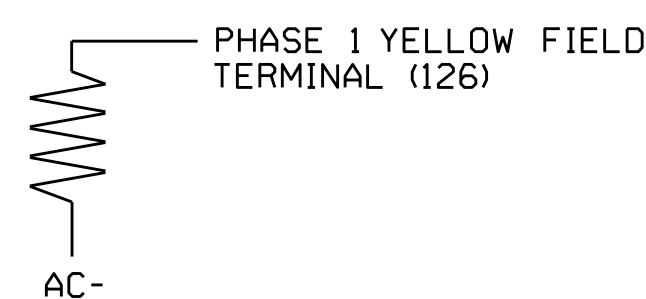
### INPUT FILE POSITION LEGEND: J2L



## LOAD RESISTOR INSTALLATION DETAIL

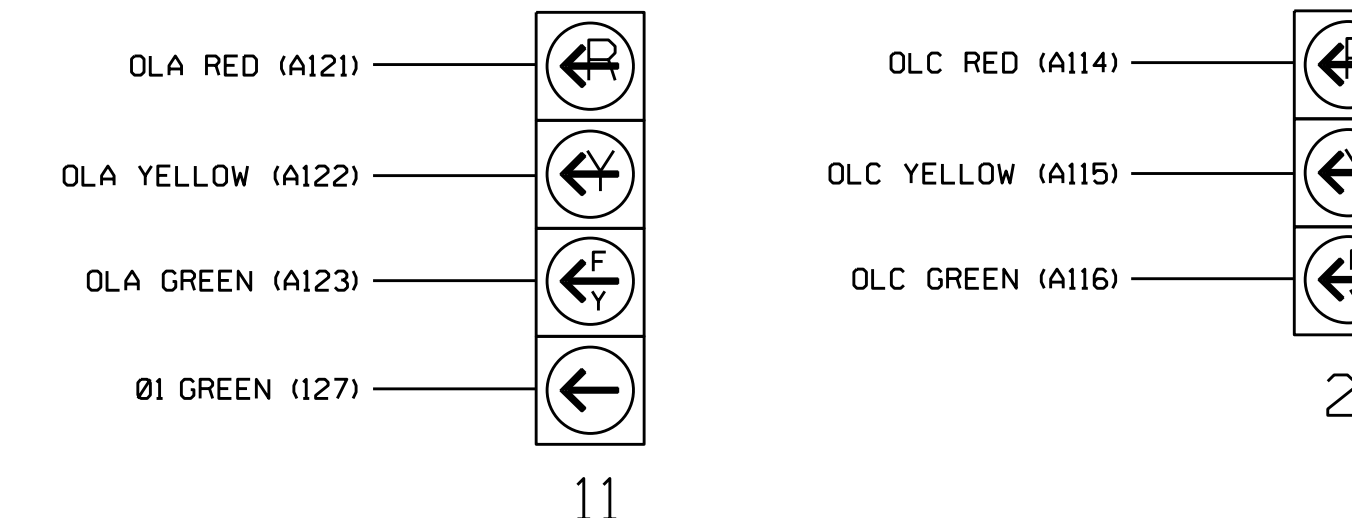
(install resistor as shown below)

VALUE (ohms)	WATTAGE
1.5K - 1.9K	25W (min)
2.0K - 3.0K	10W (min)



## FYA SIGNAL WIRING DETAIL

(wire signal heads as shown)



### NOTE

The sequence display for signal head 11 requires special logic programming. See sheet 2 for programming instructions.

THIS ELECTRICAL DETAIL IS FOR  
 THE SIGNAL DESIGN: 14-0585  
 DESIGNED: May 2016  
 SEALED: 8/10/2016  
 REVISED: N/A

Electrical Detail - Sheet 1 of 2

DOCUMENT NOT CONSIDERED FINAL  
 UNLESS ALL SIGNATURES COMPLETED

ELECTRICAL AND PROGRAMMING DETAILS FOR:  Prepared In the Offices of:  750 N. Greenfield Pkwy, Garner, NC 27529	US 25 (Hendersonville Road) at SR 1545 (Cane Creek Road)/ Library Road		SEAL  Keith M. Minns 9/13/2016	
	Division 14 Henderson County Fletcher	REVISIONS INIT. DATE		
	PLAN DATE: June 2016 PREPARED BY: S. Armstrong	REVIEWED BY: BAS REVIEWED BY:		DATE