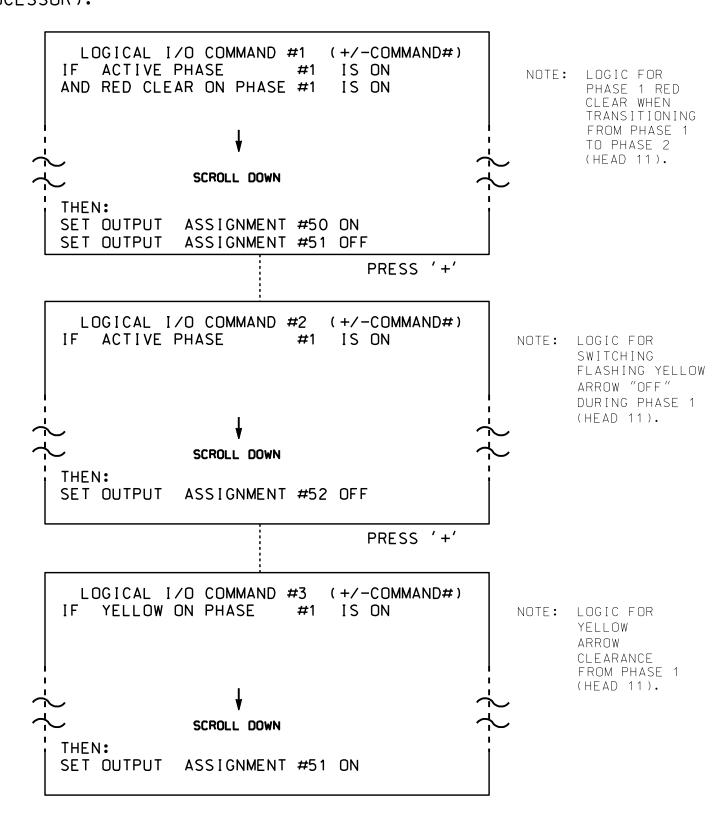
LOGICAL I/O PROCESSOR PROGRAMMING DETAIL

TO PRODUCE SPECIAL FYA-PPLT SIGNAL SEQUENCE

(program controller as shown below)

- 1. FROM MAIN MENU PRESS '2' (PHASE CONTROL), THEN '1' (PHASE CONTROL FUNCTIONS). SCROLL TO THE BOTTOM OF THE MENU AND ENABLE ACT LOGIC COMMANDS 1, 2, AND 3.
- 2. FROM MAIN MENU PRESS '6' (OUTPUTS), THEN '3' (LOGICAL I/O PROCESSOR).



LOGIC I/O PROCESSOR PROGRAMMING COMPLETE

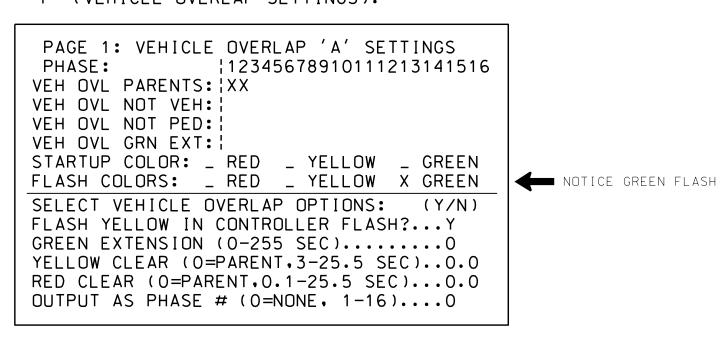
OUTPUT REFERENCE SCHEDULE

OUTPUT 50 = Overlap A Red OUTPUT 51 = Overlap A Yellow OUTPUT 52 = Overlap A Green

OVERLAP PROGRAMMING DETAIL

(program controller as shown below)

FROM MAIN MENU PRESS '8' (OVERLAPS). THEN '1' (VEHICLE OVERLAP SETTINGS).



OVERLAP PROGRAMMING COMPLETE

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-1119 DESIGNED: April 2016 SEALED: 8/9/2016 REVISED: N/A

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED Electrical Detail - Sheet 2 of 2 US 25A (Sweeten Creek Road) ELECTRICAL AND PROGRAMMING DETAILS FOR: Prepared in the Offices of: SR 3079 (Roberts Road) ivision 13 Buncombe County Asheville PLAN DATE: June 2016 REVIEWED BY: BAS PREPARED BY: S. Armstrong REVIEWED BY: REVISIONS INIT. DATE 750 N.Greenfield Pkwy, Garner, NC 27529

SIG. INVENTORY NO. 13-1119