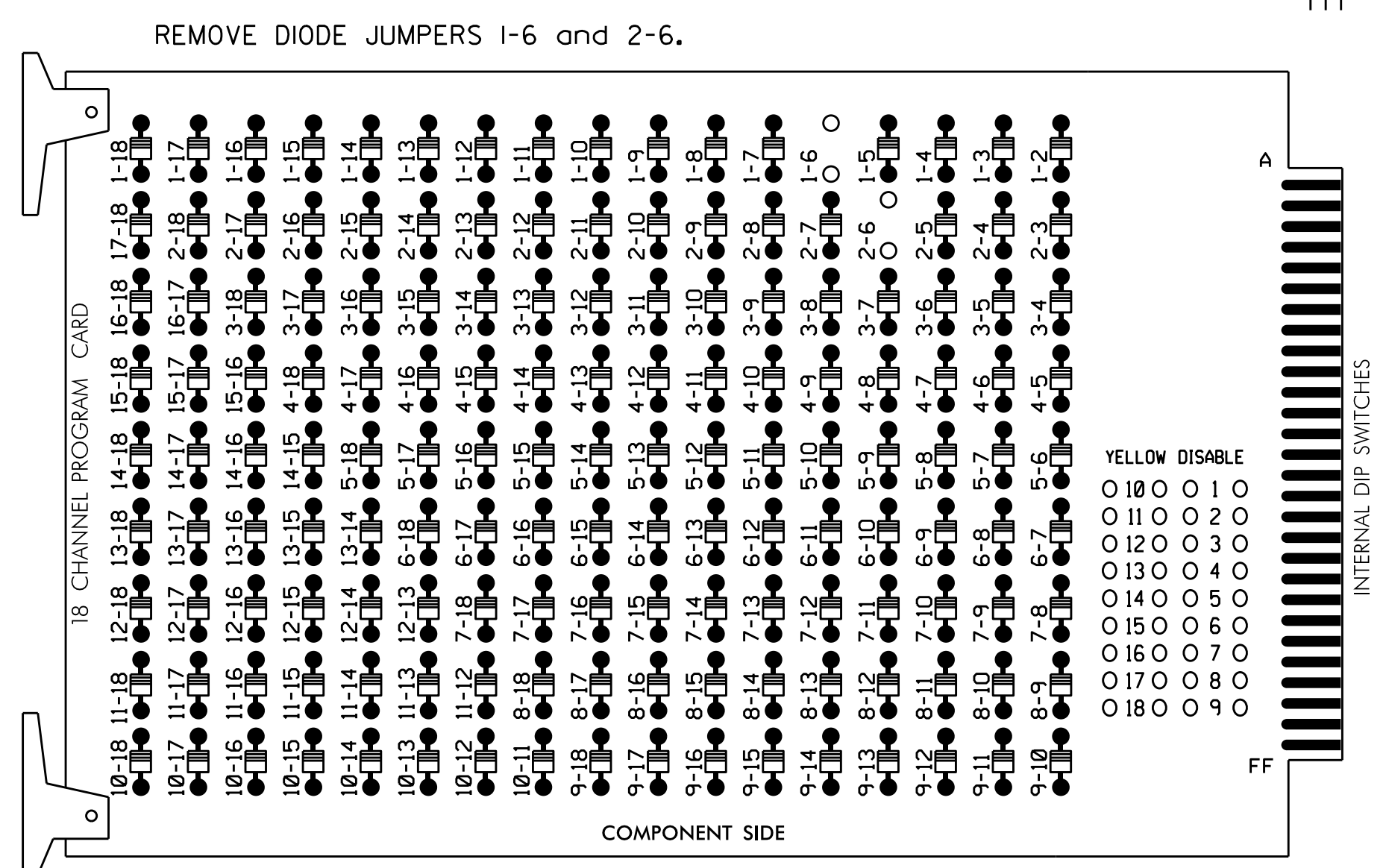


**EDI MODEL 2018ECLIP-NC CONFLICT MONITOR PROGRAMMING DETAIL**

(remove jumpers and set switches as shown)



- NOTES:
- Card is provided with all diode jumpers in place. Removal of any jumper allows its channels to run concurrently.
  - Ensure jumpers SEL2-SEL5 and SEL9 are present on the monitor board.
  - Ensure that Red Enable is active at all times during normal operation.
  - Integrate monitor with Ethernet network in cabinet.

**NOTES**

- To prevent "flash-conflict" problems, insert red flash program blocks for all unused vehicle load switches in the output file. The installer shall verify that signal heads flash in accordance with the Signal Plans.
- Enable Simultaneous Gap-Out for all phases.
- Program phases 2 and 6 for Gap Reduction.
- Program phases 2 and 6 for Start Up In Green.
- Program phases 2 and 6 for Yellow Flash.
- The cabinet and controller are part of the Asheville Signal System.

**EQUIPMENT INFORMATION**

CONTROLLER.....2070E  
 CABINET.....336  
 SOFTWARE.....ECONOLITE OASIS  
 CABINET MOUNT.....POLE  
 OUTPUT FILE POSITIONS...12  
 LOAD SWITCHES USED.....S1,S2,S4,S5,S8  
 PHASES USED.....1,2,3,4,6  
 OVERLAPS.....NONE

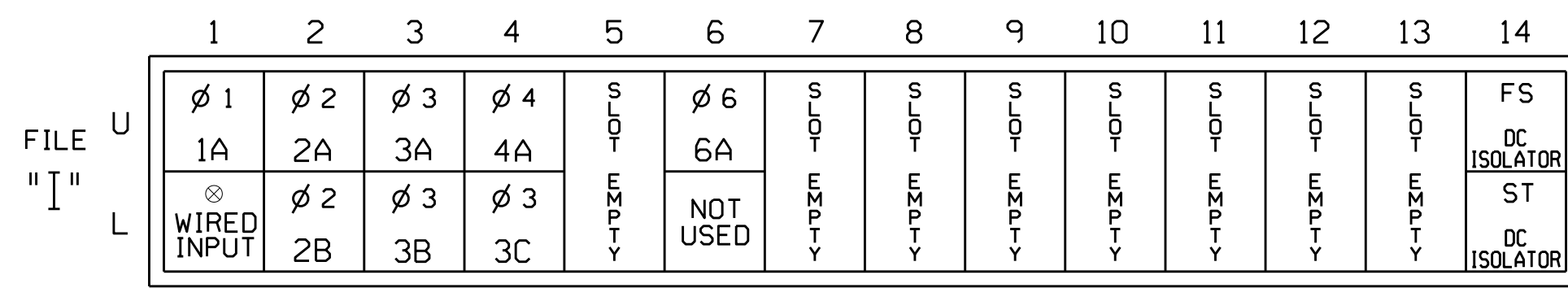
**SIGNAL HEAD HOOK-UP CHART**

LOAD SWITCH NO.	S1	S2	S3	S4	S5	S6	S7	S8	S9	S10	S11	S12
CMU CHANNEL NO.	1	2	13	3	4	14	5	6	15	7	8	16
PHASE	1	2	2 PED	3	4	4 PED	5	6	6 PED	7	8	8 PED
SIGNAL HEAD NO.	61	21,22	NU	31,32 33,34	41	42	NU	NU	61,62	NU	NU	NU
RED	*	128		116	101	101			134			
YELLOW		129		117	102	102			135			
GREEN		130		118	103	103			136			
RED ARROW												
YELLOW ARROW	126											
GREEN ARROW	127				103							

NU = Not Used  
 \* Denotes install load resistor. See load resistor installation detail this sheet.

**INPUT FILE POSITION LAYOUT**

(front view)



EX.: 1A, 2A, ETC. = LOOP NO.'S

FS = FLASH SENSE

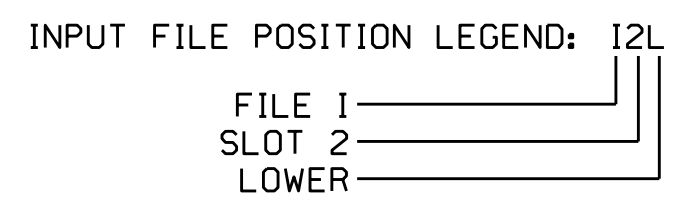
ST = STOP TIME

Disable Channel 2

**INPUT FILE CONNECTION & PROGRAMMING CHART**

LOOP NO.	LOOP TERMINAL	INPUT FILE POS.	PIN NO.	INPUT ASSIGNMENT NO.	DETECTOR NO.	NEMA PHASE	CALL	EXTEND	FULL TIME DELAY	STRETCH TIME	DELAY TIME
1A <sup>1</sup>	TB21-1,2	I1U	56	18	1	1	Y	Y			15
		I1L	47	9	22	6	Y	Y	Y	2.0	5
2A	TB21-3,4	I2U	39	1	2	2	Y	Y			
2B	TB23-3,4	I2L	43	5	12	2	Y	Y	Y	2.0	5
3A	TB21-5,6	I3U	58	20	3	3	Y	Y			3
3B	TB23-5,6	I3L	49	11	24	3	Y	Y			10
3C	TB23-7,8	I4L	45	7	14	3	Y	Y			15
4A	TB21-7,8	I4U	41	3	4	4	Y	Y			
6A	TB21-11,12	I6U	40	2	6	6	Y	Y			

<sup>1</sup>Add jumper from I1-F to I1-W, on rear of input file.



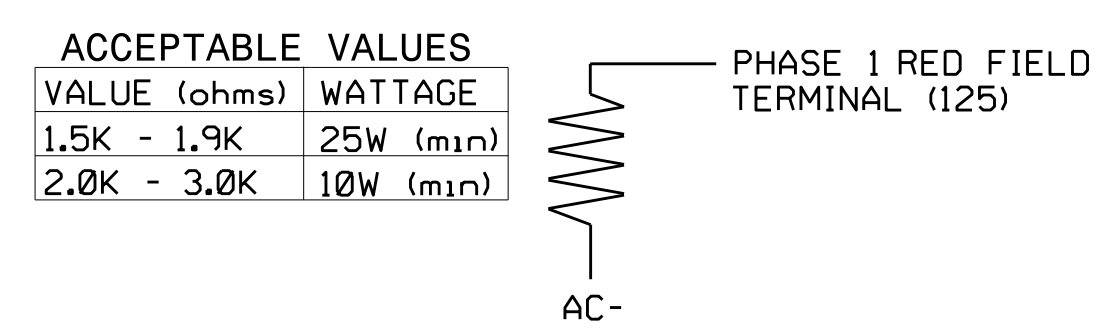
**BACKUP PROTECTION NOTE**

(program controller as shown below)

From Main Menu press '2' (Phase Control), then '1' (Phase Control Functions). Program phase 6 for 'Backup Protect'. Make sure the Red Revert times shown on the Signal Design Plans are programmed in the 'Phase Timing' menu.

THIS ELECTRICAL DETAIL IS FOR THE SIGNAL DESIGN: 13-0994  
 DESIGNED: March 2016  
 SEALED: 8/17/2016  
 REVISED:

**LOAD RESISTOR INSTALLATION DETAIL**



**Electrical Detail**

DOCUMENT NOT CONSIDERED FINAL UNLESS ALL SIGNATURES COMPLETED

Prepared In the Offices of:  
  
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NC 191 (Brevard Road) at SR 3486 (Avery's Creek Road) and SR 3486 (Glen Bridge Road) and SR 3573 (Southwick Road)

Division 13 Buncombe County Avery Creek

PLAN DATE: July 2016 REVIEWED BY: K. Mims

PREPARED BY: Z.M. Little REVIEWED BY:

REVISIONS INIT. DATE

DocuSigned by: Keith M. Mims 8/18/2016

SIG. INVENTORY NO. 13-0994

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